

## isc Silicon NPN Power Transistor

**ISCM1897**

### DESCRIPTION

- Fast turn-off times
- Operating temperature range -65~200°C
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

### APPLICATIONS

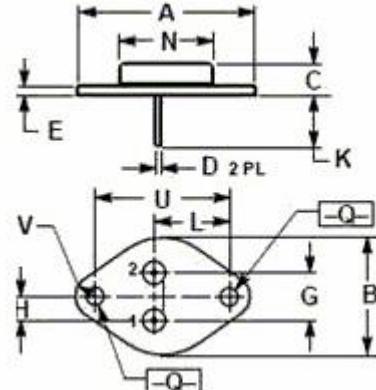
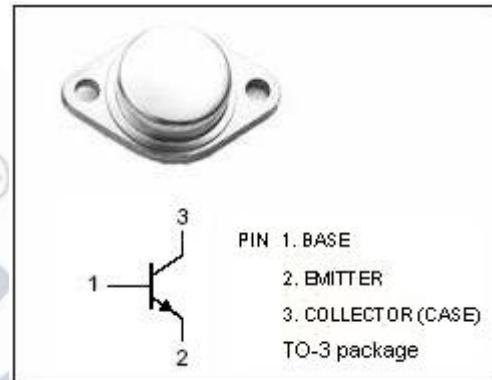
- Switching regulators
- Inverters
- Solenoid and relay drivers
- Motor controls
- Deflection circuits

### ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ\text{C}$ )

SYMBOL	PARAMETER	VALUE	UNIT
$V_{CBO}$	Collector- Base Voltage	1500	V
$V_{CEO(\text{SUS})}$	Collector-Emitter Voltage	800	V
$V_{EBO}$	Emitter-Base Voltage	6	V
$I_c$	Collector Current-Continuous	10	A
$I_{CM}$	Collector Current-Peak	15	A
$I_B$	Base Current-Continuous	8	A
$I_{BM}$	Base Current-Peak	12	A
$P_c$	Collector Power Dissipation@ $T_c=25^\circ\text{C}$	175	W
$T_J$	Junction Temperature	-65~200	°C
$T_{\text{stg}}$	Storage Temperature	-65~200	°C

### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{\text{th,j-c}}$	Thermal Resistance,Junction to Case	1.0	°C/W



DIM	mm	
	MIN	MAX
A	39.00	
B	25.30	26.67
C	7.80	8.50
D	0.90	1.10
E	1.40	1.60
G	10.92	
H	5.46	
K	11.30	13.50
L	16.75	17.05
N	19.40	19.62
Q	4.00	4.20
U	30.00	30.20
V	4.30	4.50

**isc Silicon NPN Power Transistor****ISCM1897****ELECTRICAL CHARACTERISTICS****T<sub>c</sub>=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>CEO(SUS)*</sub>	Collector-Emitter Sustaining Voltage	I <sub>C</sub> =50mA ; I <sub>B</sub> =0	80			V
V <sub>CE(sat)-1</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 5A; I <sub>B</sub> =2A			1.0	V
V <sub>CE(sat)-2</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> =10A; I <sub>B</sub> = 5A			5.0	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> = 5A; I <sub>B</sub> = 2A			1.5	V
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = 6V; I <sub>C</sub> =0			0.1	mA
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = 5A ; V <sub>CE</sub> = 5V	4			
C <sub>OB</sub>	Output Capacitance	I <sub>E</sub> = 0; V <sub>CB</sub> = 10V; f <sub>test</sub> =1.0kHz			450	pF

\*:Pulse test:Pulse width≤300us,duty cycle≤2%