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Bulletin Date: 12/14/2016	Bulletin Effective Date: 12/14/2016
Title: EFR32MG1V, EFR32BG1V & EFR32FG1V Full Crypto and Data Sheet Revision 1.1	
Bulletin Details	
Description: Silicon Labs is pleased to announce that all OPNs in the EFR32MG1 Mighty Gecko ZigBee® & Thread, EFR32BG1 Blue Gecko Bluetooth® Smart and EFR32FG1 Flex Gecko Proprietary Wireless SoC families now have full encryption capabilities. Previously, EFR32MG1V, EFR32BG1V and EFR32FG1V OPNs were listed as "AES Only". After the effective date of this PCN, the Data Sheet for these devices is updated to Revision 1.1 & Errata is updated to Revision 1.5.	
Reason: EFR32MG1V, EFR32BG1V and EFR32FG1V OPNs have been upgraded to support full encryption capabilities.	
Product Identification: OPNs EFR32MG1P232F256GM48-C0 EFR32MG1P232F256GM48-C0R EFR32MG1P232F256GM32-C0 EFR32MG1P232F256GM32-C0R EFR32MG1P132F256GM48-C0 EFR32MG1P132F256GM48-C0R EFR32MG1P132F256GM32-C0 EFR32MG1P132F256GM32-C0R EFR32MG1B232F256GM48-C0 EFR32MG1B232F256GM48-C0R EFR32MG1B232F256GM32-C0 EFR32MG1B232F256GM32-C0R EFR32MG1B132F256GM48-C0 EFR32MG1B132F256GM48-C0R EFR32MG1B132F256GM32-C0 EFR32MG1B132F256GM32-C0R EFR32MG1V132F256GM48-C0 EFR32MG1V132F256GM48-C0R EFR32MG1V132F256GM32-C0 EFR32MG1V132F256GM32-C0R EFR32BG1P332F256GM48-C0 EFR32BG1P332F256GM48-C0R EFR32BG1P332F256GM32-C0 EFR32BG1P332F256GM32-C0R EFR32BG1P232F256GM48-C0 EFR32BG1P232F256GM48-C0R EFR32BG1P232F256GM32-C0 EFR32BG1P232F256GM32-C0R EFR32BG1B232F256GM48-C0 EFR32BG1B232F256GM48-C0R	



EFR32BG1B232F256GM32-C0
EFR32BG1B232F256GM32-C0R
EFR32BG1B232F128GM48-C0
EFR32BG1B232F128GM48-C0R
EFR32BG1B232F128GM32-C0
EFR32BG1B232F128GM32-C0R
EFR32BG1B132F256GM48-C0
EFR32BG1B132F256GM48-C0R
EFR32BG1B132F256GM32-C0
EFR32BG1B132F256GM32-C0R
EFR32BG1B132F128GM48-C0
EFR32BG1B132F128GM48-C0R
EFR32BG1B132F128GM32-C0
EFR32BG1B132F128GM32-C0R
EFR32BG1V132F256GM48-C0
EFR32BG1V132F256GM48-C0R
EFR32BG1V132F256GM32-C0
EFR32BG1V132F256GM32-C0R
EFR32BG1V132F128GM48-C0
EFR32BG1V132F128GM48-C0R
EFR32BG1V132F128GM32-C0
EFR32BG1V132F128GM32-C0R
EFR32FG1P132F256GM48-C0
EFR32FG1P132F256GM48-C0R
EFR32FG1P132F256GM32-C0
EFR32FG1P132F256GM32-C0R
EFR32FG1V132F256GM48-C0
EFR32FG1V132F256GM48-C0R
EFR32FG1P132F128GM48-C0
EFR32FG1P132F128GM48-C0R
EFR32FG1V132F256GM32-C0
EFR32FG1V132F256GM32-C0R
EFR32FG1P132F128GM32-C0
EFR32FG1P132F128GM32-C0R
EFR32FG1P132F64GM48-C0
EFR32FG1P132F64GM48-C0R
EFR32FG1V132F128GM48-C0
EFR32FG1V132F128GM48-C0R
EFR32FG1V132F64GM48-C0
EFR32FG1V132F64GM48-C0R
EFR32FG1V132F32GM48-C0
EFR32FG1V132F32GM48-C0R
EFR32FG1V132F64GM32-C0
EFR32FG1V132F64GM32-C0R
EFR32FG1V132F32GM32-C0
EFR32FG1V132F32GM32-C0R

This change is considered a minor change which does not affect form, fit, function, quality, or reliability. The information is being provided as a customer courtesy.

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Customer Actions Needed:

Please use Revision 1.1 of the data sheet & Revision 1.5 of the Errata for all device specifications.