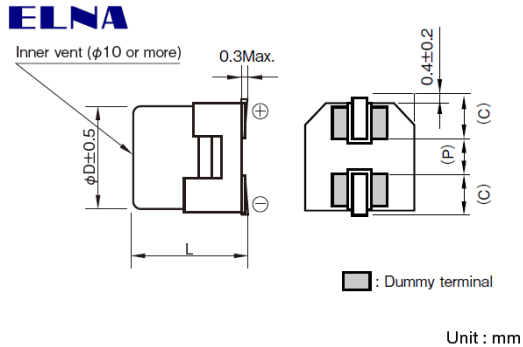


VERTICAL CHIP TYPE ALUMINUM ELECTROLYTIC CAPACITORS[RTD]

RTD-50V101MG10SU-R2



■ Features

- Item Summary
100 μ F \pm 20%, 50V, ϕ 8x10mm, SMD
- Lifecycle Stage
Mass Production
- Standard packaging quantity (minimum)
500

■ Products characteristics table

Capacitance	100 μ F \pm 20 %
Rated Voltage	50 V
tan δ (max)	0.1
Operating Temp. Range	-55 to +105 $^{\circ}$ C
Leakage current (min)	50 μ A at 2 min
Rated Ripple (max)	350 mArms at 100k Hz / 105 $^{\circ}$ C
Impedance ratio ___ [-25 $^{\circ}$ C/+20 $^{\circ}$ C]	2
Impedance ratio ___ [-40 $^{\circ}$ C/+20 $^{\circ}$ C]	3
Impedance ratio ___ [-55 $^{\circ}$ C/+20 $^{\circ}$ C]	3
Impedance (max)	0.32 Ω at 100k Hz / 20 $^{\circ}$ C
Endurance ___ [Test time]	5000 hr at 105 $^{\circ}$ C
Endurance ___ [Capacitance change]	Within \pm 30% of initial value
Endurance ___ [tan δ change]	300% or less of initial specified value
Shape	Vertical Chip for Vibration (SMD)
Type	Low Impedance, high ripple current
RoHS2 Compliance	Yes
REACH Compliance	Yes
Soldering	Reflow
LeadForming/Taping	Taping Embossed

■ External Dimensions

Dimension ϕ D	ϕ 8 \pm 0.5 mm
Dimension L	10 \pm 0.5 mm
Dimension P (F)	Typ 3.1 mm
Dimension C (d)	Typ 3 mm

The data is reference only. Electrical characteristics vary depending on environment or measurement condition.
 TAIYO YUDEN reserves the right to make change to the data at any time without notice.
 Before making final selection, please check product specification.

2017.12.06

Please read this notice before using the ELNA products.



REMINDERS

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