



1200V, 50A, $V_{ce(on)} = 2.5V$ Typical

Ultra Fast NPT - IGBT®

The Ultra Fast NPT - IGBT® family of products is the newest generation of planar IGBTs optimized for outstanding ruggedness and the best trade-off between conduction and switching losses.

TO-247 Max TO-264

All Ratings: T_c = 25°C unless otherwise specified.

Features

- · Low Saturation Voltage
- Low Tail Current
- RoHS Compliant

- Short Circuit Withstand Rated
- High Frequency Switching
- Ultra Low Leakage Current

Unless stated otherwise, Microsemi discrete IGBTs contain a single IGBT die. This device is recommended for applications such as induction heating (IH), motor control, general purpose inverters and uninterruptible power supplies (UPS).



MAXIMUM RATINGS

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Symbol	Parameter	Ratings	Unit
V _{ces}	Collector Emitter Voltage	1200	V
V _{GE}	Gate-Emitter Voltage	±30	ľ
I _{C1}	Continuous Collector Current @ T _C = 25°C	117	
I _{C2}	Continuous Collector Current @ T _C = 110°C	50	Α
I _{CM}	Pulsed Collector Current ①	200	1
SCWT	Short Circuit Withstand Time: V _{CE} = 600V, V _{GE} = 15V, T _C =125°C	10	μs
P _D	Total Power Dissipation @ T _C = 25°C	694	W
T _J ,T _{STG}	Operating and Storage Junction Temperature Range	-55 to 150	
TL	Max. Lead Temp. for Soldering: 0.063" from Case for 10 Sec.	300	°C

STATIC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Min	Тур	Max	Unit
V _{(BR)CES}	Collector-Emitter Breakdown Voltage (V _{GE} = 0V, I _C = 1.0mA)	1200			
V _{GE(TH)}	Gate Threshold Voltage $(V_{CE} = V_{GE}, I_{C} = 2.5 \text{mA}, T_{j} = 25 ^{\circ}\text{C})$	3.5	5.0	6.5	\ /-I4-
V _{CE(ON)}	Collector-Emitter On Voltage (V _{GE} = 15V, I _C = 50A, T _j = 25°C)		2.5	3.2	Volts
	Collector-Emitter On Voltage (V _{GE} = 15V, I _C = 50A, T _j = 125°C)		3.3		
	Collector-Emitter On Voltage (V _{GE} = 15V, I _C = 100A, T _j = 25°C)		3.5		
I _{CES}	Collector Cut-off Current (V _{CE} = 1200V, V _{GE} = 0V, T _j = 25°C) ②		10	1000	μA
	Collector Cut-off Current (V _{CE} = 1200V, V _{GE} = 0V, T _j = 125°C) ②		100		
I _{GES}	Gate-Emitter Leakage Current (V _{GE} = ±20V)			±250	nA

CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
C _{ies}	Input Capacitance	Capacitance		5550		
C _{oes}	Output Capacitance	$V_{GE} = 0V, V_{CE} = 25V$		500		pF
C_{res}	Reverse Transfer Capacitance	f = 1MHz		145		
$V_{\sf GEP}$	Gate to Emitter Plateau Voltage	Cata Charra		7.5		V
Qg3	Total Gate Charge	Gate Charge		330	445	
Q_{ge}	Gate-Emitter Charge	V _{GE} = 15V		52	72	" C
Q_{gc}	Gate- Collector Charge	$V_{CE} = 600V$ $I_{C} = 50A$		156	200	nC
t _{d(on)}	Turn-On Delay Time	Inductive Switching (25°C)		28		
t,	Current Rise Time	V _{CC} = 600V		38		no
t _{d(off)}	Turn-Off Delay Time	V _{GE} = 15V		237		ns
t _f	Current Fall Time	I _C = 50A		45		ĺĺ
E _{on2} 5	Turn-On Switching Energy	$R_{G} = 4.3 \Omega^{(4)}$		2135	3200	1
E _{off}	Turn-Off Switching Energy	T _J = +25°C		1478	2210	μJ
t _{d(on)}	Turn-On Delay Time	Inductive Switching (125°C)		28		
t,	Current Rise Time	V _{CC} = 600V		38		no
$t_{d(off)}$	Turn-Off Delay Time	V _{GE} = 15V		270		ns
t _f	Current Fall Time	I _C = 50A		54		
E _{on2} 5	Turn-On Switching Energy	$R_{G} = 4.3 \Omega^{4}$		3157	4765	1
E _{off}	Turn-Off Switching Energy	T _J = +125°C		1884	2820	μJ

THERMAL AND MECHANICAL CHARACTERISTICS

Symbol	Characteristic		Min	Тур	Max	Unit
R _{eJC}	Junction to Case Thermal Resistance (IGBT)				.18	°C/W
$R_{\theta JA}$	Junction to Ambient Thermal Resistance				40	C/VV
	Package Weight	B2		.22		oz
W _T				6		g
		L		.36		oz
				10		g

- 1 Repetitive Rating: Pulse width and case temperature limited by maximum junction temperature.
- 2 Pulse test: Pulse Width < $380\mu s$, duty cycle < 2%.
- 3 See Mil-Std-750 Method 3471.
- 4 $R_{_{\mathrm{S}}}$ is external gate resistance, not including internal gate resistance or gate driver impedance. (MIC4452)
- 5 E_{onz} is the clamped inductive turn on energy that includes a commutating diode reverse recovery current in the IGBT turn on energy loss. A combi device is used for the clamping diode.
- 6 E_{off} is the clamped inductive turn-off energy measured in accordance with JEDEC standard JESD24-1.

Microsemi reserves the right to change, without notice, the specifications and information contained herein.

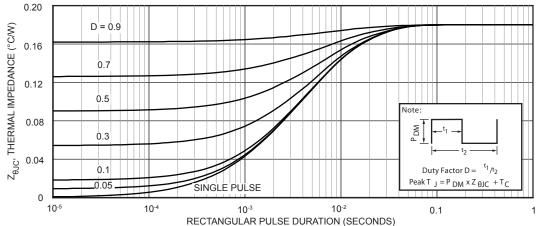
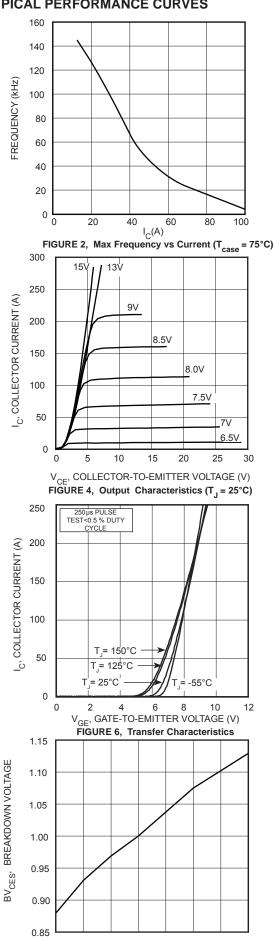


Figure 1, Maximum Effective Transient Thermal Impedance, Junction-To-Case vs Pulse Duration

052-6405



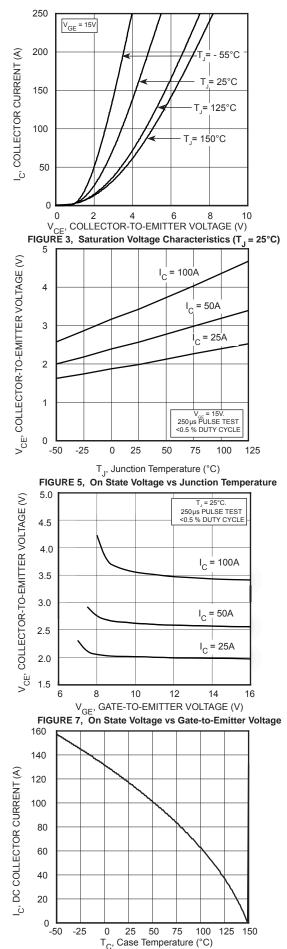


FIGURE 9, DC Collector Current vs Case Temperature

-25

T_{.I}, JUNCTION TEMPERATURE

FIGURE 8, Breakdown Voltage vs Junction Temperature

100

100n

V_{CE}, COLLECTOR-TO-EMITTER VOLTAGE

FIGURE 17, Minimum Switching Safe Operating Area

0.1



25

T_{.I}, JUNCTION TEMPERATURE (°C)

FIGURE 16, Switching Energy vs Junction Temperature

100

125

1000

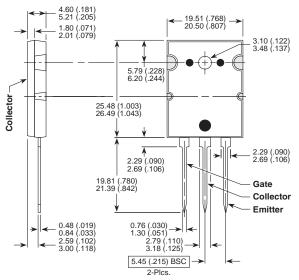
T-MAX™ (B2) Package Outline

4.69 (.185) 5.31 (.209) 1.49 (.059) 2.49 (.098) 20.80 (.819) 21.46 (.845) 4.50 (.177) Max. 2.87 (.113) 3.12 (.123) 1.65 (.065) 2.13 (.084) Gate Collector Emitter

These dimensions are equal to the TO-247 without the mounting hole.

Dimensions in Millimeters and (Inches)

TO-264 (L) Package Outline



Dimensions in Millimeters and (Inches)

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