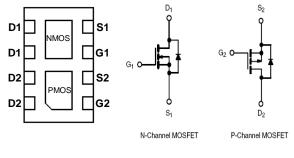


Main Product Characteristics:

	NMOS	PMOS		
V _{DSS}	30V	-30V		
R _{DS} (on)	37mohm(typ.)	68mohm(typ.)		
I _D	5A	-4.5A		



DFN2X3-8L

Schematic diagram

Features and Benefits:

- Advanced trench MOSFET process technology
- Special designed for buck-boost circuit, DSC, portable devices and general purpose applications
- Ultra low on-resistance with low gate charge
- 150°C operating temperature



Description:

It utilizes the latest trench processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in buck-boost circuit, DSC, portable devices and a wide variety of others applications

Absolute max Rating:

Complete	Baramatar	Ma	Units		
Symbol	Parameter	N-channel	P-channel	UiillS	
I _D @ TC = 25°C	25°C Continuous Drain Current, V _{GS} @ 4.5V①		-4.5		
I _D @ TC = 100°C	Continuous Drain Current, V _{GS} @ 4.5V①	4.2	-3.4	Α	
I _{DM} Pulsed Drain Current②		18.8	-12.5		
P _D @TC = 25°C	Power Dissipation③	2.1	1.8	W	
V _{DS}	Drain-Source Voltage	30	-30	V	
V _{GS}	Gate-to-Source Voltage	± 12	± 12	V	
T _J T _{STG}	Operating Junction and Storage Temperature	-55 to + 150	-55 to + 150	°C	
	Range	-55 10 + 150	-55 to + 150		

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Thermal Resistance

Symbol	Characterizes		Max.		Units
Symbol			N-channel	P-channel	Units
R _{eJA}	Junction-to-ambient (t ≤ 10s) ④	_	60	95	°C/W
	Junction-to-Ambient (PCB mounted, steady-state) ④	_	40	40	°C/W

Electrical Characterizes $@T_A=25^{\circ}C$ unless otherwise specified

Symbol	Parameter		Min.	Тур.	Max.	Units	Conditions
	Drain-to-Source breakdown voltage	N-channel	30	_	_	V	V _{GS} = 0V, ID = 250μA
\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \			27.5	_	_		T _J = 125°C
V _{(BR)DSS}		D	-30	_	_		$V_{GS} = 0V, ID = -250\mu A$
		P-channel	-27.5	_	_		T _J = 125°C
	Ctatia	N-channel	_	37	55		V_{GS} =4.5 V , I_{D} = 4.8 A
	Static	P-channel	_	68	85	m0	V_{GS} =-4.5 V , I_{D} = -2.3 A
R _{DS(on)}	Drain-to-Source	N-channel	_	50	90	mΩ	$V_{GS}=3.5V, I_D=3.8A$
	on-resistance	P-channel	_	84	115		V_{GS} =-3.5V, I_{D} = -1.8A
	Gate threshold voltage	N-channel	0.7	1.48	2	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		P-channel	0.7	1.12	2		T _J = 125°C
$V_{GS(th)}$		N-channel	-0.7	-1.49	-2		$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
		P-channel	-0.7	-1.26	-2		T _J = 125°C
	Drain-to-Source	N-channel	_	_	1		$V_{DS} = 30V, V_{GS} = 0V$
I _{DSS}	leakage current	P-channel	_	_	-1	μA	$V_{DS} = -30V, V_{GS} = 0V$
		N-channel	_	_	100		V _{GS} =12V
	Gate-to-Source	N-channel	_	_	-100		V _{GS} = -12V
I _{GSS}	forward leakage	P-channel	_	_	100	nA	V _{GS} =12V
		P-channel	_	_	-100		V _{GS} = -12V

Source-Drain Ratings and Characteristics

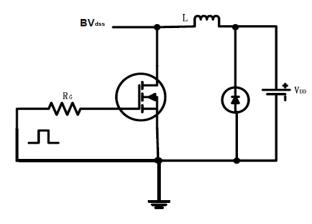
Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
Is	Continuous Source Current (Body Diode)	_	_	5	А	MOSFET symb showing the integral reverse p-n junction diode.
13		_	_	-4.5		
I _{SM}	Pulsed Source Current (Body Diode)	_	_	18.8	А	
		_	_	-12.5		
V_{SD}	Diode Forward Voltage	_	0.82	1.2	٧	I _S =2.4A, V _{GS} =0V
		_	-0.84	-1.2		I _S =-1.5A, V _{GS} =0V

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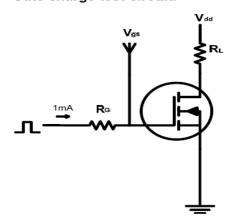


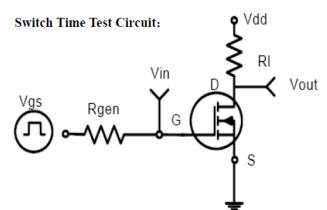
Test circuits and Waveforms

EAS test circuits:

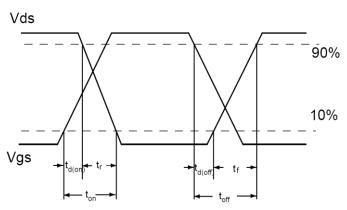


Gate charge test circuit:





Switch Waveforms:



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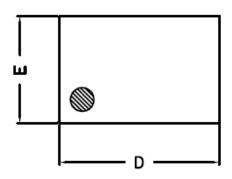
Notes:

- ①The maximum current rating is limited by bond-wires.
- ②Repetitive rating; pulse width limited by max. junction temperature.
- ③The power dissipation PD is based on max. junction temperature, using junction-to- ambient thermal resistance.
- 4 The value of $R_{\theta JA}$ is measured with the device mounted on 1in 2 FR-4 board with 2oz. Copper, in a still air environment with TA =25°C

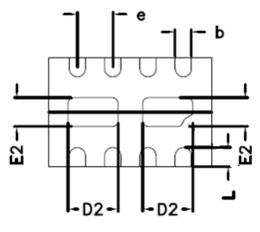


Mechanical Data:

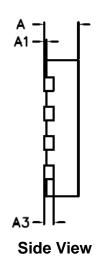
DFN2X3-8L



Top View



Bottom View



COMMON DIMENSIONS(MM)						
PKG.	W:VERY VERY THIN					
REF.	MIN.	MAX.				
Α	0.70	0.70 0.75				
A 1	0.00 — 0.05					
А3	0.2 REF.					
D	2.95 3.00 3.05					
E	1.95	2.05				
b	0.25	0.25 0.30 0.35				
L	0.25 0.35 0.45					
D2	0.77	0.77 0.92 1.02				
E2	0.38 0.53 0.63					
е	0.65 BCS.					

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NOTES:

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- 5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.





Ordering and Marking Information

Device Marking: 3056C

Package (Available)
DFN2X3-8L
Operating Temperature Range
C: -55 to 150 °C

Devices per Unit

Package	Units/	Tubes/	Units/	Inner Boxes/	Units/
Type	Tube	Inner Box	Inner Box	Carton Box	Carton Box
DFN2*3-8L	3000pcs	10pcs	30000pcs	4pcs	120000pcs

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