PS9151

R08DS0108EJ0300
Rev.3.00
Apr 09, 2013

HIGH NOISE REDUCTION, 15 Mbps CMOS OUTPUT TYPE, 5-PIN SOP (SO-5) PHOTOCOUPLER

DESCRIPTION

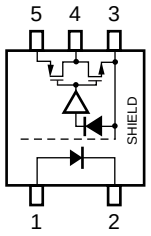
The PS9151 is an optically coupled isolator containing a GaAlAs LED on the input side and a CMOS output IC on the output side.

This photocoupler is high common mode transient immunity (CMR), a high-speed CMOS output type device designed for high-speed logic interface circuits.

FEATURES

- High-speed response (15 Mbps)
- Operable at high temperature (–40 to +100°C)
- High common mode transient immunity (CM_H, CM_L = ±20 kV/μs TYP.)
- High isolation voltage (BV = 3 750 Vr.m.s.)
- Pulse width distortion ($|t_{PHL} - t_{PLH}|$ = 3 ns TYP.)
- Ordering number of tape product : PS9151-F3: 2 500 pcs/reel
- Pb-Free product
- <R> Safety standards
  - UL approved: No. E72422
  - DIN EN 60747-5-5 (VDE 0884-5) approved (Option)

PIN CONNECTION
(Top View)



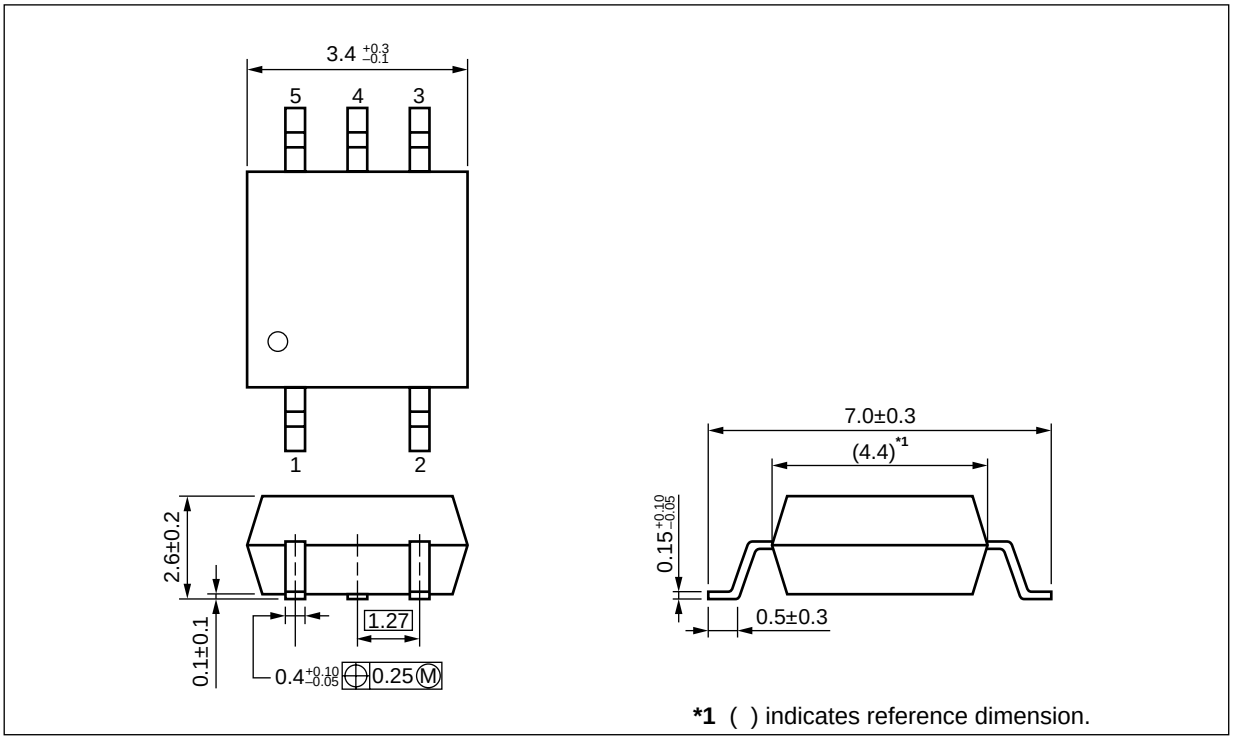
1. Anode
2. Cathode
3. GND
4. V_O
5. V_{DD}

APPLICATIONS

- FA Network
- Measurement equipment
- PDP

The mark <R> shows major revised points.
The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

<R> PACKAGE DIMENSIONS (UNIT: mm)

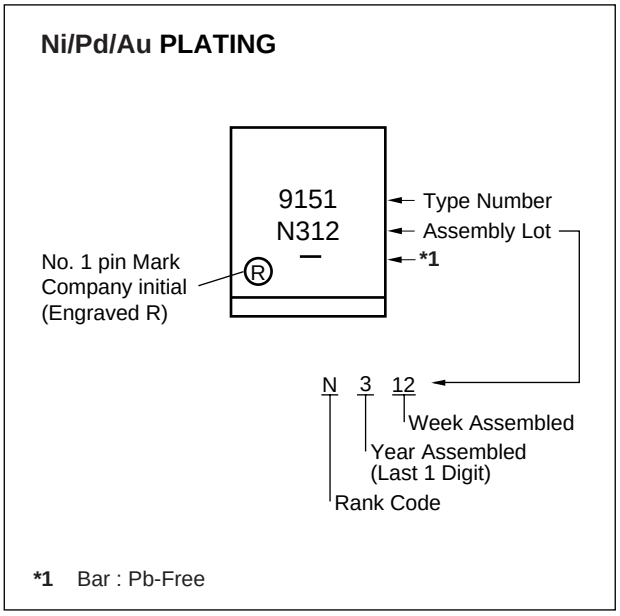


1 ( ) indicates reference dimension.

<R> PHOTOCOUPLER CONSTRUCTION

Parameter	Unit (MIN.)
Air Distance	4.2 mm
Outer Creepage Distance	4.2 mm
Isolation Distance	0.2 mm

<R> MARKING EXAMPLE



<R> ORDERING INFORMATION

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number*1
PS9151	PS9151-AX	Pb-Free (Ni/Pd/Au)	20 pcs (Tape 20 pcs cut)	Standard products (UL, CSA approved)	PS9151
PS9151-F3	PS9151-F3-AX		Embossed Tape 2 500 pcs/reel		
PS9151-V	PS9151-V-AX		20 pcs (Tape 20 pcs cut)	DIN EN 60747-5-5 (VDE 0884-5) approved (Option)	
PS9151-V-F3	PS9151-V-F3-AX		Embossed Tape 2 500 pcs/reel		

Note: ∗1. For the application of the Safety Standard, following part number should be used.

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C, unless otherwise specified)

Parameter		Symbol	Ratings	Unit
Diode	Forward Current *1	I_F	20	mA
	Reverse Voltage	V_R	5	V
Detector	Supply Voltage	V_{DD}	0 to 5.5	V
	Output Voltage	V_O	–0.5 to V_{DD}+0.5	V
	Output Current	I_O	2	mA
Isolation Voltage *2		BV	3 750	Vr.m.s.
Operating Ambient Temperature		T_A	–40 to +100	°C
Storage Temperature		T_{stg}	–55 to +125	°C

Notes: ∗1. Reduced to 0.8 mA/°C at T_A = 95°C or more.

∗2. AC voltage for 1 minute at T_A = 25°C, RH = 60% between input and output.
Pins 1-2 shorted together, 3-5 shorted together.

RECOMMENDED OPERATING CONDITIONS

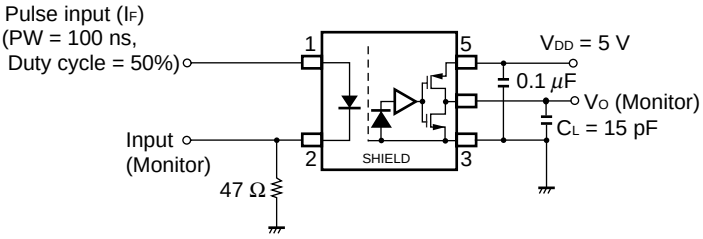
Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Forward Current	I_F	10		16	mA
Supply Voltage	V_{DD}	4.5	5.0	5.5	V

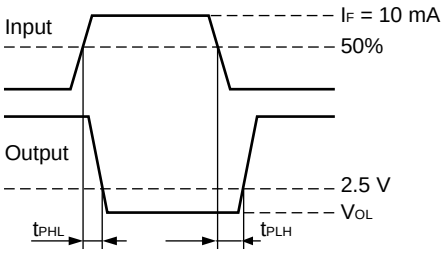
<R> ELECTRICAL CHARACTERISTICS (T_A = –40 to +100°C, V_{DD} = 4.5 to 5.5 V, unless otherwise specified)

Parameter		Symbol	Conditions	MIN.	TYP.*1	MAX.	Unit
Diode	Forward Voltage	V_F	I_F = 10 mA, T_A = 25°C	1.4	1.65	1.8	V
	Reverse Current	I_R	V_R = 3 V, T_A = 25°C			10	μA
	Terminal Capacitance	C_t	V = 0 V, f = 1 MHz, T_A = 25°C		30		pF
Detector	High Level Supply Current	I_{DDH}	I_F = 0 mA		2.5	5	mA
	Low Level Supply Current	I_{DDL}	I_F = 10 mA,		2	5	
	High Level Output Voltage	I_{CCH}	I_O = –20 μA, I_F = 0 mA	4	5		V
	Low Level Output Voltage	V_{OL}	I_O = 20 μA, I_F = 10 mA		0	0.1	
Coupled	Threshold Input Voltage	I_{FHL}	V_O < 1 V		2.2	5	mA
	Isolation Resistance	R_{I-O}	V_{I-O} = 1 kV_{DC}, RH = 40 to 60%, T_A = 25°C	10^{11}			Ω
	Isolation Capacitance	C_{I-O}	V = 0 V, f = 1 MHz, T_A = 25°C		0.6		pF
	Propagation Delay Time (H → L)*2	t_{PHL}	I_F = 10 mA, V_{DD} = 5 V, C_L = pF, CMOS Levels		35	60	ns
	Propagation Delay Time (L → H)*2	t_{PLH}			35	60	
	Pulse Width Distortion (PWD)*2	$\lvert t_{PHL}–t_{PLH} \rvert$			3	30	
	Propagation Delay Skew	t_{PSK}				40	
	Rise Time	t_r			4		
	Fall Time	t_f			4		
	Common Mode Transient Immunity at High Level Output*3	$\lvert CM_H \rvert$	V_{DD} = 5 V, I_F = 0 mA, V_{CM} = 1 kV, V_O > 4 V, T_A = 25°C	15	20		kV/μs
	Common Mode Transient Immunity at Low Level Output*3	$\lvert CM_L \rvert$	V_{DD} = 5 V, I_F = 10 mA, V_{CM} = 1 kV, V_O < 1 V, T_A = 25°C	15	20		

Notes: *1. Typical values at T_A = 25°C

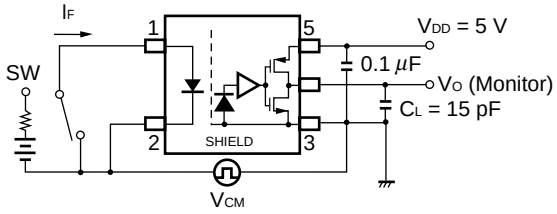
*2. Test circuit for propagation delay time

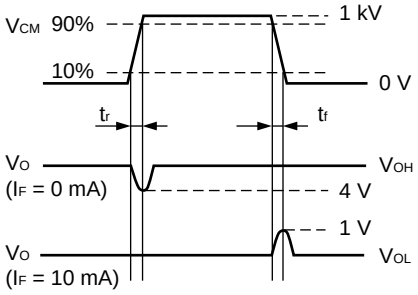




Remark C_L includes probe and stray wiring capacitance.

*3. Test circuit for common mode transient immunity

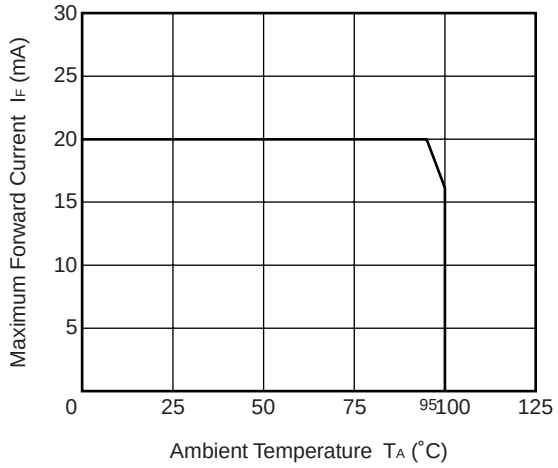




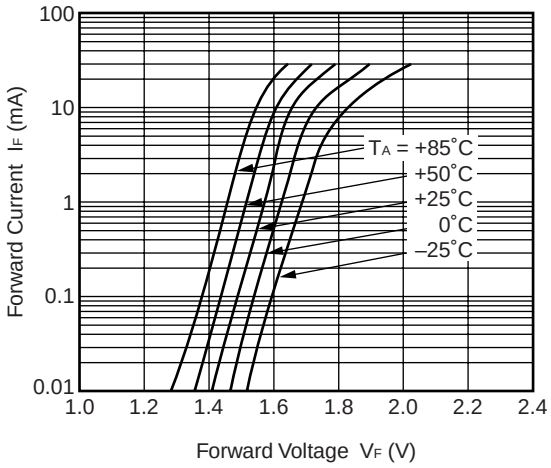
Remark C_L includes probe and stray wiring capacitance.

TYPICAL CHARACTERISTICS (T_A = 25°C, unless otherwise specified)

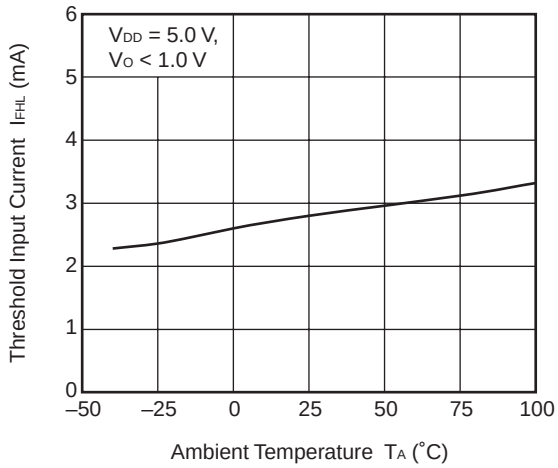
MAXIMUM FORWARD CURRENT vs. AMBIENT TEMPERATURE



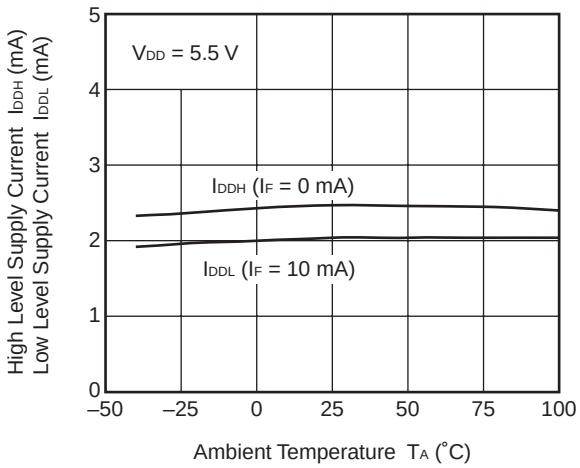
FORWARD CURRENT vs. FORWARD VOLTAGE



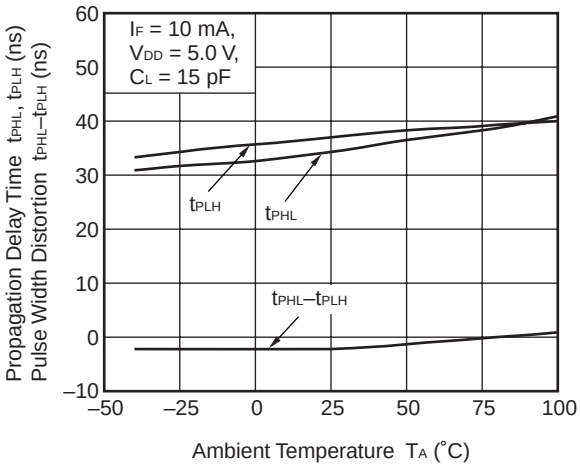
THRESHOLD INPUT CURRENT vs. AMBIENT TEMPERATURE



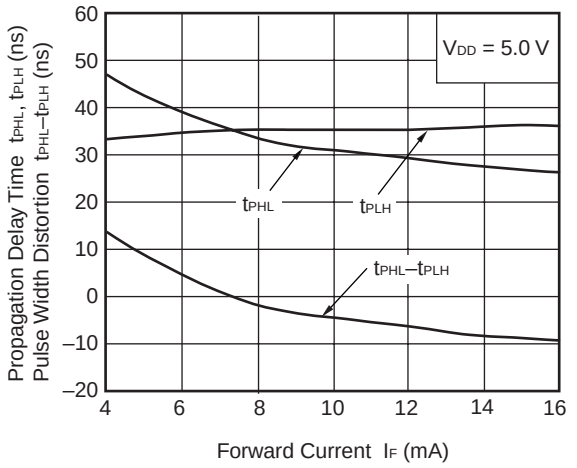
SUPPLY CURRENT vs. AMBIENT TEMPERATURE



tPHL, tPLH, tPHL–tPLH vs. AMBIENT TEMPERATURE



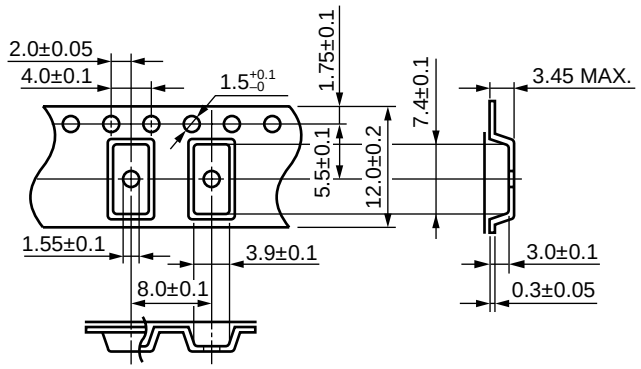
tPHL, tPLH, tPHL–tPLH vs. FORWARD CURRENT



Remark The graphs indicate nominal characteristics.

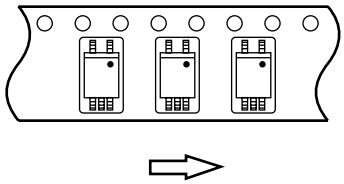
TAPING SPECIFICATIONS (UNIT: mm)

Outline and Dimensions (Tape)

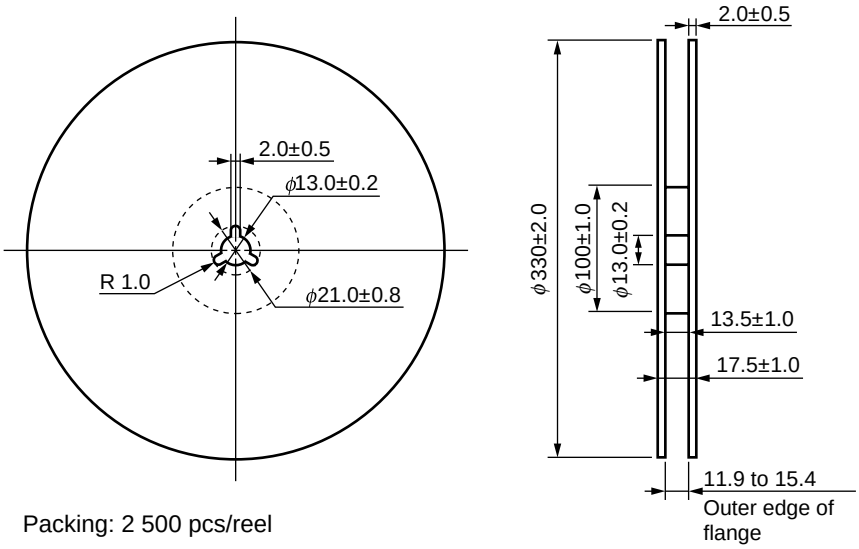


Tape Direction

PS9151-F3

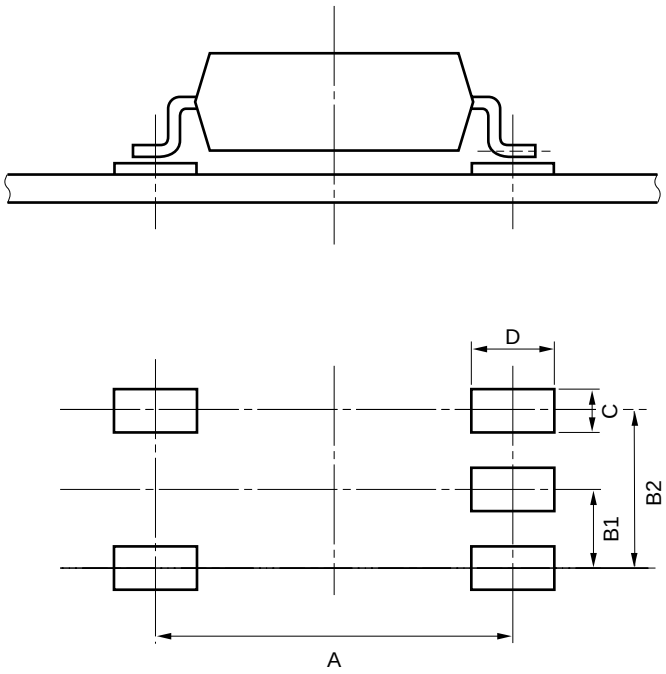


Outline and Dimensions (Reel)



Packing: 2 500 pcs/reel

<R> RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)

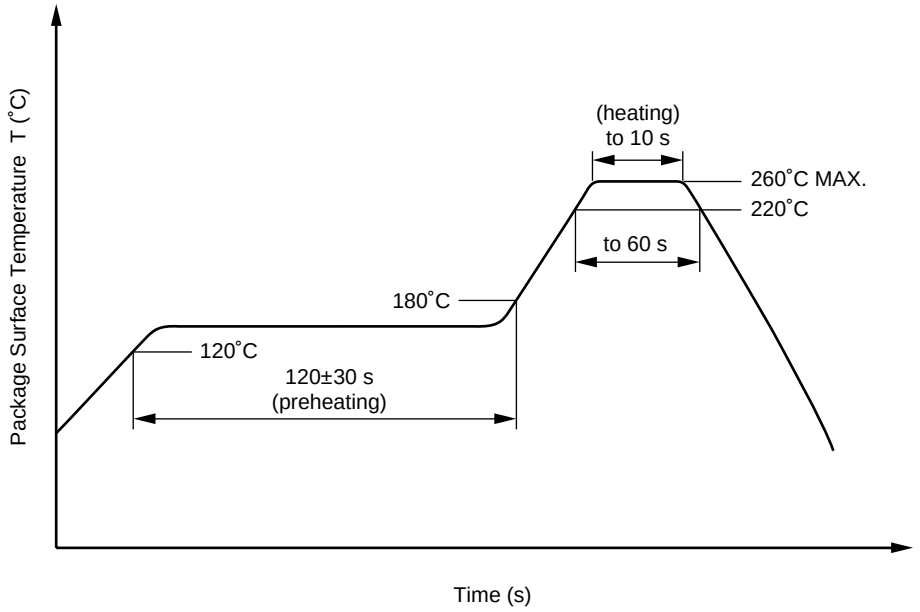


Part Number	A	B1	B2	C	D
PS9151	6.25	1.27	2.54	0.8	1.45

<R> NOTES ON HANDLING

1. Recommended soldering conditions
   (1) Infrared reflow soldering
   - Peak reflow temperature 260°C or below (package surface temperature)
   - Time of peak reflow temperature 10 seconds or less
   - Time of temperature higher than 220°C 60 seconds or less
   - Time to preheat temperature from 120 to 180°C 120±30 s
   - Number of reflows Three or less
   - Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% or less is recommended.)

Recommended Temperature Profile of Infrared Reflow



   (2) Wave soldering
   - Temperature 260°C or below (molten solder temperature)
   - Time 10 seconds or less
   - Preheating conditions 120°C or below (package surface temperature)
   - Number of times One (Allowed to be dipped in solder including plastic mold portion.)
   - Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% or less is recommended.)

   (3) Soldering by Soldering Iron
   - Peak Temperature (lead part temperature) 350°C or below
   - Time (each pin) 3 seconds or less
   - Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% or less is recommended.)

   (a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead

   (4) Cautions
   - Fluxes
     Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

2. Cautions Regarding Noise
   Be aware that when voltage is applied suddenly between the photocoupler's input and output at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

USAGE CAUTIONS

1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
2. By-pass capacitor of 0.1 μF or more is used between V_{CC} and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is 10 mm or less.

<R> 3. When V_{DD} is lower than around 2 V, the output(V_o) of this product is unstable, and this might produce undesirable operation. Be sure to check the operation of an IC or a discrete component that is connected to this product during Power-up and Power-down process. And we recommend to use a disable function (shutdown function) of the connected IC or a reset IC to avoid this undesirable operation.

4. Avoid storage at a high temperature and high humidity.

<R> SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

Parameter	Symbol	Spec.	Unit
Climatic test class (IEC 60068-1/DIN EN 60068-1)		40/100/21	
Dielectric strength maximum operating isolation voltage Test voltage (partial discharge test, procedure a for type test and random test) $U_{pr} = 1.6 \times U_{IORM}$, $P_d < 5$ pC	 U_{IORM} U_{pr}	 707 1 131	 V_{peak} V_{peak}
Test voltage (partial discharge test, procedure b for all devices) $U_{pr} = 1.875 \times U_{IORM}$, $P_d < 5$ pC	U_{pr}	1 326	V_{peak}
Highest permissible overvoltage	U_{TR}	6 000	V_{peak}
Degree of pollution (DIN EN 60664-1 VDE0110 Part 1)		2	
Comparative tracking index (IEC 60112/DIN EN 60112 (VDE 0303 Part 11))	CTI	175	
Material group (DIN EN 60664-1 VDE0110 Part 1)		III a	
Storage temperature range	T_{stg}	–55 to +125	°C
Operating temperature range	T_A	–40 to +100	°C
Isolation resistance, minimum value V_{IO} = 500 V dc at T_A = 25°C V_{IO} = 500 V dc at T_A MAX. at least 100°C	 Ris MIN. Ris MIN.	 10^{12} 10^{11}	 Ω Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve) Package temperature Current (input current I_F, P_{si} = 0) Power (output or total power dissipation) Isolation resistance V_{IO} = 500 V dc at T_A = Tsi	 Tsi Isi Psi Ris MIN.	 150 200 300 10^9	 °C mA mW Ω

Caution GaAs Products	This product uses gallium arsenide (GaAs). GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points. • Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below. 1. Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials. 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal. • Do not burn, destroy, cut, crush, or chemically dissolve the product. • Do not lick the product or in any way allow it to enter the mouth.

Revision History	PS9151 Data Sheet

Rev.	Date	Description	
		Page	Summary
1.00	Aug 22, 2008	–	First edition issued
2.00	Aug 27, 2008	–	Second edition issued
3.00	Apr 09, 2013	Throughout	Renesas format is applied to this data sheet.
		p.1	Modification of FEATURES
		p.2	Modification of PACKAGE DIMENSIONS
			Addition of PHOTOCOUPLER CONSTRUCTION
			Modification of MARKING EXAMPLE
		p.3	Modification of ORDERING INFORMATION
		pp.4, 5	Modification of ELECTRICAL CHARACTERISTICS
		p.9	Modification of RECOMMENDED MOUNT PAD DIMENSIONS
		pp.10, 11	Modification of NOTES ON HANDLING
		p.12	Addition of SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

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RENESAS

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[Colophon 2.2]