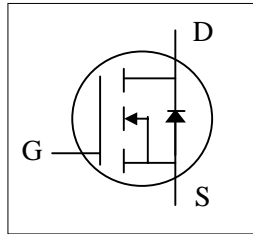




- ▼ 100% Avalanche Test
- ▼ Fast Switching Characteristics
- ▼ Simple Drive Requirement
- ▼ RoHS Compliant

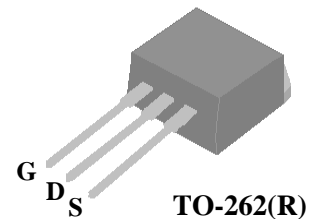


| | |
|--------------|--------------|
| BV_{DSS} | 650V |
| $R_{DS(ON)}$ | 1.4 Ω |
| I_D | 7A |

Description

AP2762 series are from Advanced Power innovated design and silicon process technology to achieve the lowest possible on-resistance and fast switching performance. It provides the designer with an extreme efficient device for use in a wide range of power applications.

The TO-262 package is widely preferred for commercial-industrial through-hole applications and suited for low voltage applications such as DC/DC converters.



Absolute Maximum Ratings @ $T_j=25^\circ\text{C}$ (unless otherwise specified)

| Symbol | Parameter | Rating | Units |
|------------------------------|--------------------------------------------|------------|------------------|
| V_{DS} | Drain-Source Voltage | 650 | V |
| V_{GS} | Gate-Source Voltage | ± 30 | V |
| $I_D @ T_C=25^\circ\text{C}$ | Drain Current, $V_{GS} @ 10\text{V}$ | 7 | A |
| I_{DM} | Pulsed Drain Current ¹ | 24 | A |
| $P_D @ T_C=25^\circ\text{C}$ | Total Power Dissipation | 92.6 | W |
| E_{AS} | Single Pulse Avalanche Energy ² | 18 | mJ |
| I_{AR} | Avalanche Current | 6 | A |
| T_{STG} | Storage Temperature Range | -55 to 150 | $^\circ\text{C}$ |
| T_J | Operating Junction Temperature Range | -55 to 150 | $^\circ\text{C}$ |

Thermal Data

| Symbol | Parameter | Value | Unit |
|--------|----------------------------------------------|-------|---------------------------|
| Rthj-c | Maximum Thermal Resistance, Junction-case | 1.35 | $^\circ\text{C}/\text{W}$ |
| Rthj-a | Maximum Thermal Resistance, Junction-ambient | 62 | $^\circ\text{C}/\text{W}$ |



AP2762R-A-HF

Electrical Characteristics @T_j=25°C(unless otherwise specified)

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Units |
|---------------------|------------------------------------------------|----------------------------------------------------------|------|------|------|-------|
| BV _{DSS} | Drain-Source Breakdown Voltage | V _{GS} =0V, I _D =1mA | 650 | - | - | V |
| R _{DS(ON)} | Static Drain-Source On-Resistance ³ | V _{GS} =10V, I _D =3A | - | - | 1.4 | Ω |
| V _{GS(th)} | Gate Threshold Voltage | V _{DS} =V _{GS} , I _D =250uA | 2 | - | 4 | V |
| g _{fs} | Forward Transconductance | V _{DS} =10V, I _D =4A | - | 3.5 | - | S |
| I _{DSS} | Drain-Source Leakage Current | V _{DS} =480V, V _{GS} =0V | - | - | 100 | uA |
| I _{GSS} | Gate-Source Leakage | V _{GS} =±30V, V _{DS} =0V | - | - | ±100 | nA |
| Q _g | Total Gate Charge ³ | I _D =6A | - | 31 | 50 | nC |
| Q _{gs} | Gate-Source Charge | V _{DS} =200V | - | 7 | - | nC |
| Q _{gd} | Gate-Drain ("Miller") Charge | V _{GS} =10V | - | 13 | - | nC |
| t _{d(on)} | Turn-on Delay Time ³ | V _{DD} =200V | - | 33 | - | ns |
| t _r | Rise Time | I _D =3A | - | 29 | - | ns |
| t _{d(off)} | Turn-off Delay Time | R _G =50Ω | - | 186 | - | ns |
| t _f | Fall Time | V _{GS} =10V | - | 46 | - | ns |
| C _{iss} | Input Capacitance | V _{GS} =0V | - | 1330 | 2130 | pF |
| C _{oss} | Output Capacitance | V _{DS} =30V | - | 100 | - | pF |
| C _{rss} | Reverse Transfer Capacitance | f=1.0MHz | - | 8 | - | pF |

Source-Drain Diode

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Units |
|-----------------|------------------------------------|------------------------------------------|------|------|------|-------|
| V _{SD} | Forward On Voltage ³ | I _S =6A, V _{GS} =0V | - | - | 1.5 | V |
| t _{rr} | Reverse Recovery Time ³ | I _S =6A, V _{GS} =0V, | - | 475 | - | ns |
| Q _{rr} | Reverse Recovery Charge | di/dt=100A/μs | - | 6.4 | - | uC |

Notes:

- 1.Pulse width limited by Max. junction temperature.
- 2.Starting T_j=25°C , V_{DD}=50V , L=1mH , R_G=25Ω
- 3.Pulse test

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

APEC DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

APEC RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN.

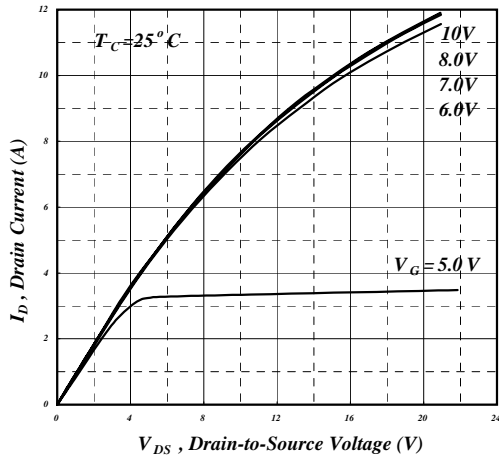


Fig 1. Typical Output Characteristics

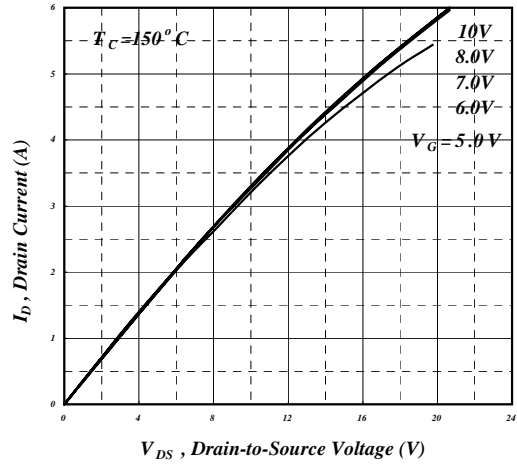


Fig 2. Typical Output Characteristics

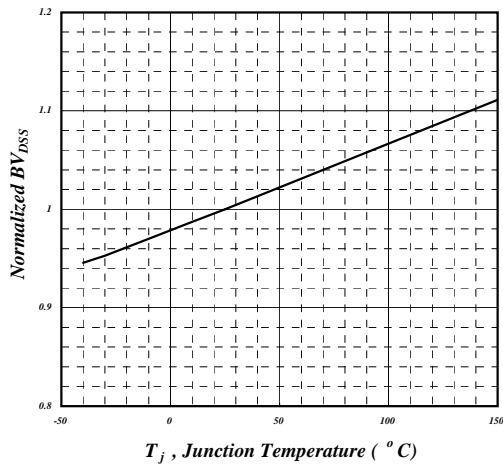


Fig 3. Normalized BV_{DSS} v.s. Junction Temperature

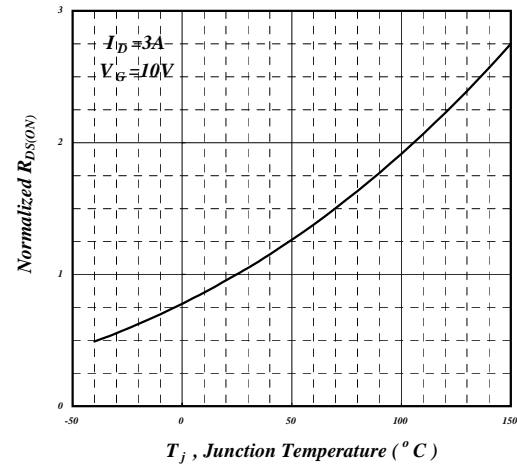


Fig 4. Normalized On-Resistance v.s. Junction Temperature

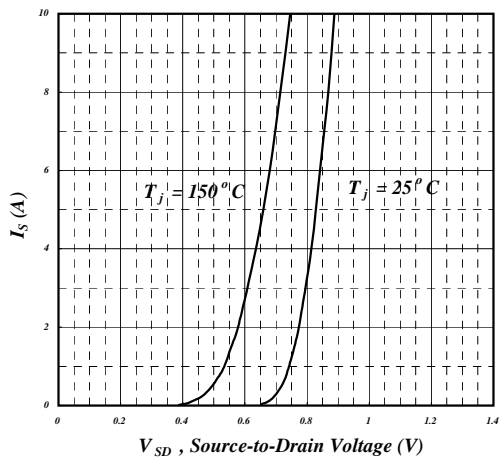


Fig 5. Forward Characteristic of Reverse Diode

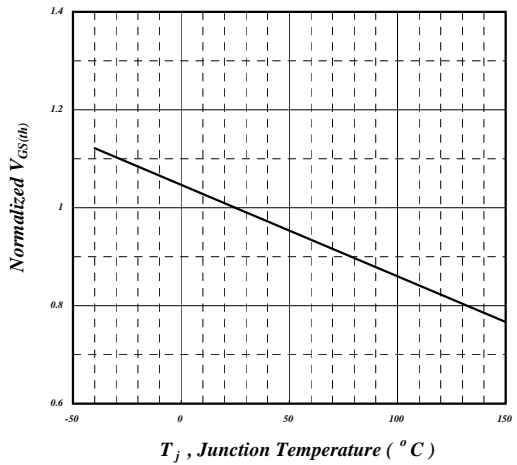


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

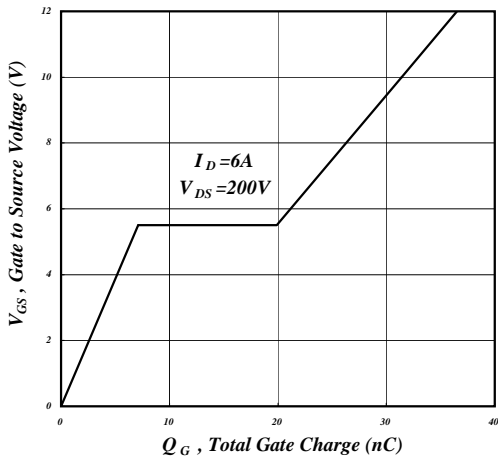


Fig 7. Gate Charge Characteristics

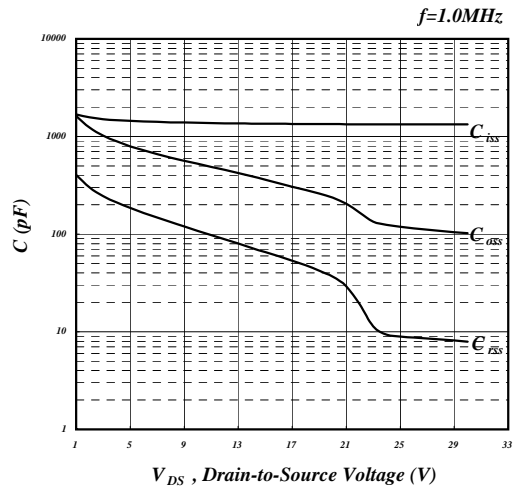


Fig 8. Typical Capacitance Characteristics

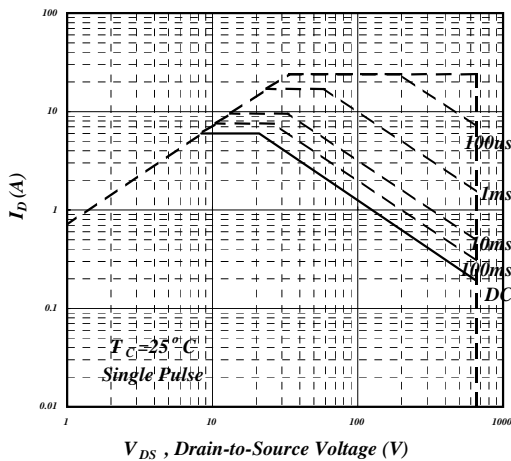


Fig 9. Maximum Safe Operating Area

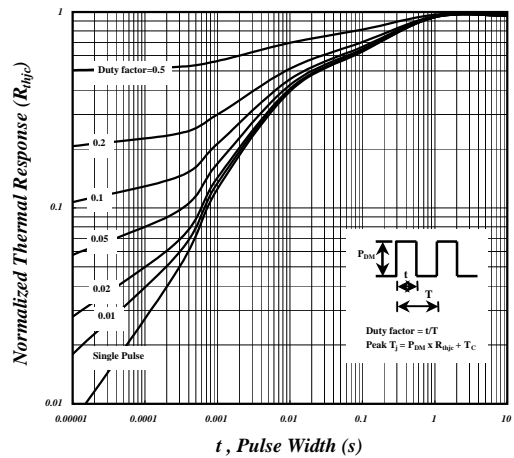


Fig 10. Effective Transient Thermal Impedance

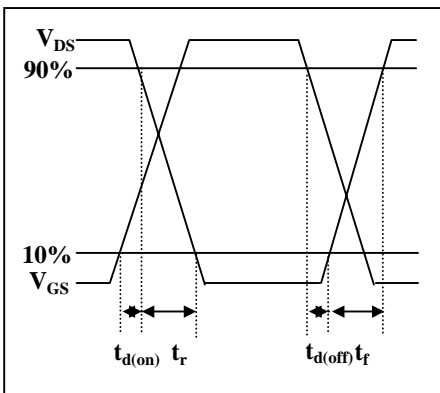


Fig 11. Switching Time Waveform

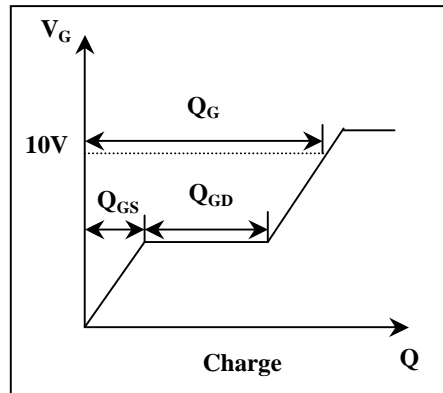


Fig 12. Gate Charge Waveform



MARKING INFORMATION

