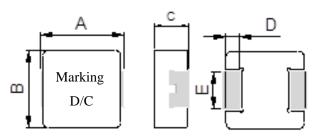
1. Part No. Expression:

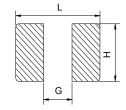
<u>PIA 1265 SP1R0 MN</u>

- (a)
- (b)
- (c)
- (d) (e)(f)
- a) Series Code
- b) Dimension Code
- c) Type Code
- d) Inductance Code
- e) Tolerance Code
- f) Internal Control Code

2. Configuration & Dimensions:

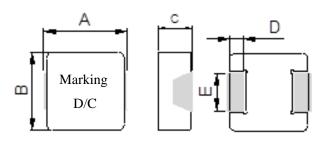
Lead Frame





Non Lead Frame

Recommend PC Board Pattern



Note:

- 1. The above PCB layout is for reference only.
- 2. Solder paste thickness of 0.15mm and above is recommended.

 3. Marking: Top row Inductance code, Bottom row YYWW

 1:4.	mm
 IIII -	TITITI

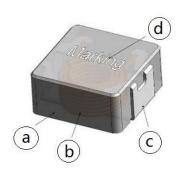
Α	В	С	D	G	Н	L	E	Inductance
40.5.0.5	40.0.0.0	00.00	00.00	0.0	F 0	44.5	4.0±0.3	Between 0.68uH~1.5uH
13.5±0.5	12.6±0.2	6.2±0.3	2.3±0.3	8.0	5.0	14.5	4.7±0.3	0.22uH and below 2.2uH and above

3. Schematic:

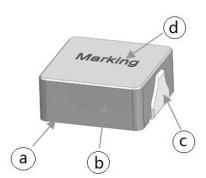


4. Material List:

i) Lead Frame



ii) Non-lead Frame



- a) Core
- b) Wire
- c) Terminal
- d) Ink

5. General Specification:

(a) Operating Temp. : -40°C to +125°C (Inclusive of coil temp rise)

(b) Storage Temp.: -40°C to +125°C (on board)

(c) Humidity Range: 85 ± 2% RH

(d) Heat Rated Current (Irms) will cause the coil temperature rise approximately Δt of 40°C (keep 1min)

(e) Saturation Current (Isat Typ.) will cause L0 to drop approximately 30%.

(f) Part Temp. (Ambient + Temp. Rise) should not exceed 125°C under worst case operating conditions.

(g) Storage condition (component in its packaging)

i) Temperature: -10 to 40°C

ii) Humidity: 50~60% RH

6. Electrical Characteristics:

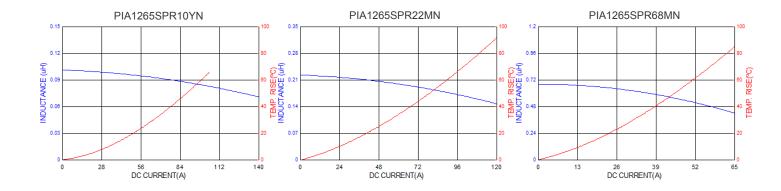
Part Number	Inductance Lo (uH) @ 0A	Test Frequency, L	Heat Rating Current DC (A) Irms.		Saturation Current DC (A) Isat.		DCR (mΩ)Typ.	DCR (mΩ)Max.	Туре
			Тур.	Max.	Тур.	Max.			
PIA1265SPR10YN	0.10	100kHz/1.0V	65	60	120	115	0.2	0.25	Non lead frame
PIA1265SPR22MN	0.22	100kHz/1.0V	53	42	112	105	0.4	0.46	Non lead frame
PIA1265SPR68MN	0.68	100kHz/1.0V	36.5	33	55	46	1.25	1.5	Non lead frame
PIA1265SP1R0MN	1.00	100kHz/1.0V	33	29	45	36	1.5	1.8	Non lead frame
PIA1265SP1R5MN	1.50	100kHz/1.0V	29	25	35	30	2.2	2.53	Non lead frame
PIA1265SP2R2MN	2.20	100kHz/1.0V	25	21	28.5	24	3.7	4.2	Lead frame
PIA1265SP3R3MN	3.30	100kHz/1.0V	22	19	27	22.5	5.3	6.2	Lead frame
PIA1265SP4R7MN	4.70	100kHz/1.0V	20	17	25	21	6.8	8.0	Lead frame
PIA1265SP5R6MN	5.60	100kHz/1.0V	18	15	23	19.5	8.3	9.8	Lead frame
PIA1265SP6R8MN	6.80	100kHz/1.0V	16.5	14	21	18	9.8	11.3	Lead frame
PIA1265SP8R2MN	8.20	100kHz/1.0V	15	12.5	19	17	12	13.8	Lead frame
PIA1265SP100MN	10.0	100kHz/1.0V	13	11	17	15	13	15.8	Lead frame
PIA1265SP220MN	22.0	100kHz/1.0V	10	8	10	9	31	35	Lead frame
PIA1265SP330MN	33.0	100kHz/1.0V	9	6.5	9	8	46	55	Lead frame

^{*}Tolerance code : $M = \pm 20\%$, $Y = \pm 30\%$

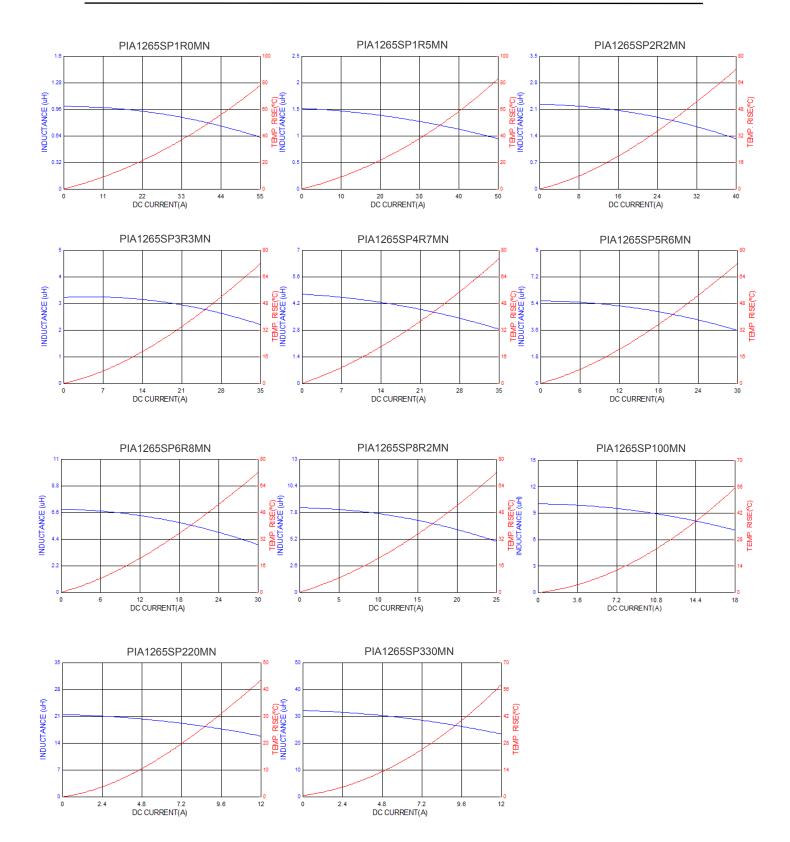
Notes:

1) At all times, the current supplied to the product should not exceed Isat Max. value.

7. Characteristics Curves:









8. Soldering:

Mildly activated rosin fluxes are preferred. The minimum amount of solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. Our terminations are suitable for all re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air.

8-1 Solder Re-flow:

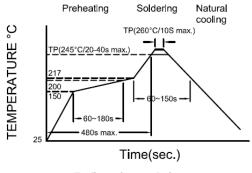
Recommended temperature profiles for re-flow soldering in Figure 1.

8-2 Soldering Iron (Figure 2):

Products attachment with soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

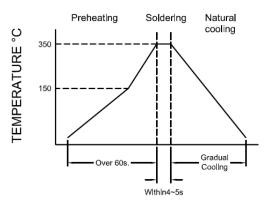
Note:

- a) Preheat circuit and products to 150°C.
- b) 355°C tip temperature (Max.)
- c) Never contact the ceramic with the iron tip
- d) 1.0mm tip diameter (Max.)
- e) Use a 20 watt soldering iron with tip diameter of 1.0mm
- f) Limit soldering time to 4~5 secs.



Reflow times: 3 times max

Fig.1

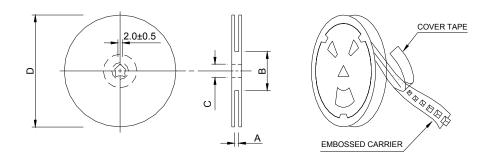


Iron Soldering times: 1 times max

Fig.2

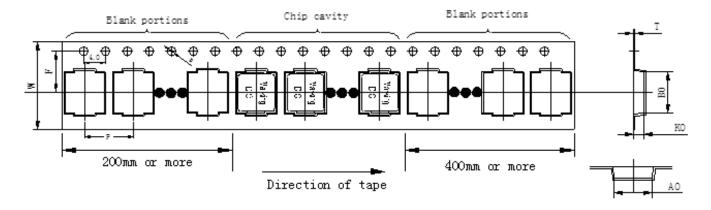
9. Packaging Information:

9-1 Reel Dimension:



Туре	A(mm)	B(mm)	C(mm)	D(mm)
13"x24mm	24.4+2/-0	100±2	13.5±0.5	330

9-2 Tape Dimension:



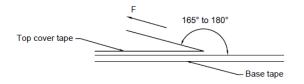
Series	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	W(mm)	F(mm)	T(mm)	D(mm)
PIA	1265	14.1±0.1	12.9±0.1	7.0±0.1	16.0±0.1	24.0±0.3	11.5±0.1	0.35±0.05	1.5±0.1

9-3 Packaging Quantity:

PIA	1265
Chip / Reel	500
Inner box	1000
Carton	4000



9-4 Tearing Off Force:



The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions.

Room Temp.	Room Humidity (%)	Room atm (hPa)	Tearing Speed mm/min	
5~35	45~85	860~1060	300	

Application Notice:

1. Storage Conditions:

To maintain the solderability of terminal electrodes:

- a) Recommended products should be used within 12 months from the time of delivery.
- b) The packaging material should be kept where no chlorine or sulfur exists in the air.

2. Transportation:

- a) Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- b) Vacuum pick up is strongly recommended for individual components.
- c) Bulk handling should ensure that abrasion and mechanical shock are minimized.