

Size: 1.30in x 0.53in x 0.31~0.37in

Size: 2.00in x 0.50in x 0.37~0.31in

OPTIONS

- SMD or SIP Package
- Remote ON/OFF Positive or Negative Logic
- Current Share
- Extra GND Pins (only for SMD package)
- Long Pins (only for SIP Type)

FEATURES

- High Efficiency up to 93%
- No Minimum Load Required
- Small Size and Low Profile
- SMD Package Qualified for Lead Free Reflow Solder Process According IPC J-STD-020D
- Monotonic Start-Up Into Pre-Biased Output
- Output Voltage Sequencing
- Tracking

- Parallel Operation with Active Current Sharing
- CE Marked
- Compliant to RoHS II & REACH
- Current Share
- Over Load, Short Circuit, and Over Temperature Protection
- UL60950-1, EN60950-1, and IEC60950-1 Safety Approvals

APPLICATIONS

1/9/2018

- Wireless Network
- Telecom/Datacom
- Industry Control System
- Distributed Power Architectures
- Semiconductor Equipment
- Microprocessor Power Applications

DESCRIPTION

The POL30 series of DC/DC non-isolated open frame converters offers up to 30A output current in a compact and low profile package. This series consists of single output models and input voltage ranges of 4.5~5.5VDC and 6~14VDC. Several different options are available for this series including SMD or SIP package, positive or negative logic, current share, and different pin options. Each model in this series is CE marked, compliant to RoHS II, and is protected against over load, short circuit and over temperature conditions. This series has UL60950-1, EN60950-1, and IEC60950-1 safety approvals.

MODEL SELECTION TABLE								
Model Number	Input Voltage Range	Output Voltage	Output Current	No Load Input Current Vin(nom), 3.3VDC	Package Type	Maximum Capacitive Load ⁽¹⁾ ESR≥1mΩ/ESR≥10mΩ	Efficiency Vin(nom), 3.3VDC @Full Load	
POLS30-05T	4.5~5.5VDC	0.9. 2.63\/DC	30A	100m A	SMD	2000/40000	93%	
POLT30-05T	Vin(min.)=Vout(set)+1.5	0.8~3.63VDC	30A	180mA	SIP	2000/10000μF		
POLS30-12T		0.8≤Vout≤2.75	30A		SMD	2000/10000μF		
	6~14VDC	2.75 <vout≤3.63< td=""><td>20A</td><td>200mA</td><td>SIVID</td><td rowspan="2">93%</td></vout≤3.63<>	20A	200mA	SIVID		93%	
	Vin(min.)=Vout(set)+2.4	0.8≤Vout≤2.75	30A	20011A	SIP			
1 OL130-121		2.75 <vout≤3.63< td=""><td>25A</td><td></td><td>SIF</td><td></td><td colspan="2"></td></vout≤3.63<>	25A		SIF			



SPECIFICATIONS					
All specifications	are based on 25°C, Nominal Input Voltage, and Maximum Output Currer We reserve the right to change specifications based on technological ad		herwise note	ed.	
SPECIFICATION	TEST CONDITIONS	Min	Тур	Max	Unit
INPUT SPECIFICATIONS	TEST CONSTITUTE		1)	Max	Orne
111 01 01 2011 107 (110110	5Vin(nom) Vin(min.)=Vout(set) + 1.5VDC	4.5	5	5.5	1
Input Voltage Range					VDC
	12Vin(nom) Vin(min.)=Vout(set) + 2.4VDC	6	12	14	
Input Reflected Ripple Current	5~20MHz, 1µH source impedance		100		mAp-p
Start-Up Voltage			4.4		VDC
Shutdown Voltage			4.3		VDC
Input Filter ⁽²⁾				tor Type	
OUTPUT SPECIFICATIONS			Capacii	or rypc	
Output Voltage			See .	Table	
Voltage Accuracy	%of Vout(set)	-1.5	000	+1.5	%
Line Regulation	Vin=Vin(min.) to Vin(max.) at Full Load; % of Vout(set)	-0.1		+0.1	%
Load Regulation	No Load to Full Load; % of Vout(set)	-0.7		+0.4	%
	POLT30-12T	0.8		5.5	
Voltage Adjustability ⁽³⁾	Others	0.8		3.63	VDC
Remote Sense	Outoro	0.0	0.5	5.05	VDC
Output Current				L Table	VDC
Maximum Capacitive Load				Table	
	Management by 200M In horsely dath, with a 4.45 MI CC 8 a 40.45 T/C			lable	
Ripple & Noise	Measured by 20MHz bandwidth, with a 1μF MLCC & a 10μF T/C		75		mVp-p
	With a 1µF MLCC & a 10µF T/C				
Dynamic Load Response	Δlo/Δt=5A/μs, Vin(nom) Peak deviation		30		mV
	50% load step change Setting time (Vout<10% peak deviation)		25		μs
	With 2pcs of 150µF polymer capacitors				
Dynamic Load Response	Δlo/Δt=5A/μs, Vin(nom) Peak Deviation		250		mV
	50% load step change Setting Time(Vout<10% peak deviation)		40		μs
Temperature Coefficient		-0.5		+0.5	%/ºC
Rise Time	Time for Vout to rise from 10% to 90% of Vout(set)			10	ms
Output Voltage Overshoot-Startup	Vin=Vin (min.) to Vin(max.) at Full Load; % of Vout (set)			3.0	%
REMOTE ON/OFF CONTROL(4)					
Negative Logic (Option)	DC-DC ON			.3~1.2VDC	
rvegative Logic (Option)	DC-DC OFF	3.0VDC~Vin (max.)			
Positive Logic (Standard)	DC-DC ON	0	pen or 3.0V		x.)
	DC-DC OFF	-0.3~1.2VDC			
Input Current of CTRL Pin				0.2	mA
Remote OFF Input Current				3.3	mA
Turn-On Delay Time ⁽⁵⁾			2.5		ms
Active Load Share (Option) ⁽⁶⁾	% of lout rated Accuracy		10		%
, ,	Number of units in parallel			5	pcs
Sequencing Delay Time	Delay from Vin, min. to application of voltage on SEQ pin	10			Ms
Tracking Accuracy	Vin(min.) to Vin(max.), lout(min.) to lout(max.), Vseq < Vout				
	Power-Up (2V/ms)		100		mV
VsEQ - Vout	Power-down (1V/ms		200		
PROTECTION					
Short Circuit Protection		Hi	iccup, Auton	natic Recov	ery
Over Load Protection	% of lout rated		150		%
Over Temperature Protection			125		°C
ENVIRONMENTAL SPECIFICATION	IS				
Operating Case Temperature	With Derating	-40		+85	°C
Storage Temperature		-55		+125	°C
Thermal Shock		- 50	MII -ST	D-810F	
Relative Humidity	Non-Condensing	5	31	95	%RH
Vibration	Gondononia		MII -ST	D-810F	731111
MTBF	MIL-HDBK-217F, Full Load		1,258,000	2 0.01	Hours
GENERAL SPECIFICATIONS	INIE FIBBIC ETTT, T dii Edud		1,200,000		riours
Efficiency			See	Table	
Switching Frequency		261	300	339	kHz
ormig i roquonoy		-01	000	000	IXI IZ



SPECIFICATIONS

All specifications are based on 25°C, Nominal Input Voltage, and Maximum Output Current unless otherwise noted.

We reserve the right to change specifications based on technological advances.

SPECIFICATION	TEST CONDITIONS			Тур	Max	Unit		
PHYSICAL SPECIFICATIONS								
Woight	SMD Models			0.21oz (6g)				
Weight	SIP Models			0.25oz (7g)				
	SMD Models	POLS-05T	1.30in x 0.53in x 0.37in (33mm x 13.5mm x 9.4mm)					
Dimensions (L.v.M.v.H.)	SIVID IVIOUEIS	POLS-12T	1.30in x 0.53in x 0.31in (33mm x 13.5mm x 7.8mm)					
Dimensions (L x W x H)	SIP Models	POLT-05T	2in x 0.50in x 0.37in (50.8mm x 12.7mm x 9.4mm)					
	SIP Wodels	POLT-12T	2in x 0.50in x 0.31in (50.8mm x 12.7mm x 7.8mm)					
SAFETY & EMC CHARACTERISTICS								
Safety Approvals	UL60950-1, EB60950-1, IEC60950-1							
Lead-Free Reflow Solder Process	IPC J-STD-020D							
Moisture Sensitivity Level (MSL)	IPC J-STD-033B Level 2a							

NOTES

- Test by minimum input and constant resistive load.
- 2. To make sure the module is stable, it is necessary that input external capacitors minimize input ripple voltage of the module.
- 3. Output voltage programmable from 0.8V to 5.0V by connecting a single resistor (shown as Trim Table) between the Trim and GND pins of the module. To calculate the value of the resistor Rtrim for a a particular output voltage Vout, use the following equation:

$$Rtrim = \begin{bmatrix} 1200 \\ \hline Vout - 0.80 \end{bmatrix} \Omega$$

$$Trim \ Figure$$

$$+INPUT + OUTPUT$$

$$GND$$

$$Rtrim$$

$$Rtrim$$



380

185

Trim Table

- 4. Referred to -Vin pin
- Case 1: ON/OFF input is set to logic low (module on) and then input power is applied (delay from instant at which Vin=Vin(min) until Vout=10% of Vout(set))

3.3

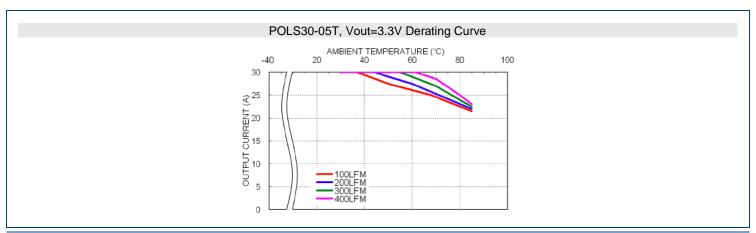
5.0

- Case 2: Input power is applied for at least one second and then the ON/OFF input is set to logic low (delay from instant at which Von/off=0.3VDC until Vout=10% of vout(set))
- 6. Selecting current share function may cause regulations to not meet listed specifications.

CAUTION: This power module is not internally fused. An input line fused must always be used.

*Due to advances in technology, specifications subject to change without notice.

DERATING CURVES

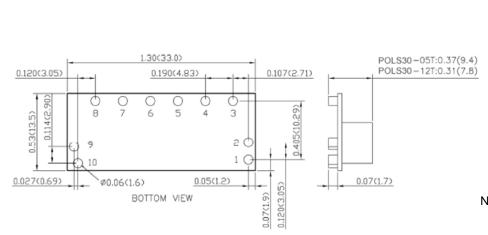


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POLS₃₀



MECHANICAL DRAWINGS



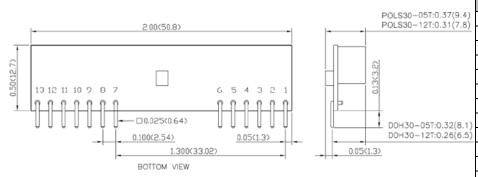
PIN CONNECTION

THEODIVIDLE					
PIN	DEFINE				
1	Ctrk				
2	GND (option)				
3	Share (option)				
4	+Sense				
5	Trim +Vout				
6					
7	GND				
8	Seq				
9	GND (option)				
10	+Vin				

Notes:

- 1. All dimensions in inch (mm)
- 2. Tolerance: x.xx±0.02 (x.xx±0.5) x.xxx±0.01 (x.xx±0.25)
- 3. Pin Pitch Tolerance ±0.01 (0.25)
- 4. Pin Dimension Tolerance ±0.004(0.1)

POLT30



PIN CONNECTION

PIN	DEFINE
1	+Vout
2	+Vout
3	+Sense
4	+Vout
5	GND
6	GND
7	Share (Option)
8	GND
9	+Vin
10	+Vin
11	Seq
12	Trim
13	Ctrl



MODEL NUMBER SETUP

POLS	30	-	05	T	-	Р
Series Name	Output Voltage		Input Voltage	No Assembly		Assembly
POLS: SMD Type POLT: SIP Type	30 : 30A		05 : 4.5~5.5VDC 12 : 6~14VDC			None: Remote On/Off Positive Logic P: Remote On/Off Negative Logic S: Current Share E: Extra GND pin 2 extra GND ⁽¹⁾ L: Long Pins 5.08mm±0.25mm ⁽²⁾

Notes:

- 1. E for SMD Type Only
- 2. L for SIP Type Only

COMPANY INFORMATION -

Wall Industries, Inc. has created custom and modified units for over 50 years. Our in-house research and development engineers will provide a solution that exceeds your performance requirements on-time and on budget. Our ISO9001-2008 certification is just one example of our commitment to producing a high quality, well-documented product for our customers.

Our past projects demonstrate our commitment to you, our customer. Wall Industries, Inc. has a reputation for working closely with its customers to ensure each solution meets or exceeds form, fit and function requirements. We will continue to provide ongoing support for your project above and beyond the design and production phases. Give us a call today to discuss your future projects.

Contact Wall Industries for further information:

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