

## **HAT2210R**

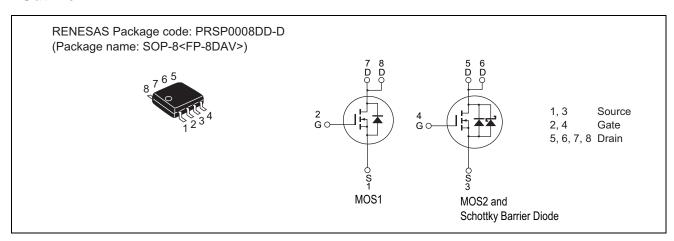
# Silicon N Channel Power MOSFET with Schottky Barrier Diode High Speed Power Switching

R07DS1368EJ0301 Rev.3.01 Jan 20, 2017

#### **Features**

- Low on-resistance
- Capable of 4.5 V gate drive
- High density mounting
- Built-in Schottky Barrier Diode

#### **Outline**



#### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

		Rat		
Item	Symbol	MOS1	MOS2 & SBD	Unit
Drain to source voltage	V <sub>DSS</sub>	30	30	V
Gate to source voltage	V <sub>GSS</sub>	±20	±12	V
Drain current	I <sub>D</sub>	7.5	8.0	Α
Drain peak current	I <sub>D(pulse)</sub> Note1	60	64	Α
Reverse drain current	I <sub>DR</sub>	7.5	8.0	Α
Channel dissipation	Pch Note2	1.5	1.5	W
Channel temperature	Tch	150	150	°C
Storage temperature	Tstg	-55 to +150	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. 1 Drive operation; When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW  $\leq$  10 s

#### **Electrical Characteristics**

#### • MOS1

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	30	_	_	V	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0
Gate to source leak current	Igss	_	_	±0.1	μА	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0
Zero gate voltage drain current	IDSS	1	_	1	μΑ	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	_	2.5	V	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA
Static drain to source on state	R <sub>DS(on)</sub>	1	19	24	mΩ	$I_D = 3.75 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note3}}$
resistance	R <sub>DS(on)</sub>	_	27	40	mΩ	$I_D = 3.75 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note3}}$
Forward transfer admittance	y <sub>fs</sub>	9	15	_	S	$I_D = 3.75 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note3}}$
Input capacitance	Ciss	_	630	_	pF	V <sub>DS</sub> = 10 V
Output capacitance	Coss	_	155	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	57	_	pF	f = 1MHz
Total gate charge	Qg	_	4.6	_	nC	V <sub>DD</sub> = 10 V
Gate to source charge	Qgs	_	2.2	_	nC	V <sub>GS</sub> = 4.5 V
Gate to drain charge	Qgd	_	1.2	_	nC	I <sub>D</sub> = 7.5 A
Turn-on delay time	t <sub>d(on)</sub>	_	7	_	ns	V <sub>GS</sub> =10 V, I <sub>D</sub> = 3.75 A
Rise time	tr	_	14	_	ns	V <sub>DD</sub> ≈ 10 V
Turn-off delay time	t <sub>d(off)</sub>	_	36	_	ns	$R_L = 2.66 \Omega$
Fall time	t <sub>f</sub>	_	3.4	_	ns	$R_g = 4.7 \Omega$
Body-drain diode forward voltage	$V_{DF}$	_	0.85	1.11	V	IF = 7.5 A, V <sub>GS</sub> = 0 <sup>Note3</sup>
Body-drain diode reverse	t <sub>rr</sub>	_	17	_	ns	IF =7.5 A, V <sub>GS</sub> = 0
recovery time						diF/ dt = 100 A/μs

Notes: 3. Pulse test

#### • MOS 2 & Schottky Barrier Diode

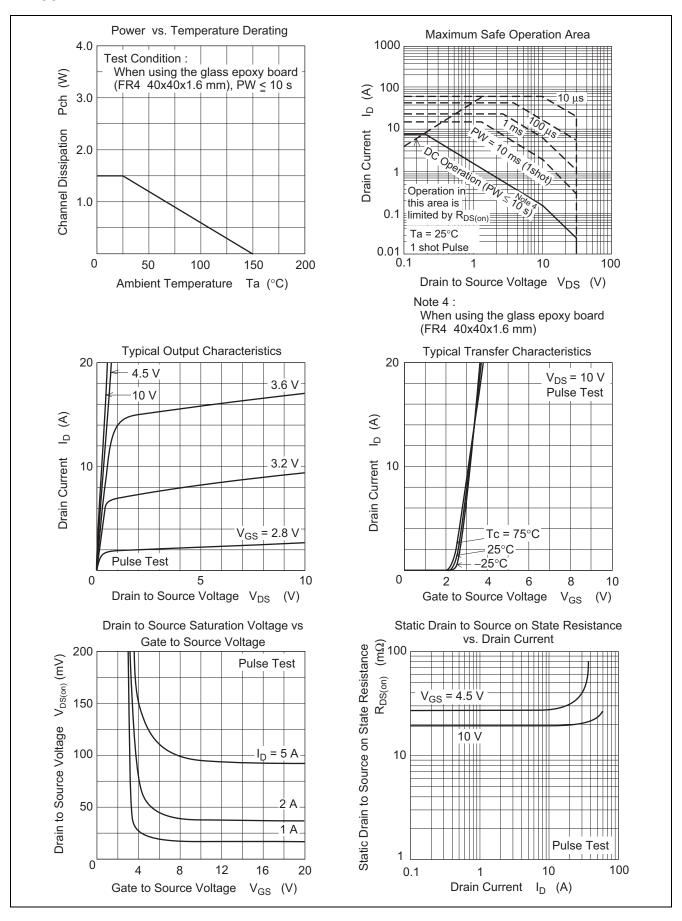
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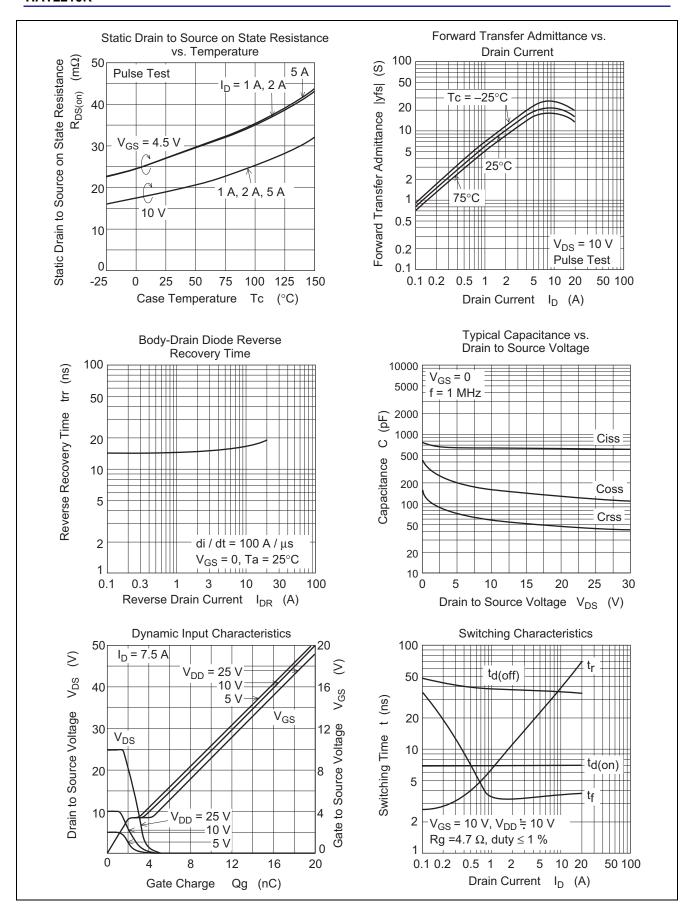
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	30	_	_	V	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0
Gate to source leak current	Igss		_	±0.1	μΑ	V <sub>GS</sub> = ±12 V, V <sub>DS</sub> = 0
Zero gate voltage drain current	IDSS		_	1	m A	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0
Gate to source cutoff voltage	V <sub>GS(off)</sub>	1.4	_	2.5	V	V <sub>DS</sub> = 10 V, I <sub>D</sub> =1 mA
Static drain to source on state	R <sub>DS(on)</sub>		17	22	mΩ	I <sub>D</sub> =4 A, V <sub>GS</sub> = 10 V <sup>Note3</sup>
resistance	R <sub>DS(on)</sub>		21	29	mΩ	I <sub>D</sub> = 4 A, V <sub>GS</sub> = 4.5 V <sup>Note3</sup>
Forward transfer admittance	y <sub>fs</sub>	15	25	_	S	I <sub>D</sub> = 4 A, V <sub>DS</sub> = 10 V <sup>Note3</sup>
Input capacitance	Ciss		1330	_	pF	V <sub>DS</sub> = 10 V
Output capacitance	Coss		230	_	pF	V <sub>GS</sub> = 0
Reverse transfer capacitance	Crss		92	_	pF	f = 1MHz
Total gate charge	Qg	_	11	_	nC	V <sub>DD</sub> = 10 V
Gate to source charge	Qgs	_	3.8	_	nC	V <sub>GS</sub> = 4.5 V
Gate to drain charge	Qgd	_	3.2	_	nC	I <sub>D</sub> = 8 A
Turn-on delay time	t <sub>d(on)</sub>	_	10	_	ns	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 4 A
Rise time	tr	_	16	_	ns	V <sub>DD</sub> ≈ 10 V
Turn-off delay time	t <sub>d(off)</sub>	_	43	_	ns	$R_L = 2.5 \Omega$
Fall time	t <sub>f</sub>	_	3.9	_	ns	$R_g = 4.7 \Omega$
Schottky Barrier diode forward voltage	$V_{F}$	_	0.5	_	V	IF = $3.5 \text{ A}$ , $V_{GS} = 0$ Note3
Body-drain diode reverse	t <sub>rr</sub>	_	15	_	ns	IF = 8 A, V <sub>GS</sub> = 0
recovery time						diF/ dt = 100 A/μs

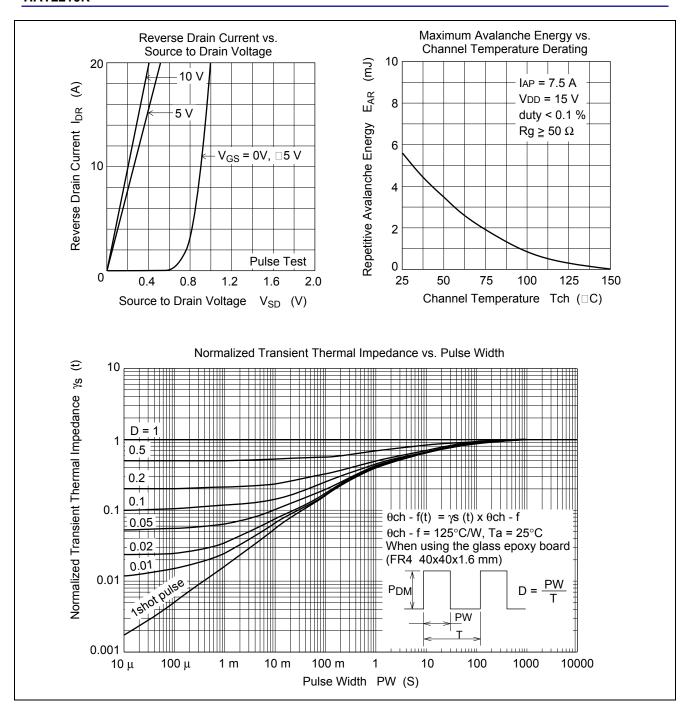
Notes: 3. Pulse test

#### **Main Characteristics**

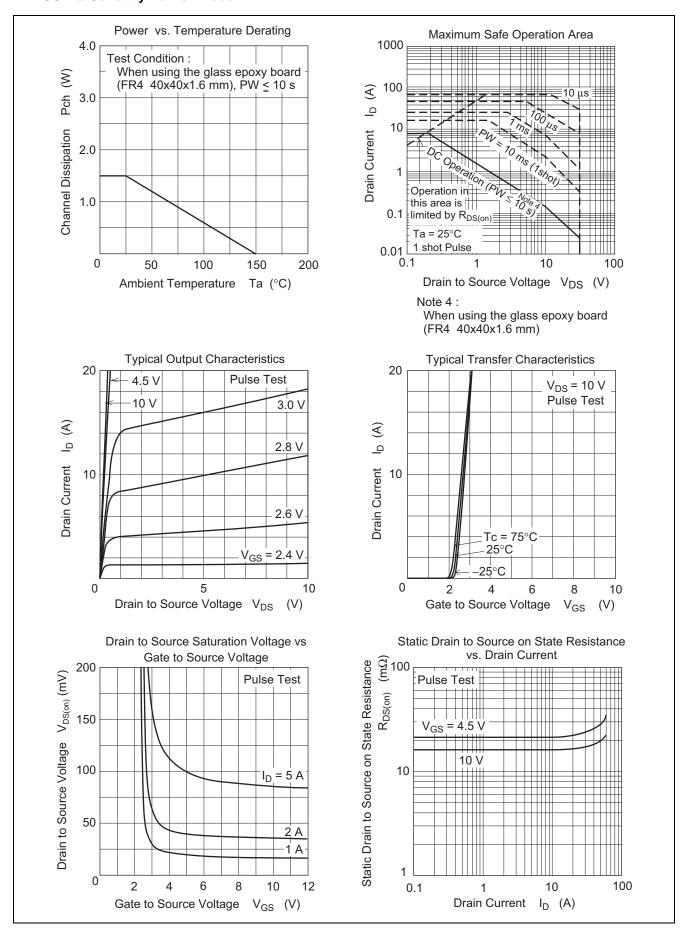
#### • MOS 1

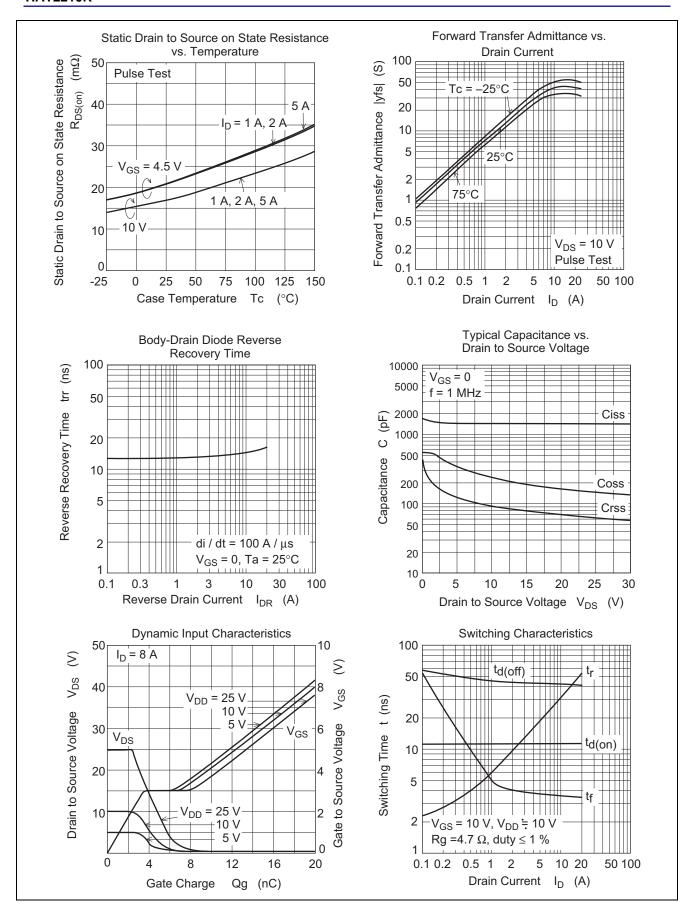


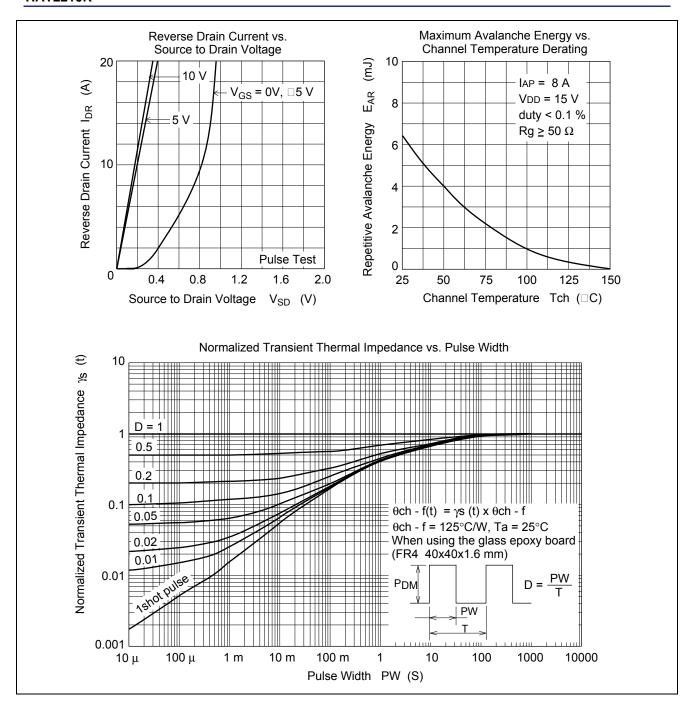




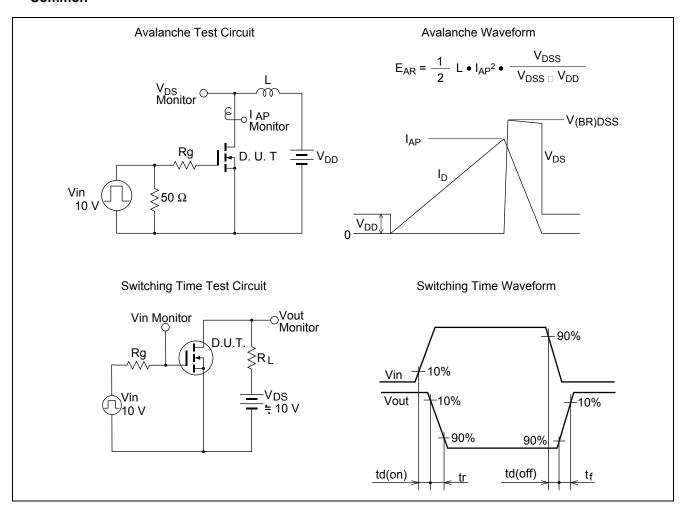
#### • MOS 2 & Schottky Barrier Diode



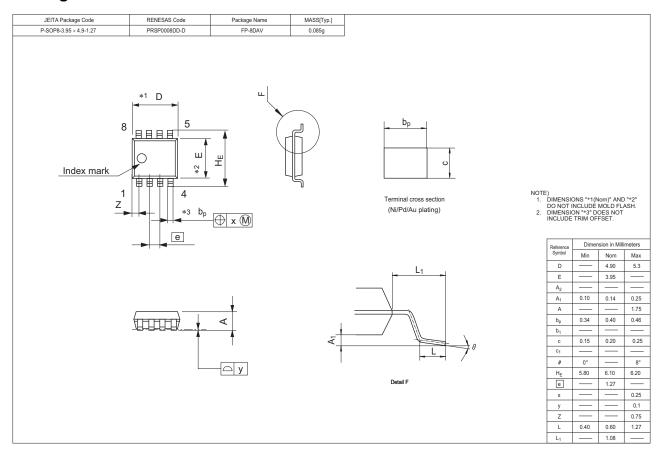




#### • Common



#### **Package Dimensions**



### **Ordering Information**

Orderable Part Number	Quantity	Shipping Container
HAT2210R-EL-E	2500 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales Office to check the state of production before ordering the product.

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