

Features

- Bidirectional TVS 3.3 V
- Low capacitance - 13 pF
- ESD protection 15 kV
- 0402 DFN package
- RoHS compliant*

Applications

- Computers and peripherals
- Communication systems
- Audio & video equipment
- Portable instrumentation
- Handheld portable devices

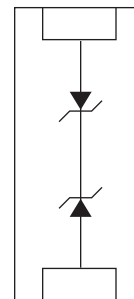
CDDFN2-T3.3B - Surface Mount TVS Diode

General Information

The Bourns® Model CDDFN2-T3.3B low capacitance device provides ESD, EFT and surge protection for external ports of electronic devices such as cellular phones, handheld electronics and other portable electronic devices.

The device measures 1.0 mm x 0.60 mm x 0.55 mm and is available in a DFN-2 package intended to be mounted directly onto an FR4 printed circuit board. The device will fit a 0402 footprint.

The device is designed to meet IEC 61000-4-2 (ESD), IEC 61000-4-4 (EFT) and IEC61000-4-5 (Surge) protection requirements.



Maximum Ratings (@ T_A = 25 °C Unless Otherwise Noted)

Parameter	Symbol	Value	Unit
Storage Temperature	T _{STG}	-55 to +150	°C
Operating Temperature	T _{OPR}	-55 to +85	°C
ESD Protection per IEC 61000-4-2	Contact Discharge	15	kV
	Air Discharge	15	kV
EFT Protection per IEC 61000-4-4 (5/50 ns)	EFT	50	A
Peak Pulse Current per IEC 61000-4-5 (8/20 μs)	I _{pp}	5	A

Electrical Characteristics (@ T_A = 25 °C Unless Otherwise Noted)

Parameter	Symbol	Min.	Typ.	Max.	Unit
Working Peak Voltage	V _{WM}	-3.3		3.3	V
Breakdown Voltage @ 1 mA	V _{BR}	4		6.5	V
Leakage Current @ 3.3 V	I _L		0.1	1.0	μA
Capacitance @ 0 V, 1 MHz	C _J		13.5	16.5	pF
Clamping Voltage @ I _{pp} = 5 A, 8/20 μs	V _C		6.5	8	V

BOURNS®

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*RoHS Directive 2002/95/EC Jan. 27, 2003 including annex and RoHS Recast 2011/65/EU June 8, 2011.

Specifications are subject to change without notice.

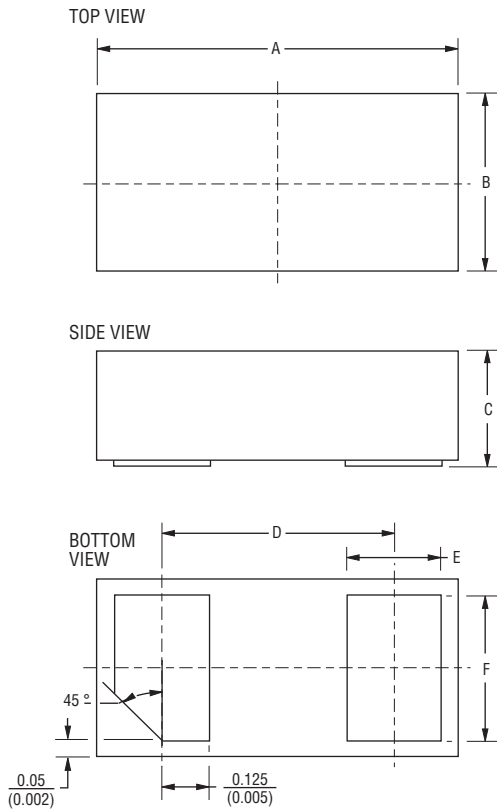
The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time. Users should verify actual device performance in their specific applications.

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Product Dimensions

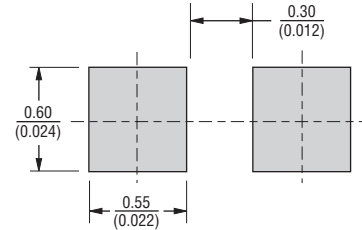
This is a Molded DFN-2 package with lead free 100 % Sn plating on the terminations. It weighs approximately 30 mg.



Symbol	Dimensions		
	Min.	Nom.	Max.
A	$\frac{0.95}{(0.037)}$	$\frac{1.00}{(0.039)}$	$\frac{1.05}{(0.041)}$
B	$\frac{0.55}{(0.022)}$	$\frac{0.60}{(0.024)}$	$\frac{0.65}{(0.026)}$
C	$\frac{0.41}{(0.016)}$	$\frac{0.45}{(0.018)}$	$\frac{0.50}{(0.020)}$
D	$\frac{0.65}{(0.026)}$		
E	$\frac{0.20}{(0.008)}$	$\frac{0.25}{(0.010)}$	$\frac{0.30}{(0.012)}$
F	$\frac{0.45}{(0.018)}$	$\frac{0.50}{(0.020)}$	$\frac{0.55}{(0.022)}$

DIMENSIONS: $\frac{\text{MM}}{\text{(INCHES)}}$

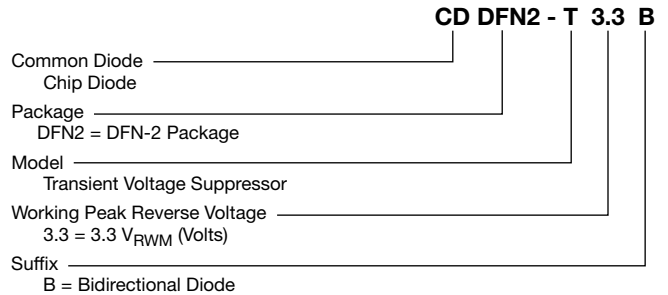
Recommended PCB Footprint



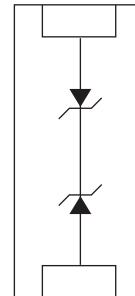
Typical Part Marking

CDDFN2-T3.3B Y

How to Order



Block Diagram



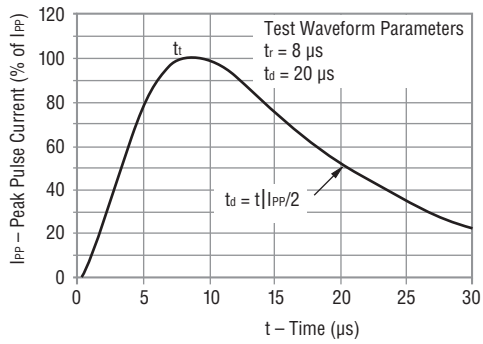
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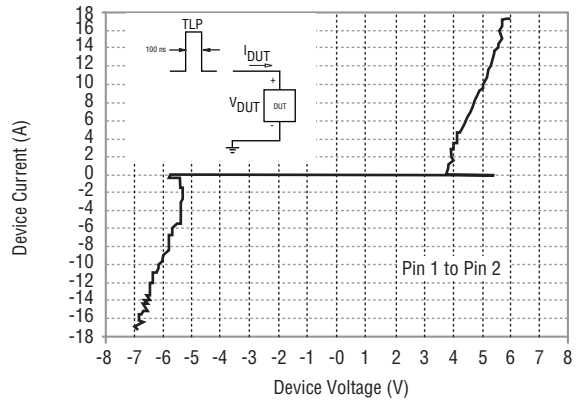
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Performance Graphs

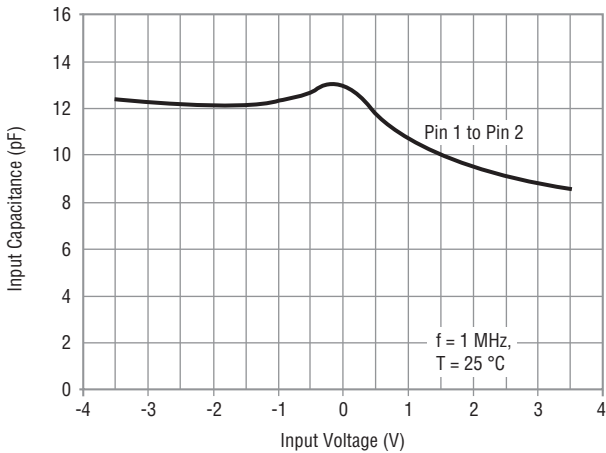
Pulse Waveform



TLP I-V Plot



Typical Capacitance Variation of C_{IN} vs V_{IN}



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