

AN4210 Application note

PowerSSO-12 devices evaluation board

Introduction

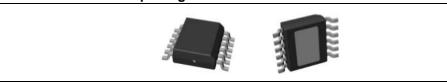
The EV board concept is intended to provide customers with an easy way to connect ST's surface mounted VIPower[®] drivers into their existing prototype circuitry. The EV boards come pre-assembled with one of ST's VIPower high-side drivers.

Figure 1. PowerSSO-12 evaluation board



These boards provide mounting solutions and some heat sinking capability for prototype development. There are still external components required to make these devices work in any application.

Figure 2. PowerSSO-12 package



This document provides application schematics for the following evaluation boards:

Table 1. EV board

12 V analog devices	12 V digital devices	24 V system device
EV-VN5016AJ	EV-VN5E050J	EV-VND5T100AJ
EV-VN5E025AJ	EV-VND5E050J	
EV-VN5E050AJ	EV-VND5E160J	
EV-VND5E050AJ		
EV-VND5E160AJ		

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1 Application schematics

The evaluation boards themselves do not have any components other than the high-side driver that is mounted on it. The schematics provided in this document are recommended for their proper functionality. All designs have different needs and requirements. Whatever design you decide to use, it will still need to be verified in order to meet your application specifications. ST implies no guarantee or warranty (see *Appendix A: Reference documents*).

Note:

When more than one pin is used for an output, all output pins must be tied together for proper operation.

ST has produced two user manuals: the UM1556 (for 12 V devices) and the UM1557 (for 24 V devices) (see *Appendix A: Reference documents*). Those user manuals are VIPower hardware design guides that provide the necessary information to successfully design your circuit using our VIPower drivers.

1.1 Digital feedback designs

These devices have a logic level fault pin that is pulled low when a fault is detected and the status disable (STAT_DIS) pin is held low. The feedback is an open Drain configuration and requires a pull-up resistor for proper operation. For this package type (PowerSSO-12) there are single, and dual output devices.

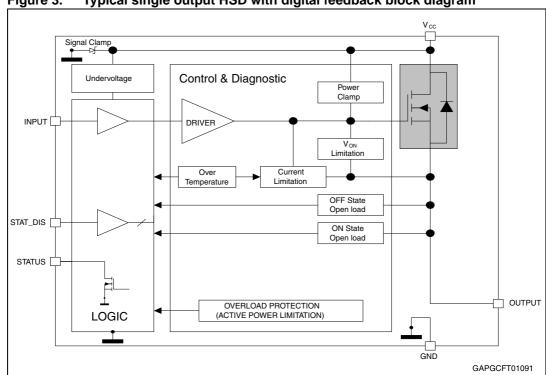
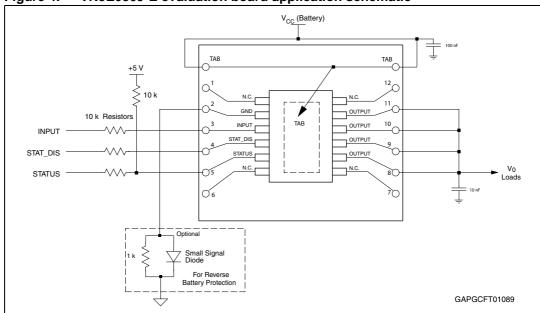


Figure 3. Typical single output HSD with digital feedback block diagram

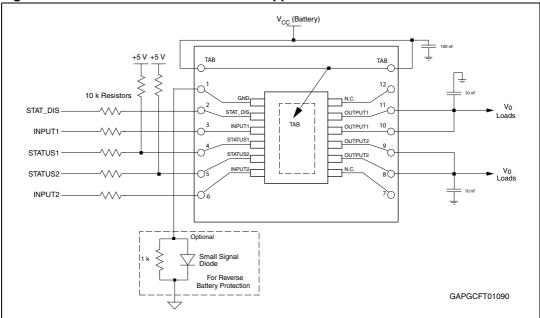
1.1.1 VN5E050J-E application schematic

Figure 4. VN5E050J-E evaluation board application schematic



1.1.2 VND5E160J-E application schematic

Figure 5. VND5E160J evaluation board application schematic



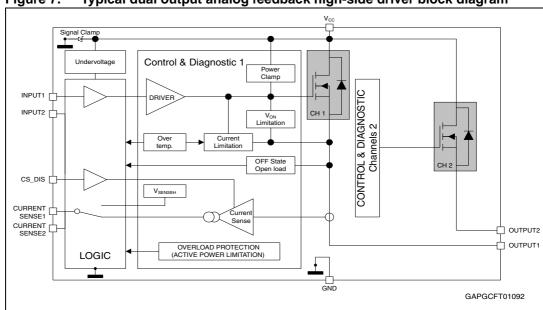
1.1.3 VND5E050J application schematic

Figure 6. V_{CC} (Battery) TAB TAB 10 k Resistors 110 STAT_DIS 10 OUTPUT1 INPUT1 INPUT1 OUTPUT2 STATUS1 OUTPUT2 V₀ Loads INPUT2 Small Signal Diode GAPGCFT01097

VND5E050J evaluation board application schematic

Analog feedback drivers 1.2

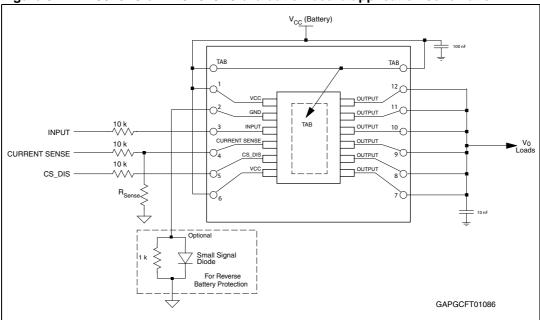
Analog feedback devices have a sense resistor that, when properly scaled, provides a reasonable representation of the output current in terms of voltage that can be measured as well as its diagnostic information. How to properly scale that resistor can be found in the user manual UM1556 (see Appendix A: Reference documents).



Typical dual output analog feedback high-side driver block diagram Figure 7.

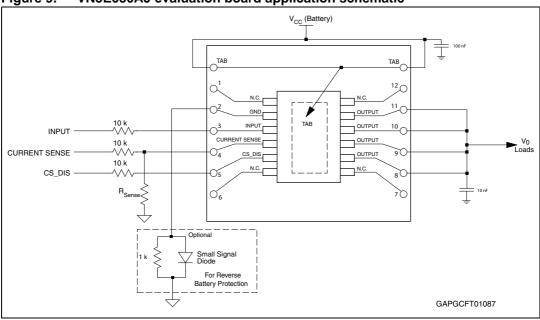
VN5016AJ-E / VN5E025AJ application schematic 1.2.1

Figure 8. VN5016AJ or VN5E025AJ evaluation board application schematic



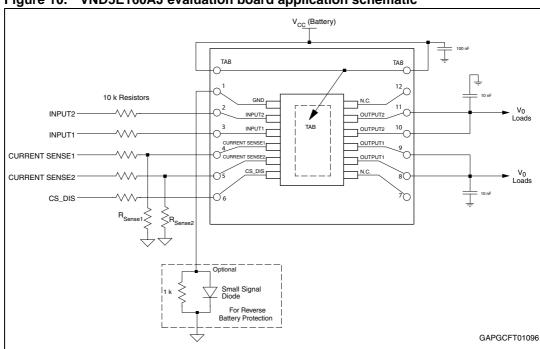
VN5E050AJ application schematic 1.2.2





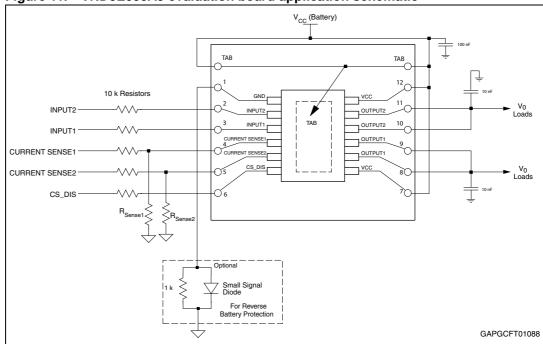
1.2.3 VND5E160AJ application schematic

Figure 10. VND5E160AJ evaluation board application schematic



1.2.4 VND5E050AJ application schematic

Figure 11. VND5E050AJ evaluation board application schematic



1.3 24 V system devices

1.3.1 VND5T100AJ application schematic

The VND5T100AJ is a 24 V system device. The maximum voltage rating for this device is elevated from the standard 41 V to 58 V. 24 V products are equipped with a latch-off feature keeping the device switched-off once power limitation or thermal shutdown is triggered. This latch can be activated by holding the FR_STBY pin high. Resetting the device to retry the load requires pulling the FR_STBY pin low. Leaving the FR_STBY pin low disables the latch off feature and lets the part automatically retry shorted loads. More details can be found in the user manual UM1557 (see *Appendix A: Reference documents*).

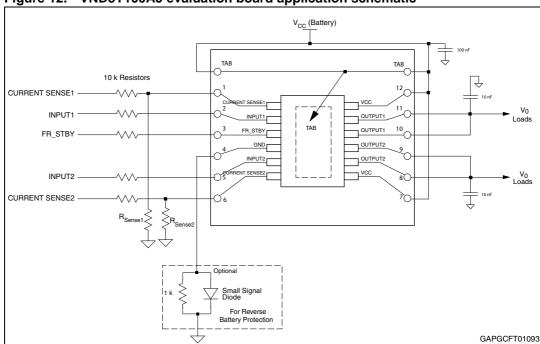


Figure 12. VND5T100AJ evaluation board application schematic

Reference documents AN4210

Appendix A Reference documents

- 1. VN5016AJ evaluation board (EV-VN5016AJ, DocID 023957)
- 2. VN5E025AJ evaluation board (EV-VN5E025AJ, DocID 023956)
- 3. VN5E050AJ evaluation board (EV-VN5E050AJ, DocID 023952)
- 4. VND5E050AJ evaluation board (EV-VND5E050AJ, DocID 023958)
- 5. VND5E160AJ evaluation board (EV-VND5E160AJ, DocID 023959)
- 6. VN5E050J evaluation board (EV-VN5E050J, DocID 023960)
- 7. VND5E050J evaluation board (EV-VND5E050J, DocID 023961)
- 8. VND5E160J evaluation board (EV-VND5E160J, DocID 023962)
- 9. VND5T100AJ evaluation board (EV-VND5T100AJ, DocID 023967)
- 10. VIPower M0-5 and M0-5Enhanced high-side drivers (UM1556, DocID 023520)
- 11. VIPower MO-5T: high-side switches for 24V systems (UM1557, DocID 023521)
- 12. Evaluation Product Licence Agreement on www.st.com

AN4210 Revision history

Revision history

Table 2. Document revision history

Date	Revision	Changes
03-Dec-2012	1	Initial release.

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