

iscN-Channel MOSFET Transistor

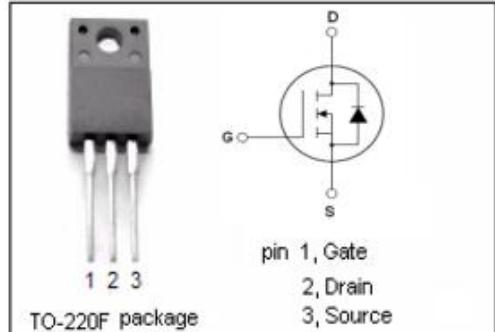
2SK3565, I2SK3565

• FEATURES

- Low drain-source on-resistance:
 $R_{DS(ON)} = 2.0\Omega$ (typ.)
- Enhancement mode:
 $V_{th} = 2.0$ to $4.0V$ ($V_{DS} = 10 V$, $I_D = 1.0mA$)
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

• DESCRIPTION

- Switching Voltage Regulators

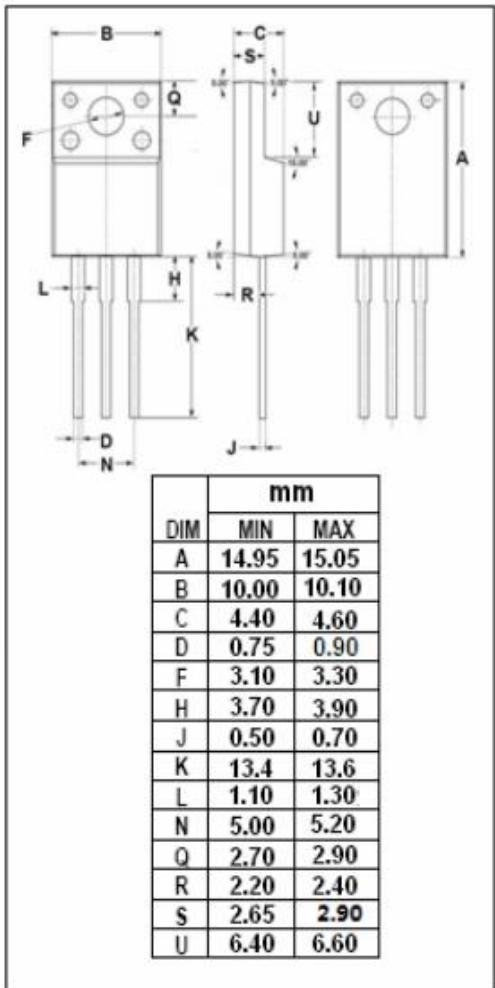


• ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DSS}	Drain-Source Voltage	900	V
V_{GS}	Gate-Source Voltage	± 30	V
I_D	Drain Current-Continuous	5	A
I_{DM}	Drain Current-Single Pulsed	15	A
P_D	Total Dissipation @ $T_c=25^\circ C$	45	W
T_j	Max. Operating Junction Temperature	150	$^\circ C$
T_{stg}	Storage Temperature	-55~150	$^\circ C$

• THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	2.78	$^\circ C/W$
$R_{th(ch-a)}$	Channel-to-ambient thermal resistance	62.5	$^\circ C/W$



iscN-Channel MOSFET Transistor**2SK3565, I2SK3565****ELECTRICAL CHARACTERISTICS** $T_c=25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}; \text{I}_D= 10\text{mA}$	900			V
$\text{V}_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}= 10\text{V}; \text{I}_D=1.0\text{mA}$	2.0		4.0	V
$\text{R}_{\text{DS}(\text{on})}$	Drain-Source On-Resistance	$\text{V}_{\text{GS}}=10\text{V}; \text{I}_D=3\text{A}$		2000	2500	$\text{m}\Omega$
I_{GSS}	Gate-Source Leakage Current	$\text{V}_{\text{GS}}= \pm 25\text{V}; \text{V}_{\text{DS}}= 0\text{V}$			± 10	μA
I_{DSS}	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=720\text{V}; \text{V}_{\text{GS}}= 0\text{V}$			100	μA
V_{SDF}	Diode forward voltage	$\text{I}_{\text{DR}} =5\text{A}, \text{V}_{\text{GS}} = 0 \text{ V}$			1.7	V