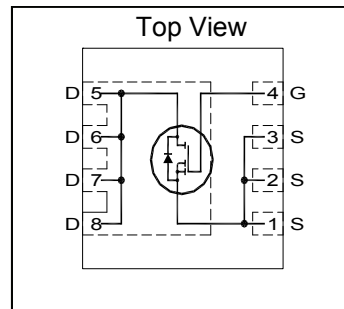


HEXFET® Power MOSFET

V_{DSS}	25	V
$R_{DS(on) \max}$ (@ $V_{GS} = 10V$)	4.4	mΩ
(@ $V_{GS} = 4.5V$)	7.1	
Qg (typical)	8.2	nC
I_D (@ T_C (Bottom) = 25°C)	60 ^⑦	A



Applications

- Control MOSFET for synchronous buck converter

Features

Low Charge (typical 8.2 nC)
Low $R_{DS(on)}$ (<4.4 mΩ)
Low Thermal Resistance to PCB (<4.4°C/W)
Low Profile (<0.9 mm)
Industry-Standard Pinout
Compatible with Existing Surface Mount Techniques
RoHS Compliant, Halogen-Free
MSL1, Industrial Qualification

results in

⇒

Benefits

Low Switching Losses
Lower Conduction Losses
Enable better Thermal Dissipation
Increased Power Density
Multi-Vendor Compatibility
Easier Manufacturing
Environmentally Friendlier
Increased Reliability

Base part number	Package Type	Standard Pack		Orderable Part Number
		Form	Quantity	
IRFHM4234PbF	PQFN 3.3mm x 3.3mm	Tape and Reel	4000	IRFHM4234TRPbF

Absolute Maximum Ratings

	Parameter	Max.	Units
V_{GS}	Gate-to-Source Voltage	± 20	V
I_D @ $T_A = 25^\circ C$	Continuous Drain Current, V_{GS} @ 10V	20	A
I_D @ $T_{C(Bottom)} = 25^\circ C$	Continuous Drain Current, V_{GS} @ 10V	63 ^{⑥⑦}	
I_D @ $T_{C(Bottom)} = 100^\circ C$	Continuous Drain Current, V_{GS} @ 10V	44 ^⑥	
I_D @ $T_C = 25^\circ C$	Continuous Drain Current, V_{GS} @ 10V (Source Bonding Technology Limited)	60 ^⑦	
I_{DM}	Pulsed Drain Current ^①	270	
P_D @ $T_A = 25^\circ C$	Power Dissipation ^⑤	2.8	W
P_D @ $T_{C(Bottom)} = 25^\circ C$	Power Dissipation	28	
	Linear Derating Factor	0.022	W/°C
T_J T_{STG}	Operating Junction and Storage Temperature Range	-55 to + 150	°C

Notes ^① through ^③ are on page 9

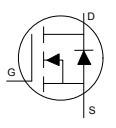
Static @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
BV _{DSS}	Drain-to-Source Breakdown Voltage	25	—	—	V	V _{GS} = 0V, I _D = 250μA
ΔBV _{DSS} /ΔT _J	Breakdown Voltage Temp. Coefficient	—	21	—	mV/°C	Reference to 25°C, I _D = 1mA
R _{DS(on)}	Static Drain-to-Source On-Resistance	—	3.5	4.4	mΩ	V _{GS} = 10V, I _D = 30A ③
		—	5.6	7.1		V _{GS} = 4.5V, I _D = 30A ③
V _{GS(th)}	Gate Threshold Voltage	1.1	1.6	2.1	V	V _{DS} = V _{GS} , I _D = 25μA
ΔV _{GS(th)}	Gate Threshold Voltage Coefficient	—	-5.5	—	mV/°C	
I _{DSS}	Drain-to-Source Leakage Current	—	—	1.0	μA	V _{DS} = 20V, V _{GS} = 0V
I _{GSS}	Gate-to-Source Forward Leakage	—	—	100	nA	V _{GS} = 20V
	Gate-to-Source Reverse Leakage	—	—	-100		V _{GS} = -20V
g _{fs}	Forward Transconductance	60	—	—	S	V _{DS} = 5.0V, I _D = 30A
Q _g	Total Gate Charge	—	17	—	nC	V _{GS} = 10V, V _{DS} = 13V, I _D = 30A
Q _g	Total Gate Charge	—	8.2	12.3	nC	V _{DS} = 13V V _{GS} = 4.5V I _D = 30A
Q _{gs1}	Pre-V _{th} Gate-to-Source Charge	—	1.6	—		
Q _{gs2}	Post-V _{th} Gate-to-Source Charge	—	1.6	—		
Q _{gd}	Gate-to-Drain Charge	—	3.1	—		
Q _{godr}	Gate Charge Overdrive	—	1.9	—		
Q _{sw}	Switch Charge (Q _{gs2} + Q _{gd})	—	4.7	—		
Q _{oss}	Output Charge	—	7.7	—	nC	V _{DS} = 16V, V _{GS} = 0V
R _G	Gate Resistance	—	1.8	—	Ω	
t _{d(on)}	Turn-On Delay Time	—	7.8	—	ns	V _{DD} = 13V, V _{GS} = 4.5V I _D = 30A R _G = 1.8Ω
t _r	Rise Time	—	30	—		
t _{d(off)}	Turn-Off Delay Time	—	8.0	—		
t _f	Fall Time	—	5.3	—		
C _{iss}	Input Capacitance	—	1011	—	pF	V _{GS} = 0V V _{DS} = 13V f = 1.0MHz
C _{oss}	Output Capacitance	—	286	—		
C _{rss}	Reverse Transfer Capacitance	—	83	—		

Avalanche Characteristics

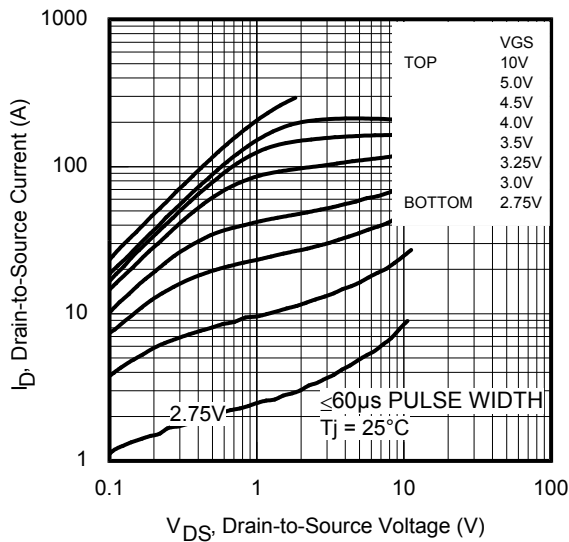
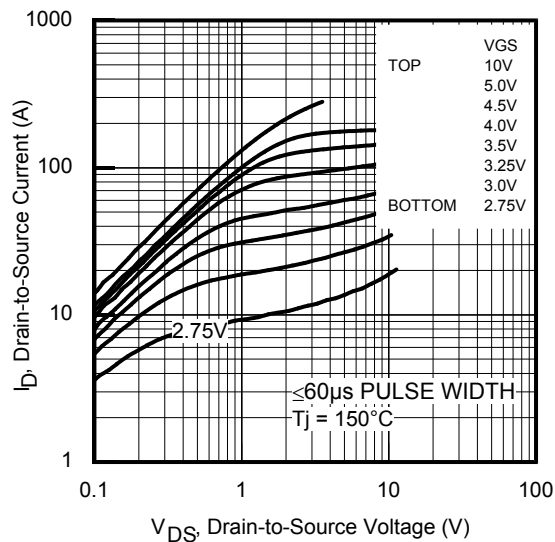
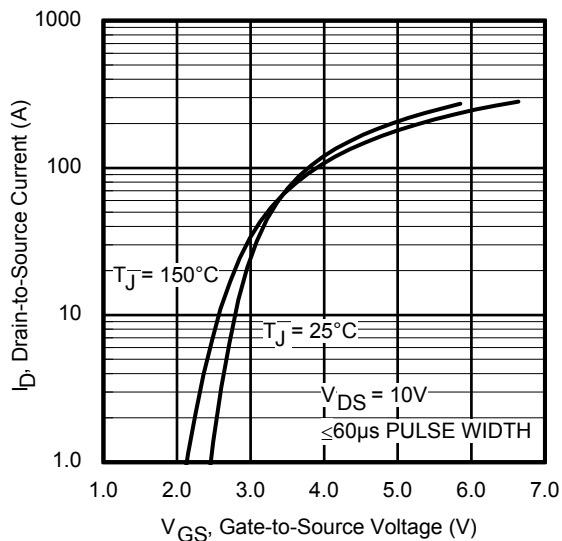
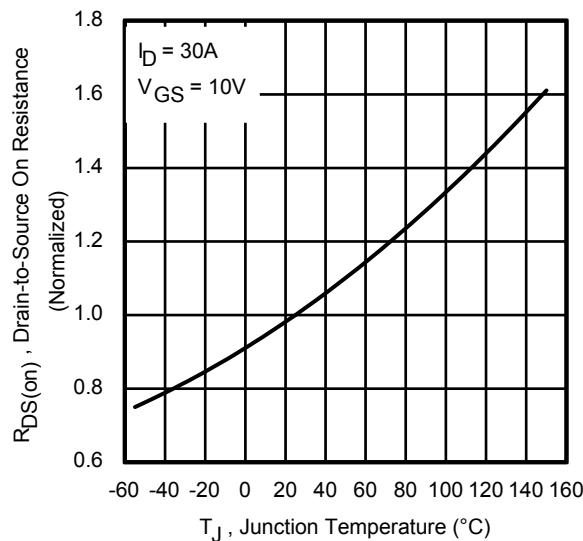
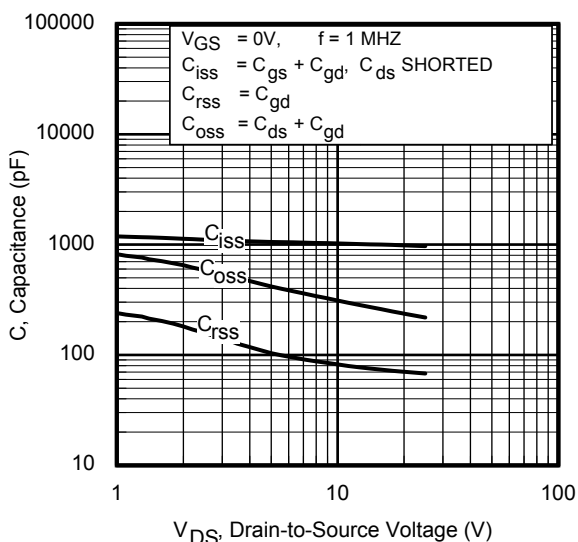
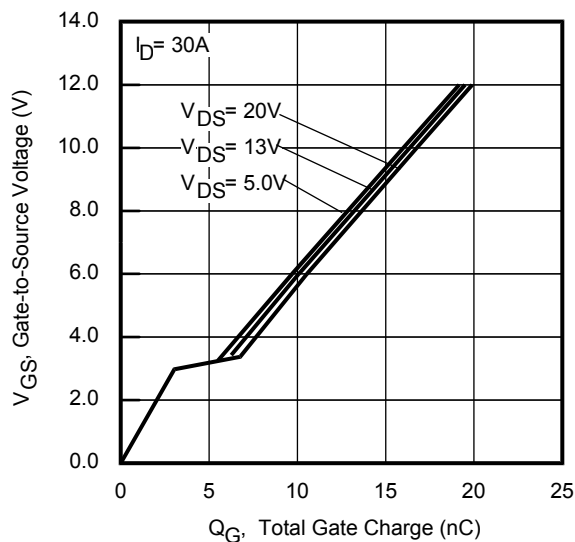
	Parameter	Typ.	Max.
E _{AS}	Single Pulse Avalanche Energy ②	—	39
I _{AR}	Avalanche Current ①	—	30

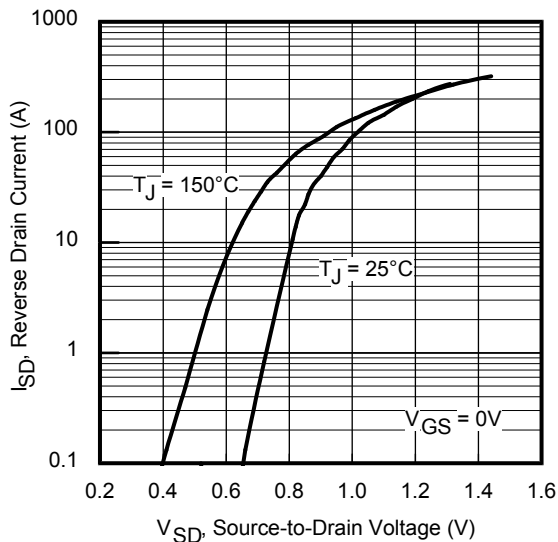
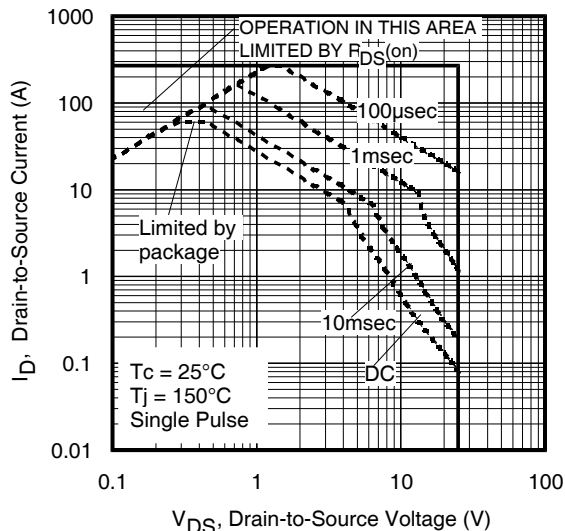
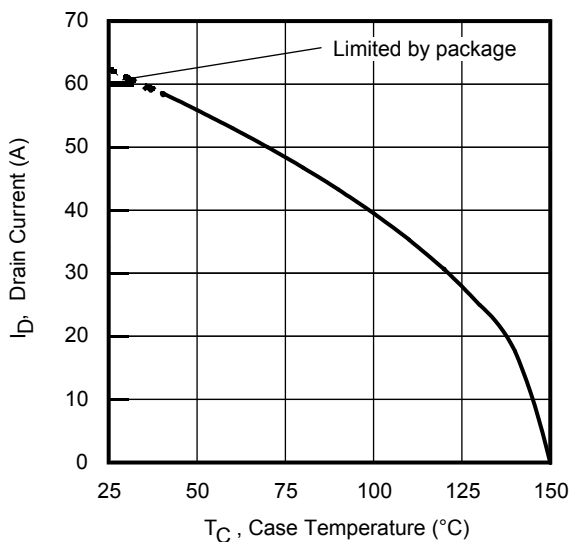
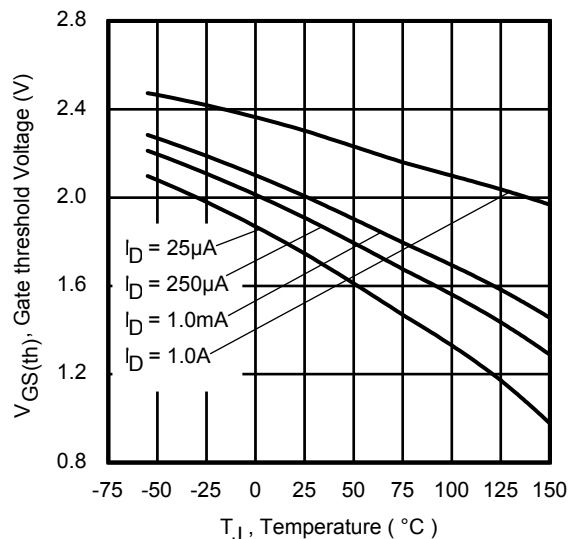
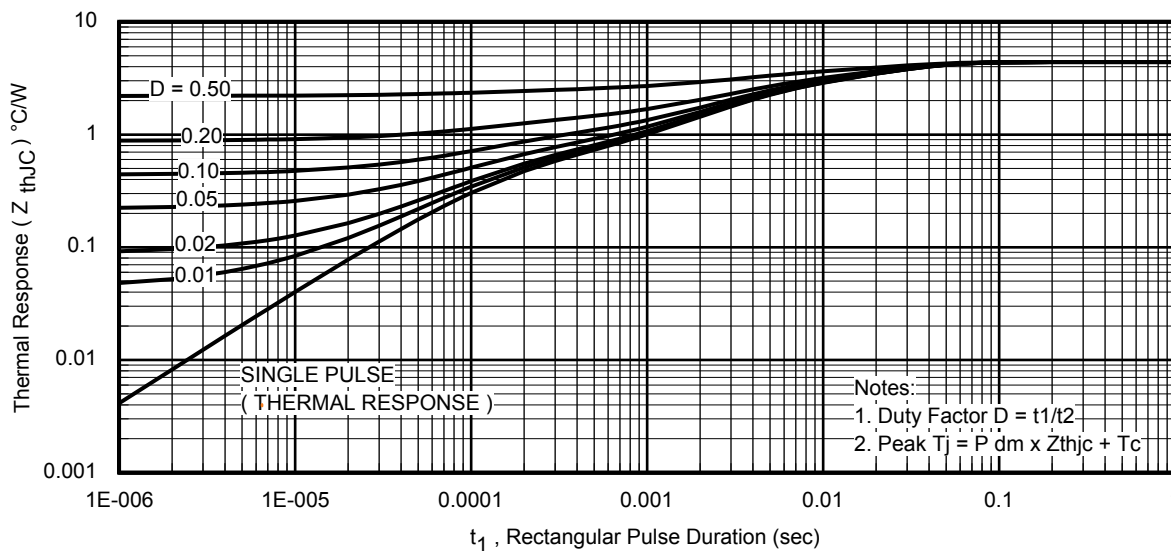
Diode Characteristics

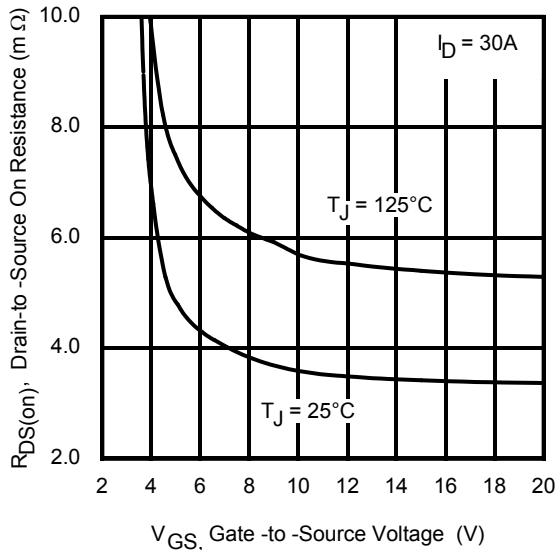
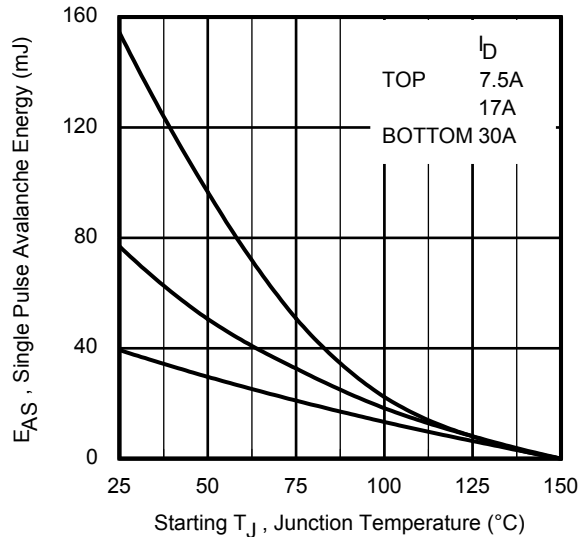
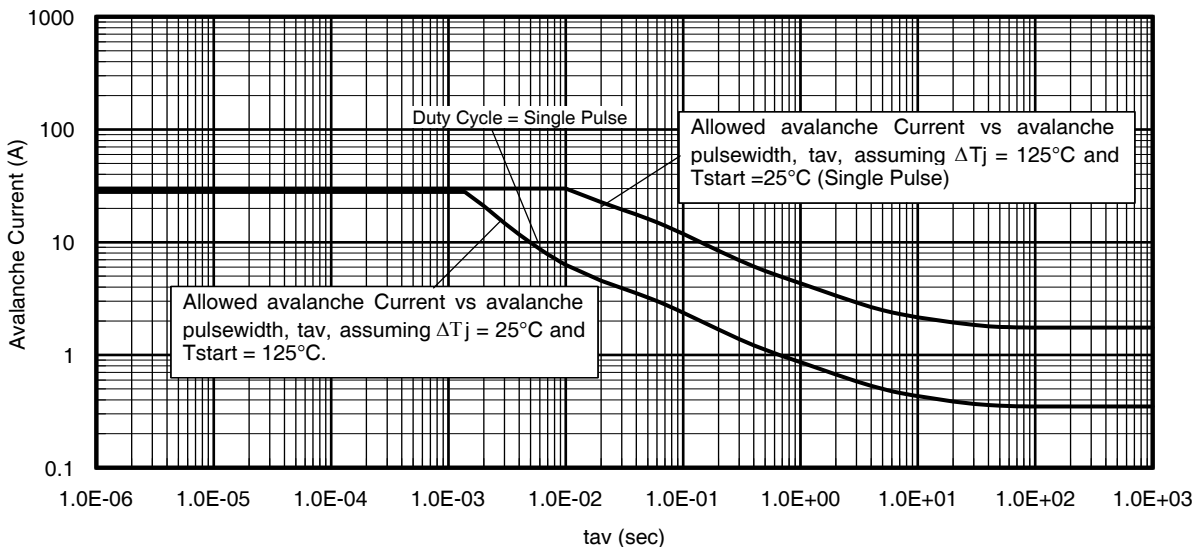
	Parameter	Min.	Typ.	Max.	Units	Conditions
I _S	Continuous Source Current (Body Diode)	—	—	60⑦	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I _{SM}	Pulsed Source Current (Body Diode) ①	—	—	270		
V _{SD}	Diode Forward Voltage	—	—	1.0	V	T _J = 25°C, I _S = 30A, V _{GS} = 0V ③
t _{rr}	Reverse Recovery Time	—	10	15	ns	T _J = 25°C, I _F = 30A, V _{DD} = 13V
Q _{rr}	Reverse Recovery Charge	—	11	17	nC	di/dt = 200A/μs ③

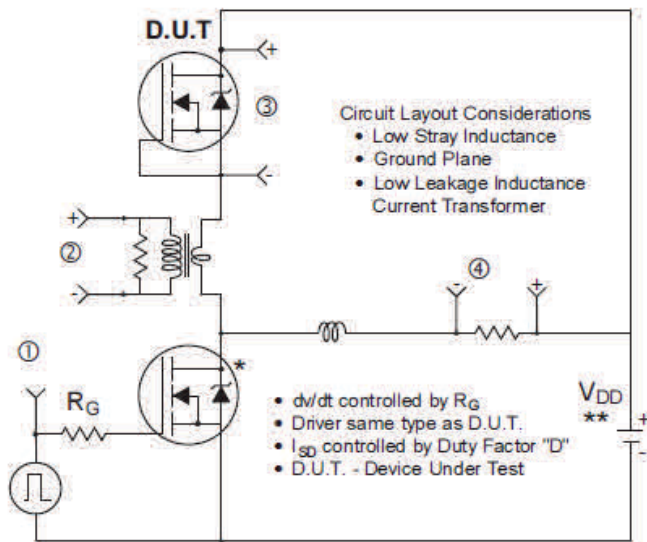
Thermal Resistance

	Parameter	Typ.	Max.	Units
R _{θJC} (Bottom)	Junction-to-Case ④	—	4.4	°C/W
R _{θJC} (Top)	Junction-to-Case ④	—	40	
R _{θJA}	Junction-to-Ambient ⑤	—	45	
R _{θJA} (<10s)	Junction-to-Ambient ⑤	—	31	


Fig 1. Typical Output Characteristics

Fig 2. Typical Output Characteristics

Fig 3. Typical Transfer Characteristics

Fig 4. Normalized On-Resistance vs. Temperature

Fig 5. Typical Capacitance vs. Drain-to-Source Voltage

Fig 6. Typical Gate Charge vs. Gate-to-Source Voltage

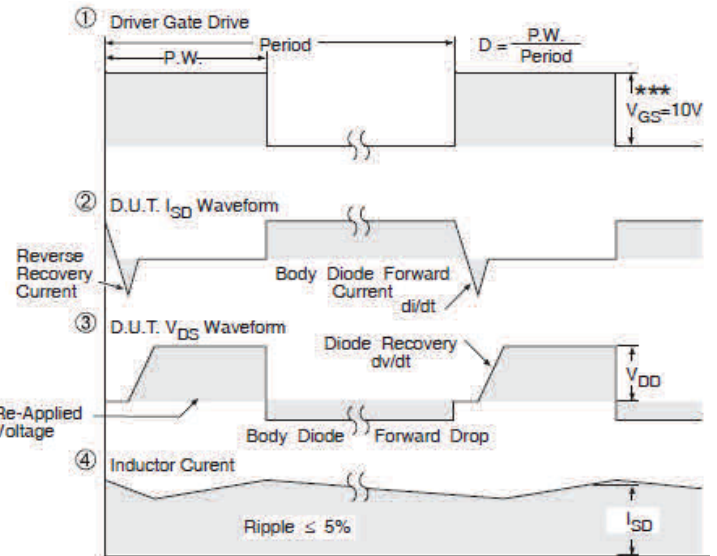

Fig 7. Typical Source-Drain Diode Forward Voltage

Fig 8. Maximum Safe Operating Area

Fig 9. Maximum Drain Current vs. Case Temperature

Fig 10. Drain-to-Source Breakdown Voltage

Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case


Fig 12. On-Resistance vs. Gate Voltage

Fig 13. Maximum Avalanche Energy vs. Drain Current

Fig 14. Typical Avalanche Current vs. Pulsewidth



* Use P-Channel Driver for P-Channel Measurements
 ** Reverse Polarity for P-Channel

Fig 15. Peak Diode Recovery dv/dt Test Circuit for N-Channel HEXFET® Power MOSFETs



*** $V_{GS} = 5V$ for Logic Level Devices

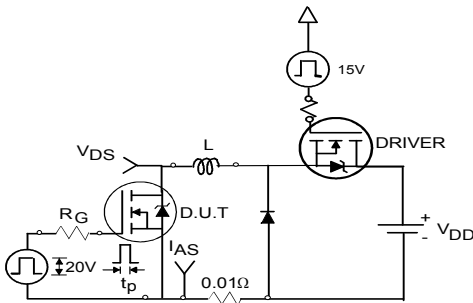


Fig 16a. Unclamped Inductive Test Circuit

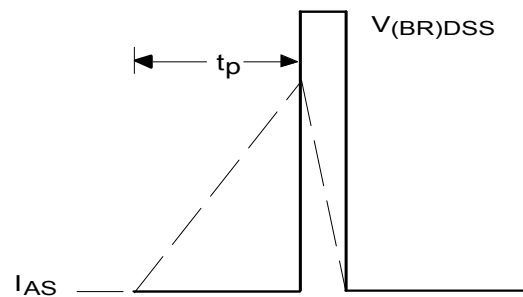


Fig 16b. Unclamped Inductive Waveforms

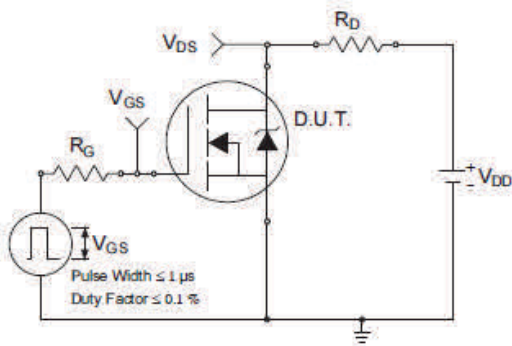


Fig 17a. Switching Time Test Circuit

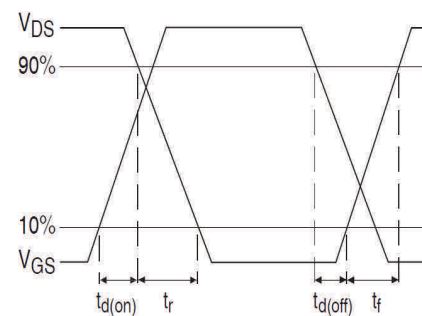


Fig 17b. Switching Time Waveforms

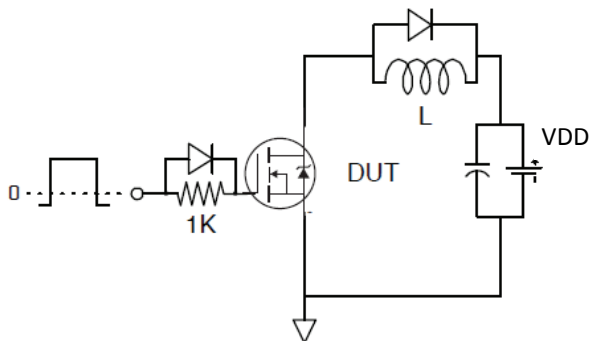


Fig 18. Gate Charge Test Circuit

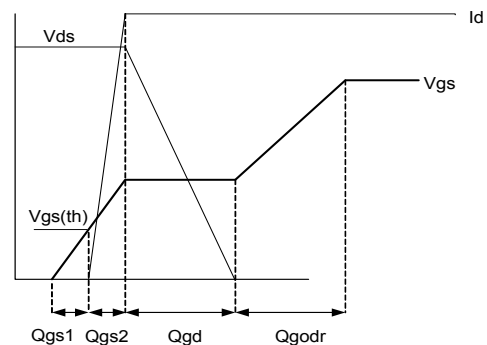
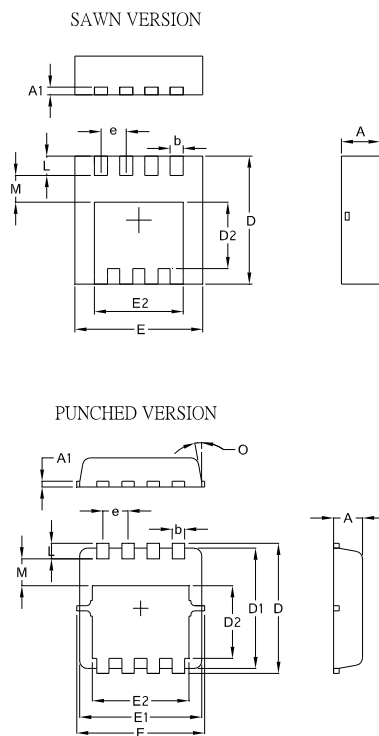


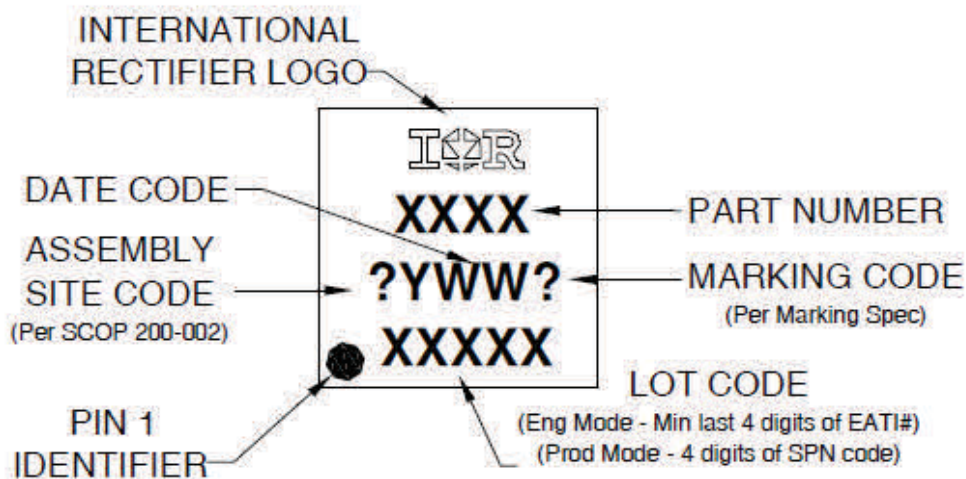
Fig 19. Gate Charge Waveform

PQFN 3.3 x 3.3 Package Details


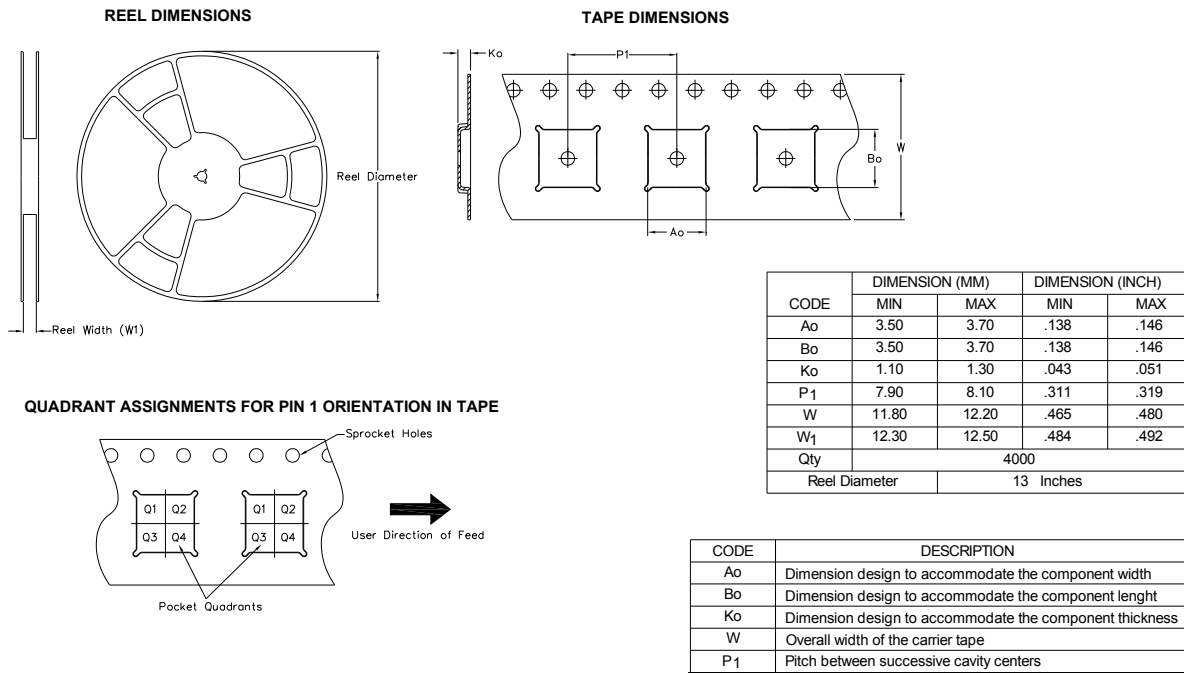
SYMBOL	COMMON			
	MM		INCH	
	MIN.	MAX.	MIN.	MAX.
A	0.70	1.05	0.0276	0.0413
A1	0.12	0.39	0.0047	0.0154
b	0.25	0.39	0.0098	0.0154
D	3.20	3.45	0.1260	0.1358
D1	3.00	3.20	0.1181	0.1417
D2	1.69	2.20	0.0665	0.0866
E	3.20	3.40	0.1260	0.1339
E1	3.00	3.20	0.1181	0.1417
E2	2.15	2.59	0.0846	0.1020
e	0.65 BSC		0.0256 BSC	
L	0.15	0.55	0.0059	0.0217
M	0.59	—	0.0232	—
O	9Deg	12Deg	9Deg	12Deg

For more information on board mounting, including footprint and stencil recommendation, please refer to application note AN-1136: <http://www.irf.com/technical-info/appnotes/an-1136.pdf>

For more information on package inspection techniques, please refer to application note AN-1154: <http://www.irf.com/technical-info/appnotes/an-1154.pdf>

PQFN 3.3 x 3.3 Part Marking


Note: For the most current drawing please refer to IR website at <http://www.irf.com/package/>

PQFN 3.3mm x 3.3mm Outline Tape and Reel


Note: For the most current drawing please refer to IR website at <http://www.irf.com/package/>

Qualification Information[†]

Qualification Level	Industrial (per JEDEC JESD47F ^{††} guidelines)	
Moisture Sensitivity Level	PQFN 3.3mm x 3.3mm	MSL1 (per JEDEC J-STD-020D ^{††})
RoHS Compliant	Yes	

† Qualification standards can be found at International Rectifier’s web site: <http://www.irf.com/product-info/reliability>

†† Applicable version of JEDEC standard at the time of product release.

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Starting $T_J = 25^\circ\text{C}$, $L = 0.087\text{mH}$, $R_G = 50\Omega$, $I_{AS} = 30\text{A}$.
- ③ Pulse width $\leq 400\mu\text{s}$; duty cycle $\leq 2\%$.
- ④ R_θ is measured at T_J of approximately 90°C .
- ⑤ When mounted on 1 inch square PCB (FR-4). Please refer to AN-994 for more details:
<http://www.irf.com/technical-info/appnotes/an-994.pdf>
- ⑥ Calculated continuous current based on maximum allowable junction temperature.
- ⑦ Current is limited to 60A by source bonding technology for 1 inch square FR-4, or 85A for large area 6 oz. copper on a large area copper Insulated Metal Substrate (IMS).

Revision History

Date	Comments
6/21/2013	<ul style="list-style-type: none"> • Updated figure 10 ID label from 1.0mA to 1.0A, on page 4.
8/15/2013	<ul style="list-style-type: none"> • Added “Fast/RFET™” above the part number, on page 1.
6/6/2014	<ul style="list-style-type: none"> • Updated schematic on page 1. • Updated tape and reel on page 8.
7/24/14	<ul style="list-style-type: none"> • Updated $I_d @ T_c 25\text{C}$ from “40A” to “60A”-pg1& 2. • Updated $I_d @ T_c$ (bottom) 100c from “40A” to “44A”-pg1. • Updated fig 8 & 9 on page 4. • Updated note7 on page 9.