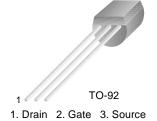


KSK117

Low Requency Low Noise AMP

- High | Y_{FS} |: 15mS (TYP)
 High Input Impedance: I_{GSS}= -1nA
 Low Noise, NF =1dB (TYP)



Silicon N-channel Junction Fet

Absolute Maximum Ratings Ta=25°C unless otherwise noted

Symbol	Parameter	Ratings	Units
V _{GDS}	Gate-Drain Voltage	-50	V
I _G	Gate-Current	10	mA
P _C	Collector Power Dissipation	300	mW
T _J	Junction Temperature	125	°C
T _{STG}	Storage Temperature	-55 ~ 125	°C

$\textbf{Electrical Characteristics} \ \, \textbf{T}_{a} \!\!=\!\! 25^{\circ} \textbf{C} \ \, \textbf{unless otherwise noted}$

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{GDS}	Gate-Drain Breakdown Voltage	V _{DS} =0, I _G = -100μA	-50			V
I _{GSS}	Gate Leak Current	V _{GS} = -30V, V _{DS} =0			-1	nA
I _{DSS}	Drain Leak Current	V _{DS} =10V, V _{GS} =0	0.6		14	mA
V _{GS} (off)	Gate-Source Voltage	V _{DS} =10V, I _D =0.1μA	-0.2		-1.5	V
Y _{FS}	Forward Transfer Admittance	V _{DS} =10V, V _{GS} =0, f=1KHz	4.0	15		mS
C _{ISS}	Input Capacitance	V _{DS} =0, V _{GS} =0, f=1MHz		13		pF
C _{RSS}	Feedback Capacitance	V _{GD} =10V, V _{DS} =0 f=1MHz		3		pF
NF1	Noise Figure	$V_{DS}=10V, R_{G}=1K\Omega$ $I_{D}=0.5mA, f=10Hz$		5	10	dB
NF2		V_{DS} =10V, R_{G} =1K Ω I_{D} =0.5mA, f=1Hz		1	2	dB

I_{DSS} Classification

Classification	Y	G	L
I _{DSS} (mA)	1.2 ~ 3.0	2.6 ~ 6.5	6.0 ~ 14

Typical Characteristics

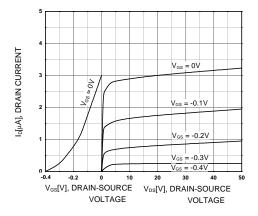


Figure 1. Static Characteristic

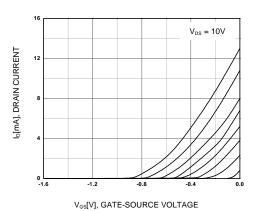


Figure 3. I_D-V_{GS}

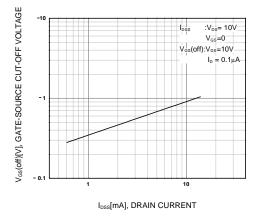


Figure 5. $V_{GS}(off)$ - I_{DSS}

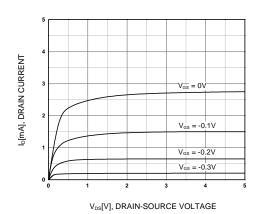


Figure 2. I_D - V_{DS}

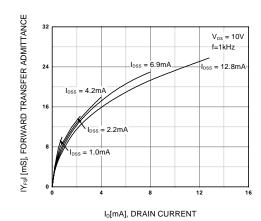


Figure 4. |Yfs |-I_D

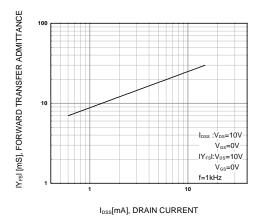


Figure 6. | Yfs | -I_{DSS}

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 $V_{DS} = 10V$

 $R_G = 1k\Omega$

Typical Characteristics (Continued)

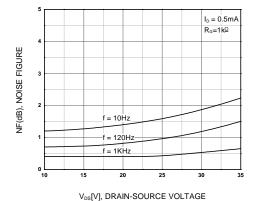


Figure 7. NF-V_{DS}

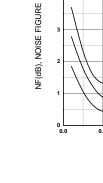
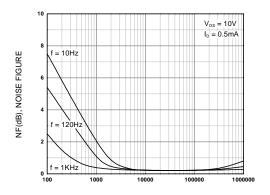


Figure 8. NF-I_D

I_D[mA], DRAIN CURRENT

f = 120Hz



 $R_{\text{\tiny G}}[\!\Omega\!]$], SOURCE RESISTANCE

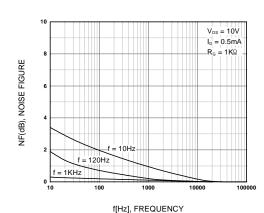


Figure 10. NF-f



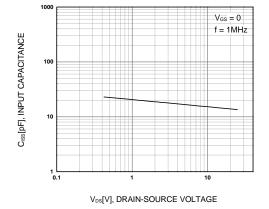


Figure 11. Ciss-V_{DS}

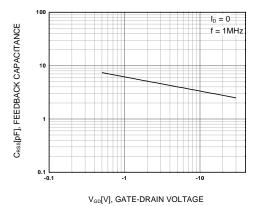


Figure 12. Crss-V_{GD}

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Typical Characteristics (Continued)

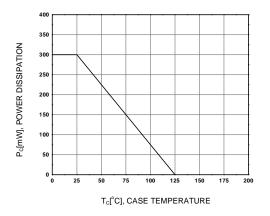
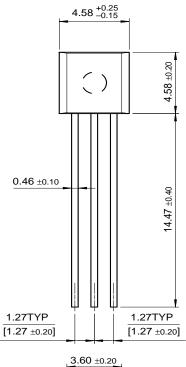


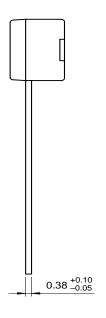
Figure 13. Power Derating

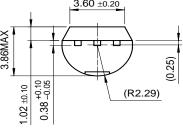
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Package Demensions

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