

1W, Fixed input voltage, isolated & unregulated dual output



Patent Protection RoHS

A_D-1WR2 series is specially designed for applications where an isolated voltage is required in a distributed power supply system. It is suitable for

1. Where the voltage of the input power supply is stable (voltage variation: $\pm 10\%V_{in}$);
2. Where isolation is necessary between input and output (isolation voltage $\leq 1500VDC$);
3. Where do not has high requirement of line regulation and load regulation;
4. Such as: pure digital circuits, low frequency analog circuits, relay-driven circuits and data switching circuits.

FEATURES

- DIP package
- Efficiency up to 81%
- Operating temperature range: -40°C to +105°C
- Isolation voltage: 1.5KVDC
- Continuous short circuit protection
- No external component required
- International standard pin-out

Selection Guide

Part No.	Input Voltage (VDC)	Output		Efficiency (%Min./Typ.) @ Full Load	Max. Capacitive Load * (μF)
		Output Voltage (VDC)	Output Current (mA) (Max./Min.)		
A0505D-1WR2	4.5-5.5 (5VDC nominal)	± 5	$\pm 100/\pm 10$	76/80	100
A0509D-1WR2		± 9	$\pm 56/\pm 6$	76/80	
A0512D-1WR2		± 12	$\pm 42/\pm 5$	77/81	
A0515D-1WR2		± 15	$\pm 34/\pm 4$	77/81	
A0524D-1WR2		± 24	$\pm 21/\pm 3$	77/81	
A1205D-1WR2	10.8-13.2 (12VDC nominal)	± 5	$\pm 100/\pm 10$	76/80	100
A1209D-1WR2		± 9	$\pm 56/\pm 6$	76/80	
A1212D-1WR2		± 12	$\pm 42/\pm 5$	77/81	
A1215D-1WR2		± 15	$\pm 34/\pm 4$	77/81	
A1224D-1WR2		± 24	$\pm 21/\pm 3$	77/81	
A1505D-1WR2	13.5-16.5 (15VDC nominal)	± 5	$\pm 100/\pm 10$	76/80	100
A1509D-1WR2		± 9	$\pm 56/\pm 6$	76/80	
A1512D-1WR2		± 12	$\pm 42/\pm 5$	77/81	
A1515D-1WR2		± 15	$\pm 34/\pm 4$	77/81	
A1524D-1WR2		± 24	$\pm 21/\pm 3$	77/81	
A2405D-1WR2	21.6-26.4 (24VDC nominal)	± 5	$\pm 100/\pm 10$	76/80	100
A2409D-1WR2		± 9	$\pm 56/\pm 6$	76/80	
A2412D-1WR2		± 12	$\pm 42/\pm 5$	77/81	
A2415D-1WR2		± 15	$\pm 34/\pm 4$	77/81	
A2424D-1WR2		± 24	$\pm 21/\pm 3$	77/81	

Note: *The capacitive loads of positive and negative outputs are the same.

Input Specifications

Item	Operating Conditions	Min.	Typ.	Max.	Unit
Input Current (full load / no-load)	5V input	--	256/25	--	mA
	12V input	--	106/15	--	
	15V input	--	84/10	--	
	24V input	--	54/7	--	

Surge Voltage (1sec. max.)	5V input	-0.7	--	9	VDC
	12V input	-0.7	--	18	
	15V input	-0.7	--	21	
	24V input	-0.7	--	30	
Reflected Ripple Current			--	15	--
Input Filter	Capacitor filter				

Output Specifications

Item	Operating Conditions	Min.	Typ.	Max.	Unit
Output Voltage Accuracy	See tolerance envelope graph (Fig. 1)				
Line Regulation	Input voltage change: $\pm 1\%$	--	--	± 1.2	%
Load Regulation	10%-100% load	5VDC output	--	12	
		9VDC output	--	9	
		12VDC output	--	8	
		15VDC output	--	7	
		24VDC output	--	6	
Ripple & Noise*	20MHz bandwidth	--	60	--	mVp-p
Temperature Drift Coefficient	100% load	--	--	± 0.03	%/°C
Output Short Circuit Protection **	5V/12V/15 input	Continuous, self-recovery			
	24V input	--	--	1	s

Note: * Ripple and noise tested with "parallel cable" method, please see DC-DC Converter Application Notes for specific operation methods.

**Supply voltage must be discontinued at the end of short circuit duration for A_D-1WR2 series of 24V input.

General Specifications

Item	Operating Conditions	Min.	Typ.	Max.	Unit
Insulation Voltage	Input-output, with the test time of 1 minute and the leak current lower than 1mA	1500	--	--	VDC
Insulation Resistance	Input-output, insulation voltage 500VDC	1000	--	--	MΩ
Isolation Capacitance	Input-output, 100KHz/0.1V	--	20	--	pF
Operating Temperature	Derating if the temperature $\geq 85^{\circ}\text{C}$, (see Fig. 2)	-40	--	105	°C
Storage Temperature		-55	--	125	
Casing Temperature Rise	Ta=25°C	--	25	--	
Pin Welding Resistance Temperature	Welding spot is 1.5mm away from the casing, 10 seconds	--	--	300	
Storage Humidity	Non-condensing	--	--	95	
Switching Frequency	100% load, nominal input voltage	--	100	300	KHz
MTBF	MIL-HDFK-217F@25°C	3500	--	--	K hours

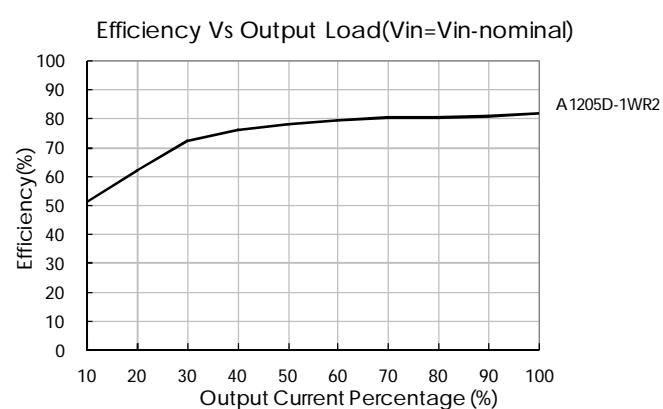
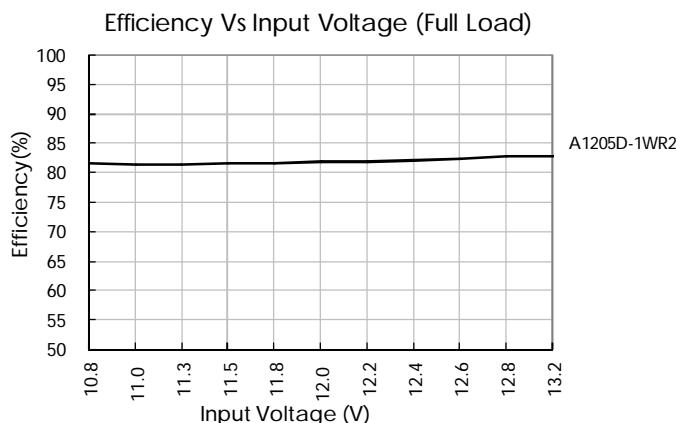
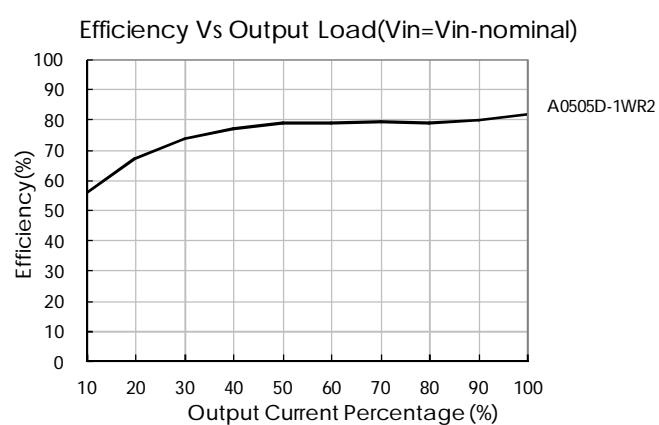
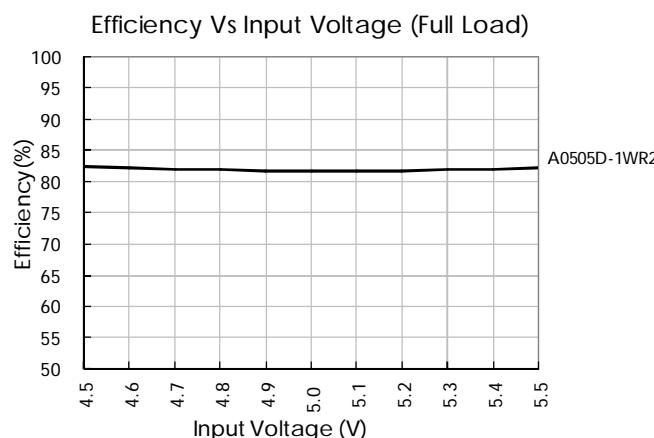
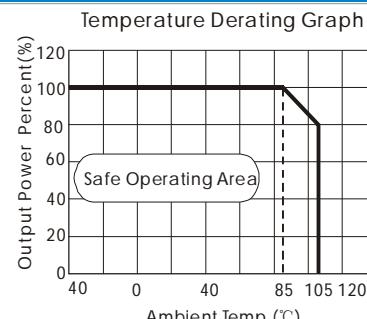
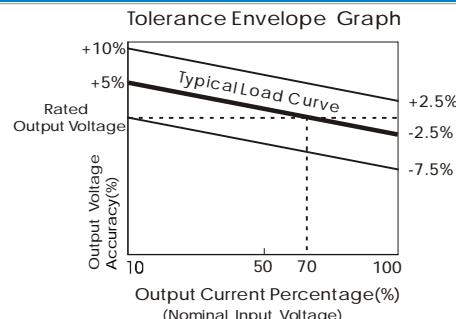
Physical Specifications

Casing Material	Black flame-retardant heat-proof epoxy resin (UL94-V0)
Package Dimensions	20.00*10.00*7.00mm
Weight	2.4g(Typ.)
Cooling methods	Free air convection

EMC Specifications

EMI	Conducted disturbance	CISPR22/EN55022	CLASS B (see Fig. 4 for recommended circuit)
	Radiated emission	CISPR22/EN55022	CLASS B (see Fig. 4 for recommended circuit)
EMS	Electrostatic discharge	IEC/EN61000-4-2	Contact $\pm 6\text{kV}$ perf. Criteria B

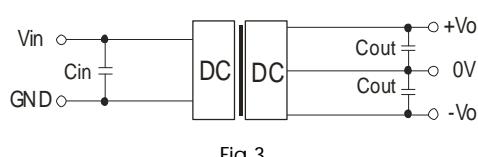
Product Characteristic Curve



Design Reference

1. Typical application

If it is required to further reduce input and output ripple, a filter capacitor can be connected to the input and output terminals, see Fig.3. Moreover, choosing suitable filter capacitor is very important, start-up problems may be caused by too large capacitance. To ensure the modules running well, the recommended capacitive load values as shown in Table 1.

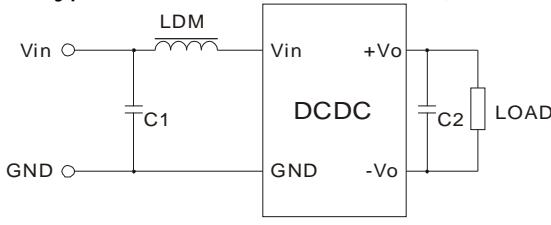


Recommended capacitive load value table (Table 1)

Vin (VDC)	Cin (μ F)	Dual Vout (VDC)	Cout (μ F)
5	4.7	± 5	4.7
12	2.2	$\pm 9/\pm 12$	1
15	2.2	$\pm 15/\pm 24$	0.47
24	1	--	--

It is not recommended to connect any external capacitor when output power is less than 0.5W.

2. EMC typical recommended circuit (CLASS B)



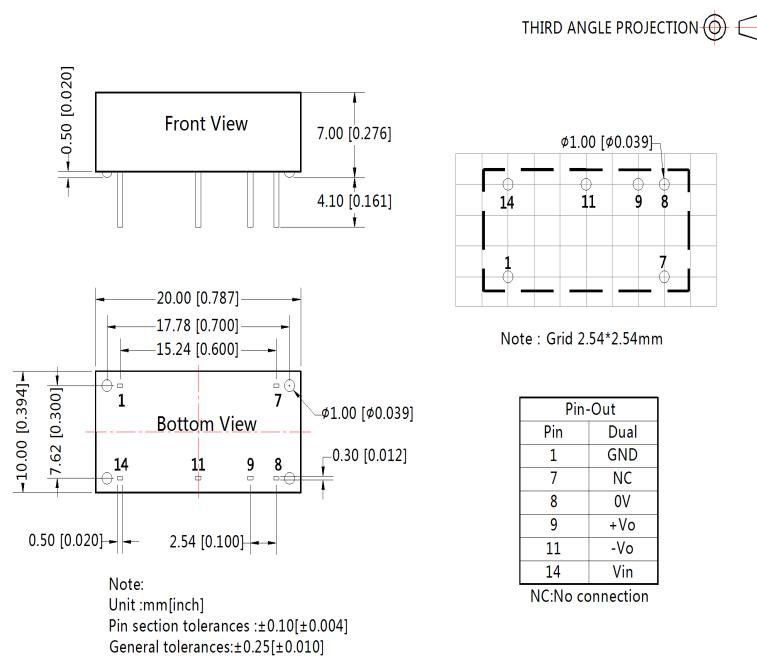
	Input voltage (V)	5/12/15/24
EMI	C1	4.7µF /50V
	C2	Refer to the Cout in Fig.3
	LDM	6.8µH

3. Output load requirements

To ensure the module work efficiently and reliably, during the operation, the min. output load should be no less than 10% of the full load. If the actual output power is low, please connect a resistor to the output terminal in parallel, with a recommended resistance which is 10% of the rated power, and derating is required during operation.

4. For more information please find the application notes on www.mornsun-power.com

Dimensions and Recommended Layout



Note:

1. Packing Information please refer to 'Product Packing Information'. Packing bag number: 58200009;
2. If the product is operated under the min. required load, the product performance cannot be guaranteed to comply with all performance indexes in this datasheet;
3. The max. capacitive load should be tested within the input voltage range and under full load conditions;
4. Unless otherwise specified, data in this data sheet should be tested under the conditions of Ta=25°C, humidity<75% when inputting nominal voltage and outputting rated load;
5. All index testing methods in this datasheet are based on our Company's corporate standards;
6. The performance indexes of the product models listed in this manual are as above, but some indexes of non-standard model products will exceed the above-mentioned requirements, and please directly contact our technicians for specific information;
7. We can provide product customization service;
8. Specifications of this product are subject to changes without prior notice.

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