

CMNTVS5V0
SURFACE MOUNT SILICON
UNI-DIRECTIONAL
5.0 VOLT, 4-LINE
TVS ARRAY

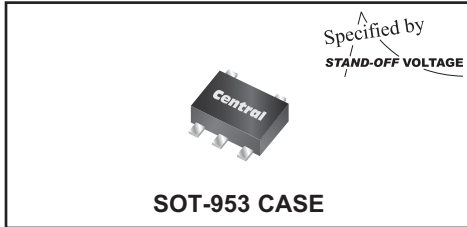


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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMNTVS5V0 is a 4-line Transient Voltage Suppressor array in the space saving SOT-953 surface mount package. This device is designed to protect sensitive equipment against ESD damage.

MARKING CODE: HH



APPLICATIONS:

- User interface protection
- Data line protection
- Voltage rail protection
- Voltage clamping

FEATURES:

- Small, 1.0 x 0.8mm, SOT-953 surface mount package
- Low capacitance
- Low leakage current
- 4-Line common anode array

MAXIMUM RATINGS: ($T_A=25^\circ\text{C}$)

Peak Power Dissipation (8x20 μs)
 ESD Voltage (IEC 61000-4-2, Air)
 ESD Voltage (IEC 61000-4-2, Contact)
 Operating and Storage Junction Temperature

SYMBOL

P_{PK} 25
 V_{ESD} 15
 V_{ESD} 15
 T_J, T_{stg} -55 to +150

UNITS

W
 kV
 kV
 $^\circ\text{C}$

ELECTRICAL CHARACTERISTICS PER DIODE: ($T_A=25^\circ\text{C}$)

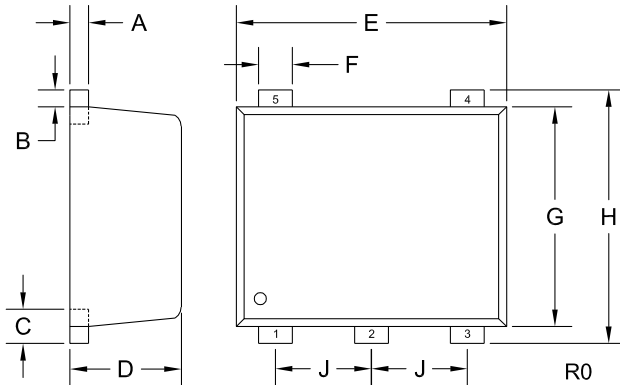
Maximum Reverse Stand-Off Voltage V_{RWM}	Breakdown Voltage $V_{BR} @ 1.0\text{mA}$			Reverse Leakage Current $I_R @ V_{RWM}$		Maximum Clamping Voltage (8x20 μs) $V_C @ I_{PP}$		Junction Capacitance @ 0V Bias C_J		Typical Junction Capacitance @ 3.3V Bias C_J
	V	MIN V	NOM V	MAX V	TYP nA	MAX nA	V	A	TYP pF	MAX pF
5.0	6.0	7.1	8.0	5.0	250	12.5	2.0	9.5	10	5.5

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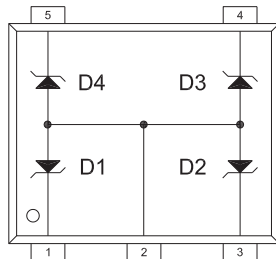
SOT-953 CASE - MECHANICAL OUTLINE



SYMBOL	DIMENSIONS			
	INCHES	MILLIMETERS		
	MIN	MAX	MIN	MAX
A	0.002	0.006	0.050	0.150
B	0.002	0.006	0.050	0.150
C	0.005	0.007	0.125	0.175
D	0.016	0.020	0.400	0.500
E	0.037	0.041	0.950	1.050
F	0.004	0.008	0.100	0.200
G	0.030	0.033	0.750	0.850
H	0.037	0.041	0.950	1.050
J	0.014		0.350	

SOT-953 (REV: R0)

PIN CONFIGURATION



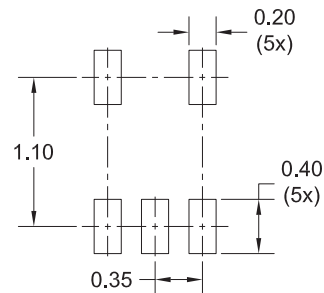
LEAD CODE:

- 1) Cathode D1
- 2) Anode D1, D2, D3, D4
- 3) Cathode D2
- 4) Cathode D3
- 5) Cathode D4

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SUGGESTED MOUNTING PADS

(Dimensions in mm)



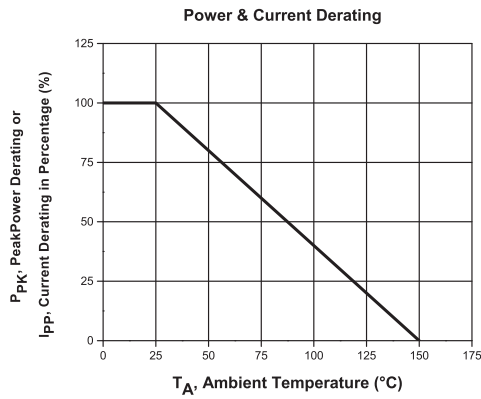
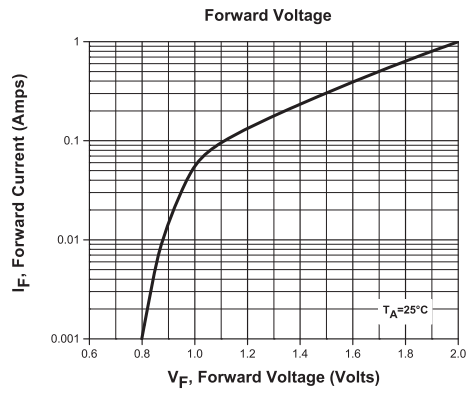
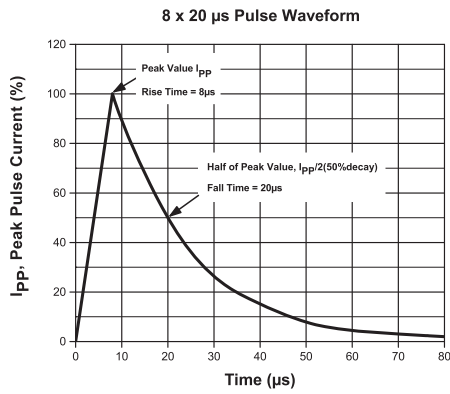
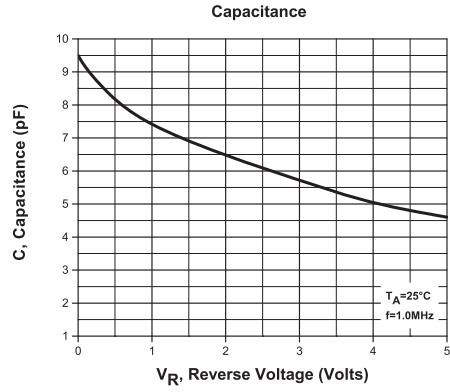
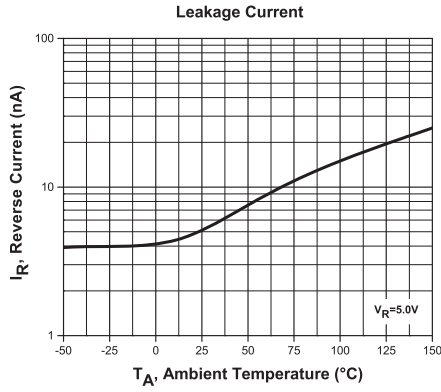
R0

R3 (6-April 2015)

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TYPICAL ELECTRICAL CHARACTERISTICS



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R3 (6-April 2015)