

## **i709x**

### **General Description**

The i7090 limiting amplifier, with complementary PECL data and signal detect outputs, is designed for high speed fiber-optic based communications.

The i7090 also includes a programmable signal-level detector, allowing the user to set threshold at which the PECL outputs are enabled. Typically 1.75 dB(Optical) of hysteresis is provided to prevent chattering when signal level is close to the threshold.

STB and JAM can be used to implement a squelch function, which gives fixed logic levels at data outputs when the input signal is below the programmed threshold. The i7090 has both PECL and CMOS status outputs (pins ST and STB).

### **Features**

- Programmable chatter-free signal level detector
- 4 mV input sensitivity (differential).
- 3.3V to 5V supply voltage
- PECL data outputs
- PECL signal detect logic interface (i7090)
- CMOS signal detect logic interface (i7091)
- Available in SSOP16 , QFN 3mmx3mm package