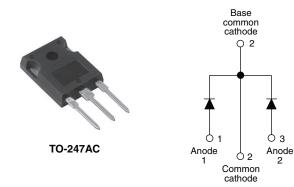


**Vishay Semiconductors** 

## Schottky Rectifier, 2 x 25 A



PRODUCT SUMMARY						
Package	TO-247AC					
I <sub>F(AV)</sub>	2 x 25 A					
V <sub>R</sub>	30 V					
V <sub>F</sub> at I <sub>F</sub>	0.38 V					
I <sub>RM</sub> max.	450 mA at 125 °C					
T <sub>J</sub> max.	150 °C					
Diode variation	Common cathode					
E <sub>AS</sub>	27 mJ					

### FEATURES

- 150 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



- RoHS COMPLIANT HALOGEN FREE
- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

### DESCRIPTION

The VS-52CPQ030... center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I <sub>F(AV)</sub>	Rectangular waveform	50	A						
V <sub>RRM</sub>		30	V						
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	2180	A						
V <sub>F</sub>	25 Apk, T <sub>J</sub> = 125 °C (per leg)	0.38	V						
TJ	Range	- 55 to 150	°C						

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-52CPQ030PbF	VS-52CPQ030-N3	UNITS				
Maximum DC reverse voltage	V <sub>R</sub>	30	30	V				
Maximum working peak reverse voltage	V <sub>RWM</sub>			V				

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDI	TIONS	VALUES	UNITS				
Maximum average per leg		50 % duty cycle at $T_{C}$ = 132 °C, rectangular waveform		25					
See fig. 5 per device	I <sub>F(AV)</sub>	50 % ddty cycle at 1 <sub>0</sub> = 152 ° 0,1	50	А					
Maximum peak one cycle non-repetitive surge current per leg	1	5 $\mu s$ sine or 3 $\mu s$ rect. pulse	Following any rated load condition and with rated	2180	A				
See fig. 7	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	600					
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 6 A, L = 1.5 mH		27	mJ				
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero i Frequency limited by $T_J$ maximum		6	А				

Revision: 31-Aug-11

Document Number: 94237

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PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS
		25 A	T - 25 °C	0.48	
Maximum forward voltage drop per leg See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	50 A	T <sub>J</sub> = 25 °C	0.55	V
	VFM (")	25 A	T <sub>.1</sub> = 125 °C	0.38	V
		50 A	$I_{\rm J} = 125$ C	0.49	
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V - Poted V	1.9	mA
See fig. 2	IRM (")	T <sub>J</sub> = 125 °C	$V_R = Rated V_R$	450	
Threshold voltage	V <sub>F(TO)</sub>			0.24	V
Forward slope resistance	r <sub>t</sub>	$T_J = T_J maximum$		5.05	mΩ
Maximum junction capacitance per leg	CT	$V_{R} = 5 V_{DC}$ (test signal rang	ge 100 kHz to 1 MHz) 25 °C	4600	pF
Typical series inductance per leg	LS	Measured lead to lead 5 m	7.5	nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10000	V/µs	

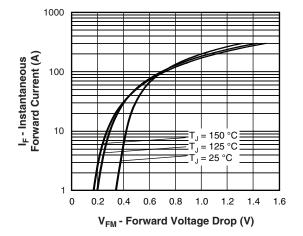
#### Note

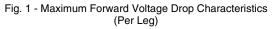
 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

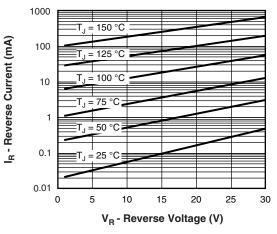
THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 150	°C					
Maximum thermal resistance, junction to case per leg	P	DC operation See fig. 4	0.8						
Maximum thermal resistance, junction to case per package	– R <sub>thJC</sub>	DC operation	0.4	°C/W					
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.25						
Approvimete weight			6	g					
Approximate weight			0.21	oz.					
	inimum		6 (5)	kgf ⋅ cm					
Mounting torque ma	aximum		12 (10)	(lbf ⋅ in)					
Marking device		Case style TO-247AC (JEDEC)	52CP	Q030					

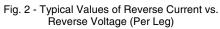


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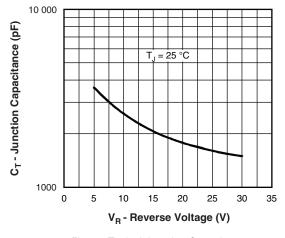
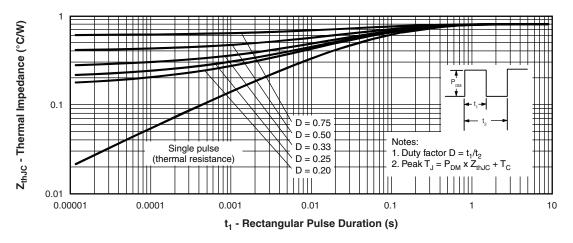


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)



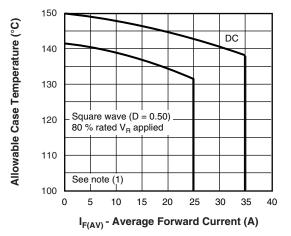


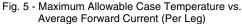
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 3
 Document Number: 94237

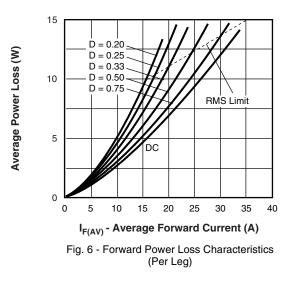
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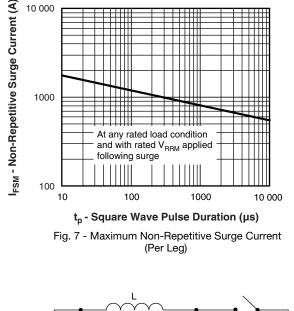


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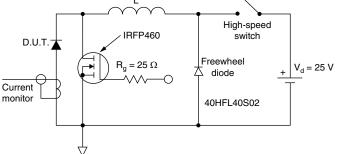


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

(1) Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$ 

Revision: 31-Aug-11

4

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### **ORDERING INFORMATION TABLE**

Device code	VS-	52	С	Р	Q	030	PbF		
		(2)	(3)	(4)	(5)	6	(7)		
	1 - 2 - 3 - 4 -	<ul> <li>Vishay Semiconductors product</li> <li>Current rating (50 A)</li> <li>Circuit configuration: C = Common cathode</li> <li>Package: P = TO-247</li> </ul>							
	5 - 6 -	Volt	age coo	" series le (030 =					
	7 -	• F	bF = Le	ntal digit ead (Pb)	-free an		-		

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)								
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION					
VS-52CPQ030PbF	25	500	Antistatic plastic tube					
VS-52CPQ030-N3	25	500	Antistatic plastic tube					

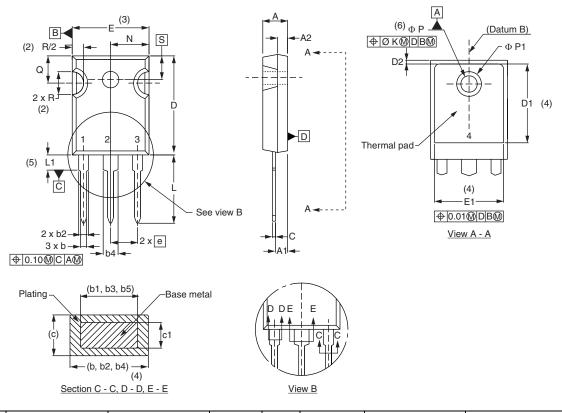
LINKS TO RELATED DOCUMENTS						
Dimensions		www.vishay.com/doc?95223				
Part marking information	TO-247AC PbF	www.vishay.com/doc?95226				
	TO-247AC -N3	www.vishay.com/doc?95007				



Vishay Semiconductors

**TO-247** 

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS		INC	INCHES		NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES		STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.65	5.31	0.183	0.209			D2	0.51	1.30	0.020	0.051	
A1	2.21	2.59	0.087	0.102			E	15.29	15.87	0.602	0.625	3
A2	1.50	2.49	0.059	0.098			E1	13.72	-	0.540	-	
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215	5 BSC	
b1	0.99	1.35	0.039	0.053			ØК	2.	54	0.0	010	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.34	0.065	0.092			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			N	7.62	BSC	0	.3	
b5	2.59	3.38	0.102	0.133			ØР	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035			Ø P1	-	6.98	-	0.275	
c1	0.38	0.84	0.015	0.033			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	' BSC	

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

(4) Thermal pad contour optional with dimensions D1 and E1

<sup>(5)</sup> Lead finish uncontrolled in L1

<sup>(6)</sup> Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

<sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-247 with exception of dimension c

Revision: 07-Apr-15

1



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