TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SZ126FE

Bus Buffer 3-State Output

Features

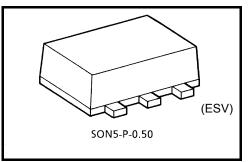
- High output current : ±24mA (min) at V_{CC} = 3V
- Super high speed operation : t_{pd} = 2.6ns (typ.)

at V_{CC} = 5 V, C_L = 50pF

- Operation voltage range : V_{CC} = 1.65 to 5.5V
- 5.5-V tolerant inputs
- 5.5-V power down protection output
- ESD performance : Machine model $\ge \pm 200 \text{ V}$

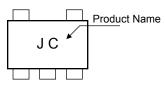
Human body model ≥ ±2000 V

- Matches the performance of TC74LCX series when operated at 3.3-V V_{CC}

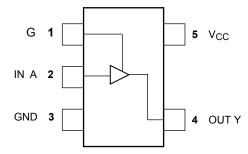


Weight: 0.003 g (typ.)

Marking



Pin Assignment (top view)



Absolute Maximum Ratings (Ta = 25°C)

| Characteristic | Symbol | Rating | Unit |
|------------------------------------|------------------|--|------|
| Supply voltage | V _{CC} | –0.5 to 6 | V |
| DC input voltage | V _{IN} | –0.5 to 6 | V |
| | | -0.5 to 6 (Note 1) | V |
| DC output voltage | Vout | –0.5 to V _{CC} + 0.5 (Note 2) | |
| Input diode current | I _{IK} | -20 | mA |
| Output diode current | lok | –20 (Note3) | mA |
| DC output current | IOUT | ±50 | mA |
| DC V _{CC} /ground current | ICC | ±50 | mA |
| Power dissipation | PD | 150 | mW |
| Storage temperature | T _{stg} | -65 to 150 | °C |

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

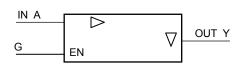
Note 1: V_{CC} = 0V or High impedance condition.

Note 2: High or Low state. Do not exceed I_{OUT} of absolute maximum ratings. Note 3: V_{OUT} < GND

Start of commercial production 2008-10

TOSHIBA

IEC Logic Symbol



Truth Table

| G | А | Y |
|---|---|---|
| L | Х | Z |
| Н | L | L |
| Н | Н | Н |

X: Don't Care Z: High Impedance

Operating Ranges

| Characteristic | Symbol | Rating | Unit | | |
|-------------------------------|------------------|---|------|--|--|
| Supply voltage | V _{CC} | 1.65 to 5.5 | V | | |
| Supply voltage | | 1.5 to 5.5 (Note 4) | v | | |
| Input voltage | V _{IN} | 0 to 5.5 | V | | |
| Output voltage | V _{OUT} | 0 to 5.5 (Note 5) | V | | |
| | | 0 to V _{CC} (Note 6) | | | |
| Operating temperature | T _{opr} | -40 to 85 | °C | | |
| | dt/dv | 0 to 20 (V_{CC} = 1.8 V \pm 0.15V, 2.5 V \pm 0.2 V) | ns/V | | |
| Input rise time and fall time | | 0 to 10 (V_{CC} = 3.3 V \pm 0.3 V) | | | |
| | | 0 to 5 (V_{CC} = 5.0 V \pm 0.5 V) | | | |

Note 4: Data retention only

Note 5: $V_{CC} = 0$ V or High impedance condition

Note 6: High or Low State

Electrical Characteristics

DC Characteristics

| Characteristic | Symbol | Test Condition | | | Ta = 25°C | | | Ta = -40 to 85°C | | Unit |
|----------------------------------|-----------------|--|---------------------------|---------------------|---------------------------|------|---------------------------|---------------------------|---------------------------|------|
| Characteristic | Symbol | | | V _{CC} (V) | Min | Тур. | Max | Min | Max | Unit |
| High-level | VIH | | | 1.65 to 1.95 | V _{CC} × 0.75 | _ | _ | V _{CC} × 0.75 | _ | |
| input voltage | ЧН | | _ | 2.3 to 5.5 | V _{CC} × 0.7 | _ | _ | $V_{CC} \times 0.7$ | _ | v |
| Low-level | VIL | | | 1.65 to 1.95 | | | V _{CC} × 0.25 | _ | V _{CC} × 0.25 | v |
| input voltage | ۷IL | | - | 2.3 to 5.5 | | | $V_{CC} \times 0.3$ | _ | V _{CC} × 0.3 | |
| | | | | 1.65 | 1.55 | 1.65 | | 1.55 | | |
| | | | I _{OH} = -100 μA | 2.3 | 2.2 | 2.3 | _ | 2.2 | | |
| | | | IOH = -100 μA | 3.0 | 2.9 | 3.0 | _ | 2.9 | | |
| | | | | 4.5 | 4.4 | 4.5 | _ | 4.4 | | |
| High-level output voltage | V _{OH} | $V_{IN} = V_{IH}$ | I _{OH} = -4 mA | 1.65 | 1.29 | 1.52 | _ | 1.29 | _ | |
| | | | I _{OH} = -8 mA | 2.3 | 1.9 | 2.15 | _ | 1.9 | _ | |
| | | | I _{OH} = -16 mA | 3.0 | 2.4 | 2.8 | _ | 2.4 | | - V |
| | | | I _{OH} = -24 mA | 3.0 | 2.3 | 2.68 | _ | 2.3 | | |
| | | | I _{OH} = -32 mA | 4.5 | 3.8 | 4.2 | | 3.8 | | |
| | | | | 1.65 | _ | 0 | 0.1 | _ | 0.1 | |
| | | 100 4 | 2.3 | _ | 0 | 0.1 | _ | 0.1 | | |
| | | | I _{OL} = 100 μA | 3.0 | _ | 0 | 0.1 | _ | 0.1 | - |
| | | | | 4.5 | _ | 0 | 0.1 | _ | 0.1 | |
| Low-level output voltage | V _{OL} | V _{IN} = V _{IH} or V _{IL} | I _{OL} = 4 mA | 1.65 | | 0.08 | 0.24 | — | 0.24 | |
| output ronugo | | | I _{OL} = 8 mA | 2.3 | _ | 0.1 | 0.3 | _ | 0.3 | - |
| | | | I _{OL} = 16 mA | 3.0 | _ | 0.15 | 0.4 | _ | 0.4 | |
| | | | I _{OL} = 24 mA | 3.0 | _ | 0.22 | 0.55 | _ | 0.55 | |
| | | I _{OL} = 32 mA | 4.5 | _ | 0.22 | 0.55 | _ | 0.55 | | |
| Input leakage current | I _{IN} | V _{IN} = 5.5 V | or GND | 0 to 5.5 | | | ±1 | _ | ±10 | μA |
| 3-state output off-state current | I _{OZ} | $V_{IN} = V_{IH} \text{ or } V_{IL}$ $V_{OUT} = 0 \text{ to } 5.5V$ | | 1.65 to 5.5 | | | ±1 | | ±10 | μΑ |
| Power off leakage current | IOFF | V _{IN} or V _{OUT} = 5.5 V | | 0.0 | | | 1 | — | 10 | μΑ |
| Quiescent supply current | ICC | $V_{IN} = V_{CC}$ or GND | | 5.5 | _ | | 2 | | 20 | μA |

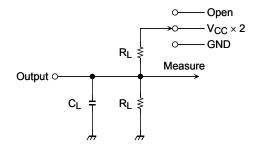
AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

| Characteristic Symb | | Test Condition | | Ta = 25°C Ta = -40 to 85°C | | |) to 85°C | Unit | |
|------------------------|---|---|-------------------------------|----------------------------|------|------|-----------|------|------|
| Characteristic | Symbol | Test Condition | V _{CC} (V) | Min | Тур. | Max | Min | Max | Unit |
| | | | 1.8 ± 0.15 | 2.0 | 5.3 | 11.0 | 2.0 | 11.5 | |
| | | $C_L = 15 \text{ pF}, R_L = 1M\Omega$ | 2.5 ± 0.2 | 0.8 | 3.4 | 7.5 | 0.8 | 8.0 | |
| Propagation delay time | t _{pLH} | $O_{L} = 10 \text{ pr}, \text{ N}_{L} = 1002$ | $\textbf{3.3}\pm\textbf{0.3}$ | 0.5 | 2.5 | 5.2 | 0.5 | 5.5 | ns |
| Topagation delay time | t _{pHL} | | 5.0 ± 0.5 | 0.5 | 2.1 | 4.5 | 0.5 | 4.8 | |
| | | | $\textbf{3.3}\pm\textbf{0.3}$ | 1.5 | 3.2 | 5.7 | 1.5 | 6.0 | |
| | | $C_L = 50 \text{ pF}, R_L = 500\Omega$ | 5.0 ± 0.5 | 0.8 | 2.6 | 5.0 | 0.8 | 5.3 | |
| | | | 1.8 ± 0.15 | 2.0 | 7.0 | 14.9 | 2.0 | 16.6 | |
| t _{pZL} | $C_{L} = 50 \text{ pF}, R_{L} = 500 \Omega$ | 2.5 ± 0.2 | 1.5 | 4.6 | 8.5 | 1.5 | 9.0 | ns | |
| Output enable time | t _{pZH} | $C_{L} = 50 \text{ pr}, \text{ K}_{L} = 500 \text{ sz}$ | $\textbf{3.3}\pm\textbf{0.3}$ | 1.5 | 3.5 | 6.2 | 1.5 | 6.5 | 115 |
| | | | 5.0 ± 0.5 | 0.8 | 2.8 | 5.5 | 0.8 | 5.8 | |
| | | | 1.8 ± 0.15 | 2.0 | 5.4 | 11.8 | 2.0 | 12.7 | |
| Output disable time | | 2.5 ± 0.2 | 1.5 | 4.0 | 8.0 | 1.5 | 8.5 | 20 | |
| | t _{pHZ} | $C_L = 50 \text{ pF}, R_L = 500 \Omega$ | $\textbf{3.3}\pm\textbf{0.3}$ | 1.0 | 3.5 | 5.7 | 1.0 | 6.0 | ns |
| | | | 5.0 ± 0.5 | 0.5 | 2.5 | 4.7 | 0.5 | 5.0 | |
| Input capacitance | C _{IN} | — | 0 to 5.5 | _ | 4 | | _ | _ | pF |
| Power dissipation | C _{PD} | (Note 7) | 3.3 | _ | 17 | | _ | _ | рF |
| capacitance | | | 5.5 | _ | 24 | _ | _ | | Ч |

Note 7: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation: $I_{CC\ (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

AC Characteristics Measurement Circuit



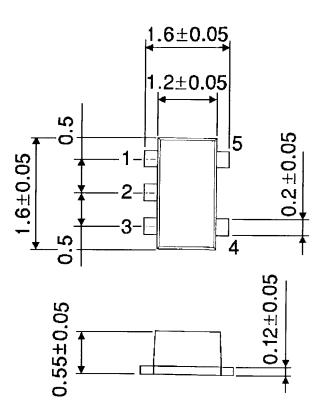
| Characteristics | Switch |
|------------------------------------|-------------------|
| t _{pLH,} t _{pHL} | Open |
| t _{pLZ,} t _{pZL} | $V_{CC} \times 2$ |
| t _{pHZ,} t _{pZH} | GND |

TOSHIBA

Package Dimensions

SON5-P-0.50

Unit : mm



Weight: 0.003 g (typ.)

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