

# NCE6005R

# NCE N-Channel Enhancement Mode Power MOSFET

#### **Description**

The NCE6005R uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

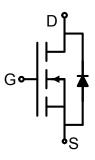
#### **General Features**

V<sub>DS</sub> =60V,I<sub>D</sub> =5A

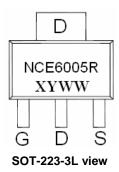
 $R_{DS(ON)} < 45 \text{m}\Omega @ V_{GS} = 10V$  (Typ:  $38 \text{m}\Omega$ )

### **Application**

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply



Schematic diagram



### **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE6005R	NCE6005R	SOT-223-3L	Ø330mm	12mm	2500 units

## Absolute Maximum Ratings (T<sub>A</sub>=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	60	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Drain Current-Continuous	I <sub>D</sub>	5	А
Drain Current-Continuous(T <sub>C</sub> =100°C)	I <sub>D</sub> (100℃)	3.5	Α
Pulsed Drain Current	I <sub>DM</sub>	20	Α
Maximum Power Dissipation	P <sub>D</sub>	2	W
Operating Junction and Storage Temperature Range	$T_{J}$ , $T_{STG}$	-55 To 150	$^{\circ}$ C

#### **Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ heta JA}$	62.5	°C/W

### Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	60	69	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V,V <sub>GS</sub> =0V	-	-	1	μA



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Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}$ =±20 $V$ , $V_{DS}$ =0 $V$	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS},I_{D}=250\mu A$	1.2	2	2.5	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	$V_{GS}$ =10V, $I_D$ =4.5A		38	45	
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =5V,I <sub>D</sub> =4.5A	11	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>	\/ -25\/\/ -0\/		450		PF
Output Capacitance	Coss	$V_{DS}$ =25V, $V_{GS}$ =0V, F=1.0MHz		60		PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.UIVITZ		25		PF
Switching Characteristics (Note 4)	<u> </u>		•	•		
Turn-on Delay Time	t <sub>d(on)</sub>	$V_{Ds}$ =30V, $I_D$ =4.5A $V_{GS}$ =10V, $R_{GEN}$ =3 $\Omega$	-	4.7	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	2.3	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	15.7	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	1.9	-	nS
Total Gate Charge	Qg		-	8.5	-	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =30V, $I_{D}$ =4.5A, $V_{GS}$ =10V	-	1.6	-	nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =10V	-	2.2	-	nC
Drain-Source Diode Characteristics	<u> </u>		•		•	•
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =5A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	5	Α

#### Notes:

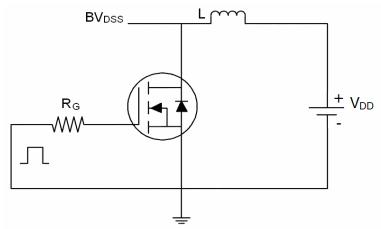
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- **3.** Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production

**Pb Free Product** 

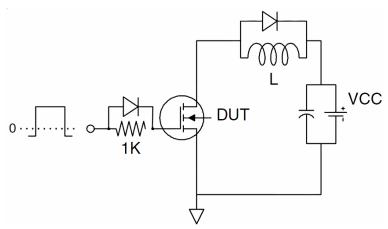
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# **Test Circuit**

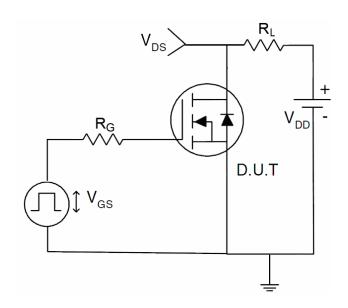
# 1) E<sub>AS</sub> Test Circuit



## 2) Gate Charge Test Circuit



### 3) Switch Time Test Circuit

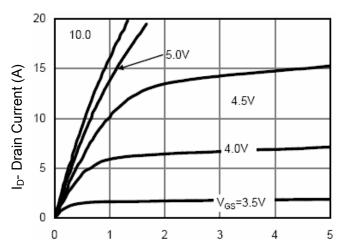


**Pb Free Product** 



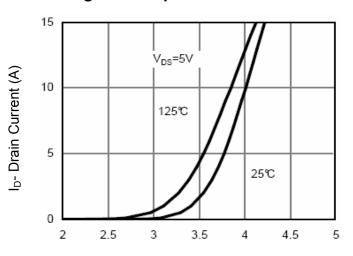
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## **Typical Electrical and Thermal Characteristics (Curves)**

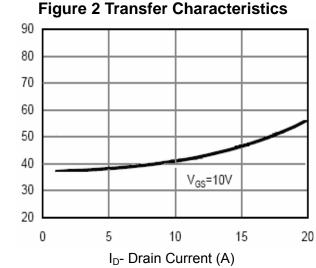


Vds Drain-Source Voltage (V)

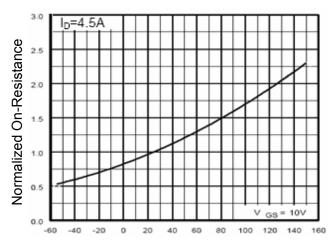
**Figure 1 Output Characteristics** 



Vgs Gate-Source Voltage (V)

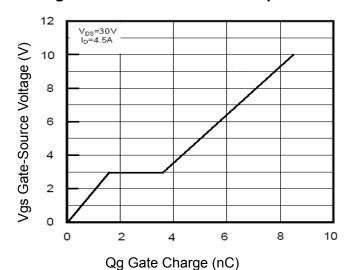


**Figure 3 Rdson- Drain Current** 



T<sub>J</sub>-Junction Temperature(°C)

#### **Figure 4 Rdson-Junction Temperature**



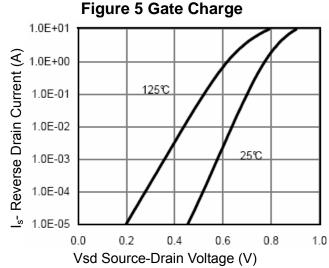


Figure 6 Source- Drain Diode Forward

Rdson On-Resistance(mΩ)



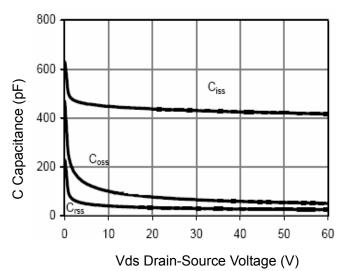
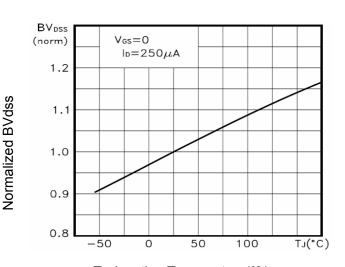


Figure 7 Capacitance vs Vds



 $T_J$ -Junction Temperature (°C) Figure 9 BV<sub>DSS</sub> vs Junction Temperature

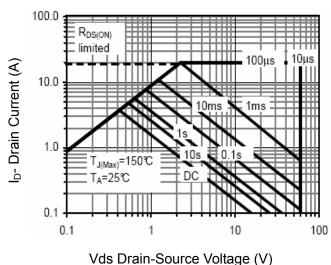


Figure 8 Safe Operation Area

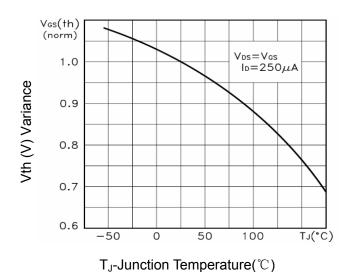
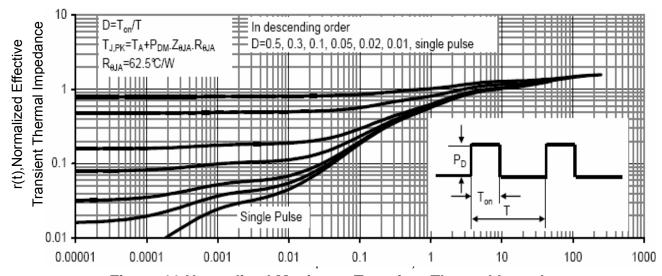


Figure 10 V<sub>GS(th)</sub> vs Junction Temperature

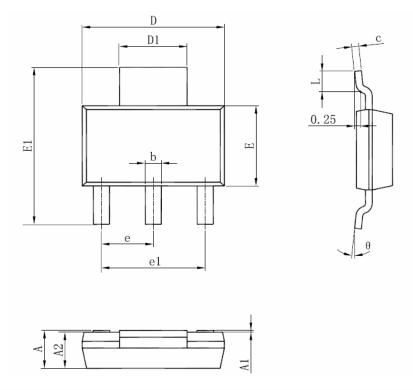


**Figure 11 Normalized Maximum Transient Thermal Impedance** 

**Pb Free Product** 

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# **SOT-223-3L Package Information**



Symbol	Dimensions Ir	n Millimeters	Dimensions In Inches		
	Min	Max	Min	Max	
Α	1.520	1.800	0.060	0.071	
A1	0.000	0.100	0.000	0.004	
A2	1.500	1.700	0.059	0.067	
b	0.660	0.820	0.026	0.032	
С	0.250	0.350	0.010	0.014	
D	6.200	6.400	0.244	0.252	
D1	2.900	3.100	0.114	0.122	
E	3.300	3.700	0.130	0.146	
E1	6.830	7.070	0.269	0.278	
е	2.300	(BSC)	0.091(	BSC)	
e1	4.500	4.700	0.177	0.185	
L	0.900	1.150	0.035	0.045	
θ	0°	10°	0°	10°	

#### Notes

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- 5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.



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