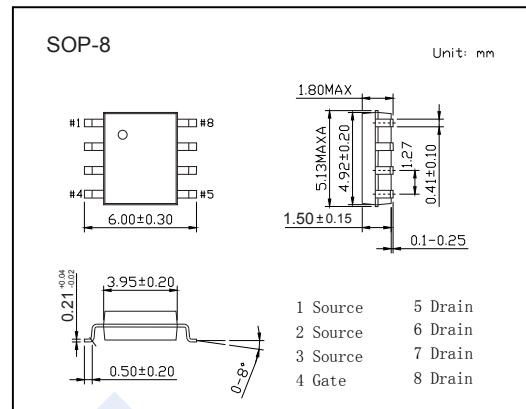
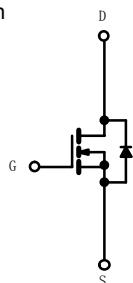


N-Channel MOSFET

SI4490DY-HF (KI4490DY-HF)

■ Features

- $V_{DS} (V) = 200V$
- $I_D = 4A$ ($V_{GS} = 10V$)
- $R_{DS(ON)} < 80m\Omega$ ($V_{GS} = 10V$)
- $R_{DS(ON)} < 90m\Omega$ ($V_{GS} = 6V$)
- Pb-Free Package May be Available. The G-Suffix Denotes a Pb-Free Lead Finish



■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter		Symbol	10S	Steady State	Unit
Drain-Source Voltage		V_{DS}	200		V
Gate-Source Voltage		V_{GS}	± 20		
Continuous Drain Current ($T_j=150^\circ C$) *1	TA=25°C	I_D	4	2.85	A
	TA=70°C		3.2	2.3	
Pulsed Drain Current		I_{DM}	40		
Avalanche Current	L=0.1mH	I_{AS}	15		
Power Dissipation *1	TA=25°C	P_D	3.1	1.56	W
	TA=70°C		2	1	
Thermal Resistance.Junction- to-Ambient *1		R_{thJA}	40	80	°C/W
Thermal Resistance.Junction- to-Foot		R_{thJF}	21		
Junction Temperature		T_J	150		°C
Storage Temperature Range		T_{stg}	-55 to 150		

*1: Surface Mounted on 1" x 1" FR4 board.

■ Marking

Marking	4490 KC**** F
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N-Channel MOSFET

SI4490DY-HF (KI4490DY-HF)

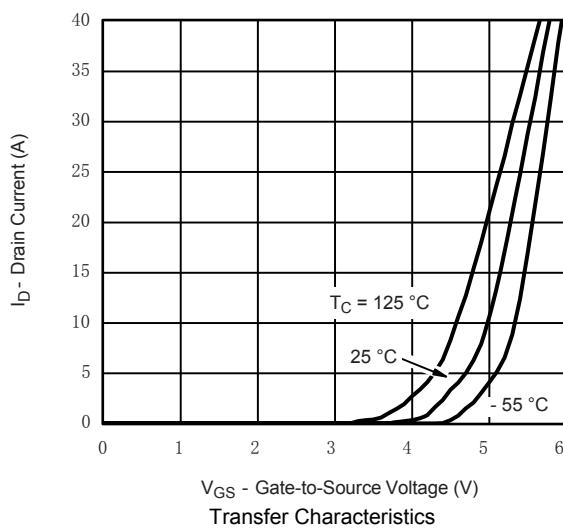
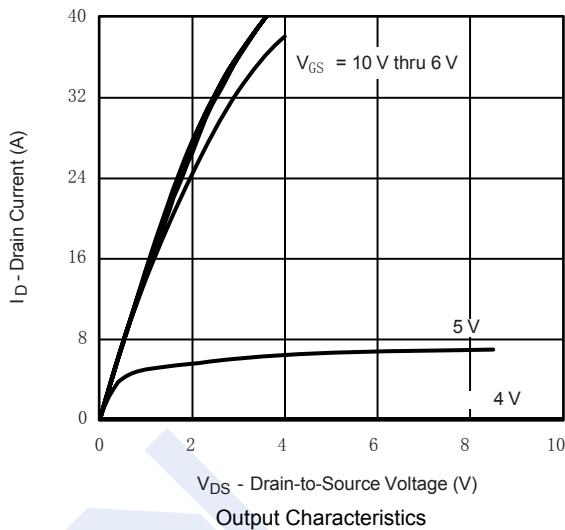
■ Electrical Characteristics $T_a = 25^\circ\text{C}$

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V_{DSS}	$I_D=250 \mu\text{A}, V_{GS}=0\text{V}$	200			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=160\text{V}, V_{GS}=0\text{V}$		1		μA
		$V_{DS}=160\text{V}, V_{GS}=0\text{V}, T_J=55^\circ\text{C}$		5		
Gate-Body Leakage Current	I_{GSS}	$V_{DS}=0\text{V}, V_{GS}=\pm 20\text{V}$			± 100	nA
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS}=V_{GS}, I_D=250 \mu\text{A}$	2			V
Static Drain-Source On-Resistance *1	$R_{DS(\text{on})}$	$V_{GS}=10\text{V}, I_D=4\text{A}$		80		$\text{m}\Omega$
		$V_{GS}=6\text{V}, I_D=4\text{A}$		90		
On State Drain Current	$I_{D(\text{ON})}$	$V_{GS}=10\text{V}, V_{DS} \geq 5\text{V}$	40			A
Forward Transconductance *1	g_{FS}	$V_{DS}=15\text{V}, I_D=5\text{A}$		19		S
Gate Resistance *2	R_g		0.2	0.85	1.3	Ω
Total Gate Charge	Q_g	$V_{GS}=10\text{V}, V_{DS}=100\text{V}, I_D=4\text{A}$ *2		34	42	nC
Gate Source Charge	Q_{GS}			7.5		
Gate Drain Charge	Q_{GD}			12		
Turn-On Delay Time	$t_{d(on)}$	$V_{GS}=10\text{V}, V_{DS}=100\text{V}, R_L=25\Omega, R_G=6\Omega, I_D=4\text{A}$ *2		14	20	ns
Turn-On Rise Time	t_r			20	30	
Turn-Off Delay Time	$t_{d(off)}$			32	50	
Turn-Off Fall Time	t_f			25	35	
Body Diode Reverse Recovery Time	t_{rr}	$I_F=2.8\text{A}, dI/dt=100\text{A}/\mu\text{s}$		70	100	
Maximum Body-Diode Continuous Current	I_S				2.8	A
Diode Forward Voltage *1	V_{SD}	$I_S=2.8\text{A}, V_{GS}=0\text{V}$		0.75	1.2	V

*1: Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$.

*2: Guaranteed by design, not subject to production testing.

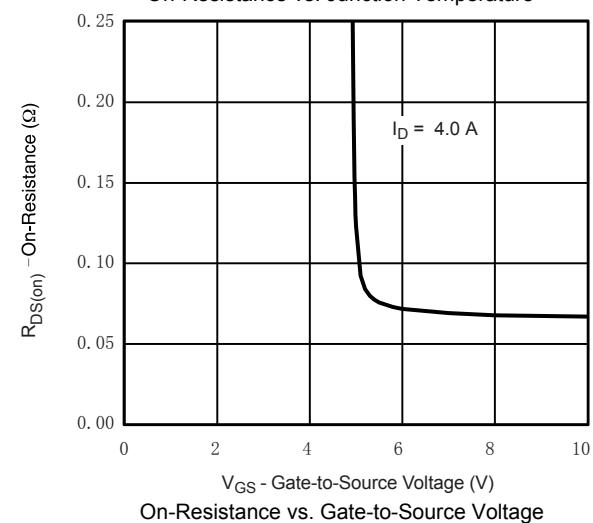
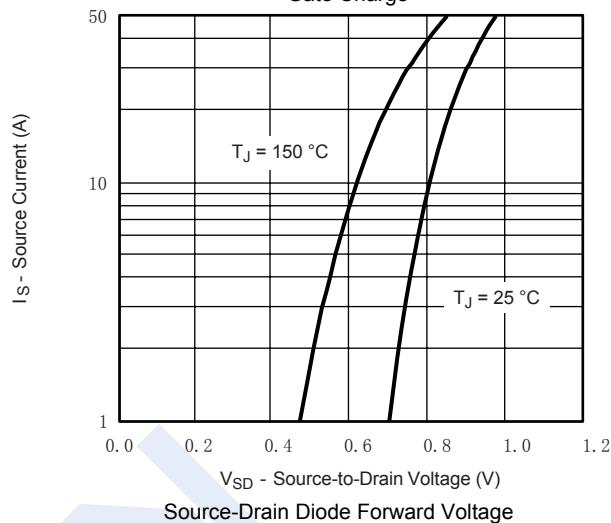
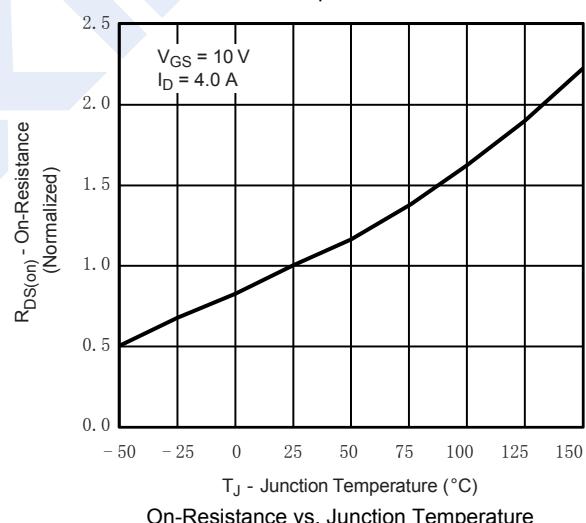
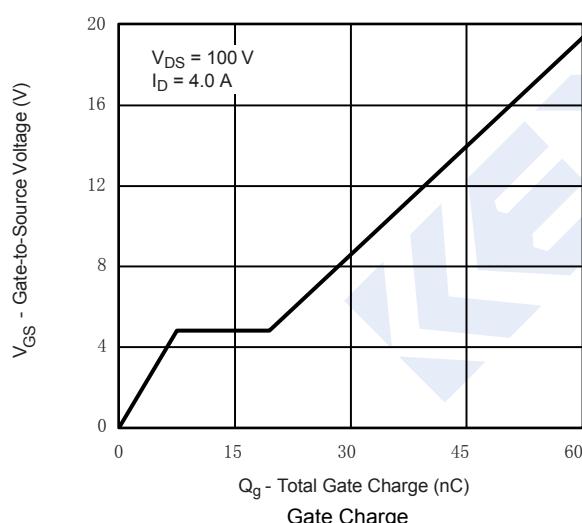
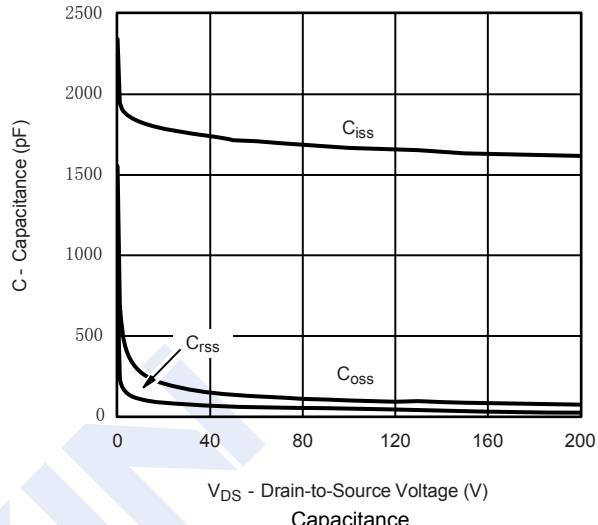
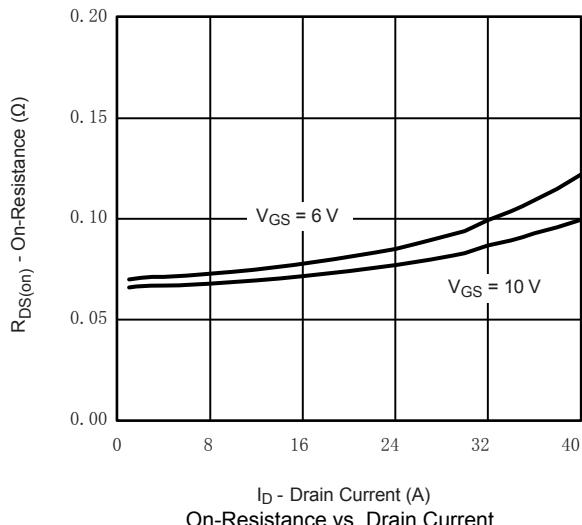
■ Typical Characteristics



N-Channel MOSFET

SI4490DY-HF (KI4490DY-HF)

■ Typical Characteristics



N-Channel MOSFET

SI4490DY-HF (KI4490DY-HF)

■ Typical Characteristics

