Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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2SK3418

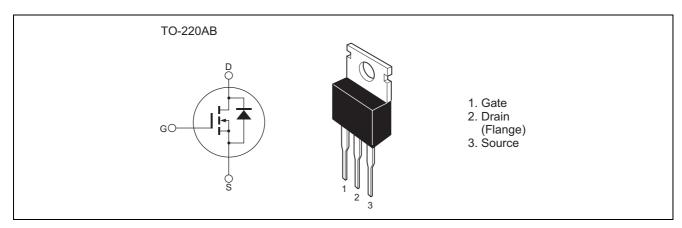
Silicon N Channel MOS FET High Speed Power Switching

REJ03G0407-0200 (Previous ADE-208-941 (Z)) Rev.2.00 Sep.10.2004

Features

- Low on-resistance $R_{DS(on)} = 4.3 \text{ m}\Omega \text{ typ.}$
- Capable of 4 V gate drive
- High speed switching

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	60	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	85	A
Drain peak current	I _D (pulse) ^{Note1}	340	A
Body-drain diode reverse drain current	I _{DR}	85	A
Avalanche current	I _{AP} Note3	60	A
Avalanche energy	E _{AR} Note3	308	mJ
Channel dissipation	Pch ^{Note2}	110	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	- 55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

- 2. Value at Tc = 25°C
- 3. Value at Tch = 25°C, Rg \geq 50 Ω

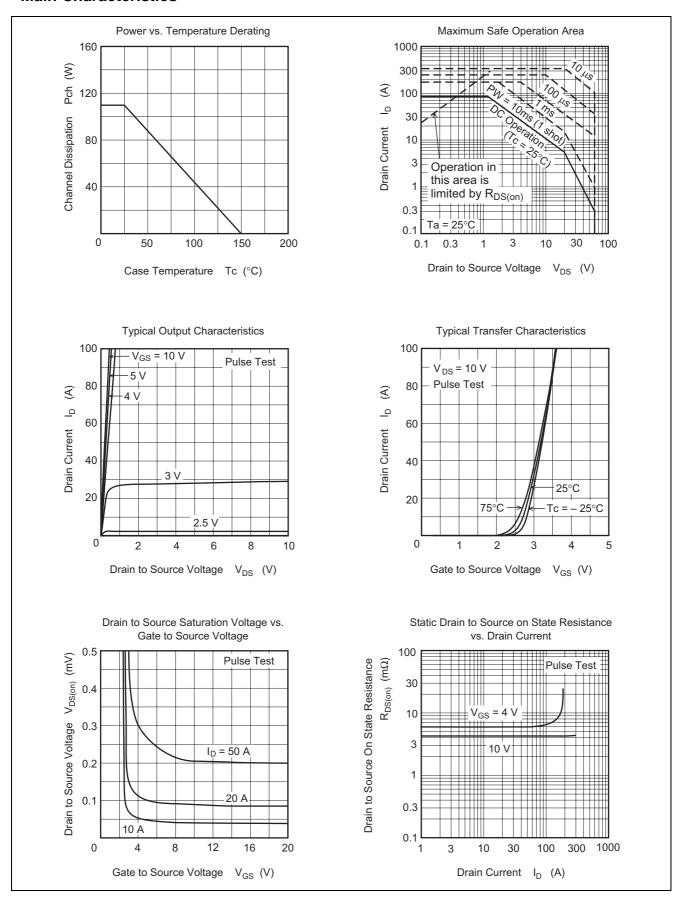
Electrical Characteristics

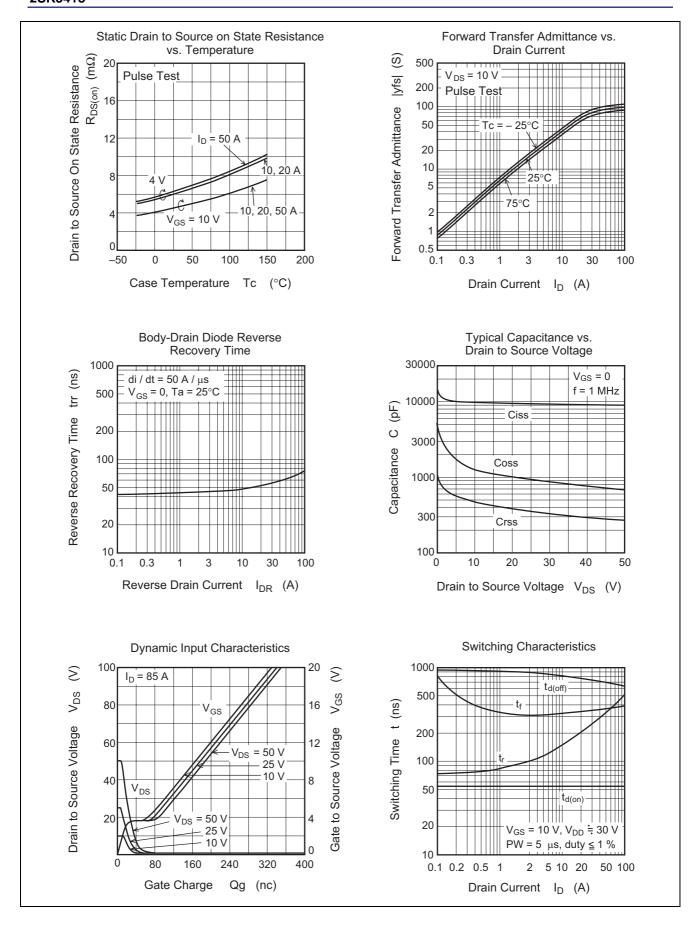
 $(Ta = 25^{\circ}C)$

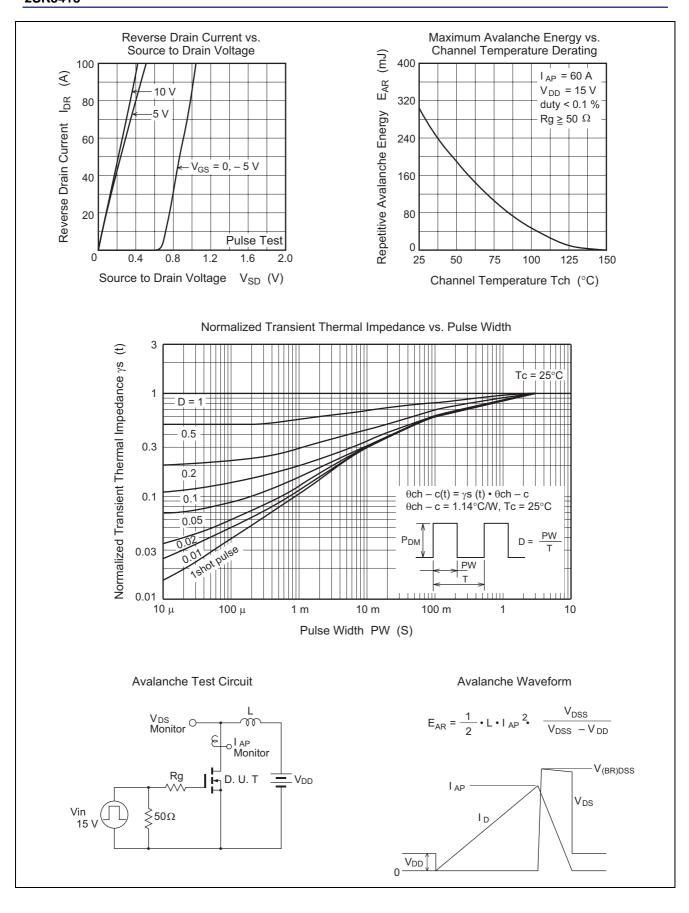
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	60	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	10	μΑ	$V_{DS} = 60 \text{ V}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	±0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	V _{GS(off)}	1.0	_	2.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}^{\text{Note1}}$
Forward transfer admittance	y _{fs}	55	90	_	S	$I_D = 45 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note1}}$
Static drain to source on state	R _{DS(on)}	_	4.3	5.5	mΩ	$I_D = 45 \text{ A}, V_{GS} = 10 \text{ V}^{Note1}$
resistance	R _{DS(on)}	_	6.0	9.0	mΩ	$I_D = 45 \text{ A}, V_{GS}=4 \text{ V}^{\text{Note1}}$
Input capacitance	Ciss	_	9770	_	pF	V _{DS} = 10 V
Output capacitance	Coss	_	1340	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	470	_	pF	f = 1 MHz
Total gate charge	Qg	_	180	_	nC	$V_{DD} = 50 \text{ V}$
Gate to source charge	Qgs	_	32	_	nC	V _{GS} = 10 V
Gate to drain charge	Qgd	_	36	_	nC	I _D = 85 A
Turn-on delay time	t _{d(on)}	_	53	_	ns	V _{GS} = 10 V
Rise time	t _r	_	320	_	ns	I _D = 45 A
Turn-off delay time	t _{d(off)}	_	700	_	ns	$R_L = 0.67 \Omega$
Fall time	t _f	_	380	_	ns	
Body-drain diode forward voltage	V_{DF}	_	1.0	_	V	I _F = 85 A, V _{GS} = 0
Body-drain diode reverse recovery time	t _{rr}	_	70	_	ns	$I_F = 85 \text{ A}, V_{GS} = 0$ diF / dt = 50 A / μ s

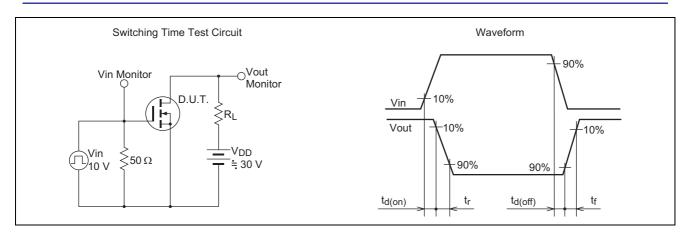
Note: 1. Pulse test

Main Characteristics

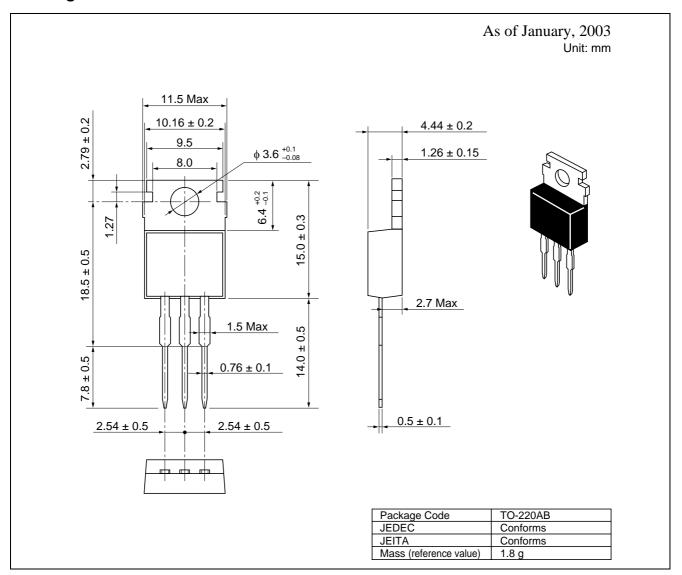








Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
2SK3418-E	50 pcs	sack

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