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RFP2N08, RFP2N10

2A, 80V and 100V, 1.05 Ohm, N-Channel Power MOSFETs

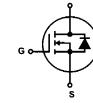
Features

- 2A, 80V and 100V
- r_{DS(ON)} 1.05Ω
- · SOA is Power Dissipation Limited
- · Nanosecond Switching Speeds
- · Linear Transfer Characteristics
- High Input Impedance
- · Majority Carrier Device
- · Related Literature

Description

These are N-channel enhancement mode silicon gate power field effect transistors designed for applications such as switching regulators, switching converters, motor drivers, relay drivers, and drivers for high power bipolar switching transistors requiring high speed and low gate drive power. These types can be operated directly from integrated circuits.

Symbol



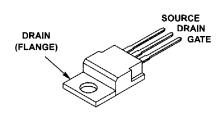
Ordering Information

PART NUMBER	PACKAGE	BRAND
RFP2N08	TO-220AB	RFP2N08
RFP2N10	TO-220AB	RFP2N10

NOTE: When ordering, use entire part number.

Packaging

JEDEC TQ-220AB



NJ Semi-Conductors reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by NJ Semi-Conductors is believed to be both accurate and reliable at the time of going to press. However, NJ Semi-Conductors assumes no responsibility for any errors or omissions discovered in its use. NJ Semi-Conductors encourages customers to verify that datasheets are current before placing orders.

Quality Semi-Conductors

Absolute Maximum Ratings $T_C = 25^{\circ}C$, Unless Otherwise Specified

	RFP2N08	RFP2N10	UNITS
Drain to Source Voltage (Note 1)	80	100	V
Drain to Gate Voltage ($R_{GS} = 1M\Omega$) (Note 1)V _{DGR}	80	100	V
Continuous Drain CurrentI _D	2	2	Α
Pulsed Drain Current (Note 3)	5	5	Α
Gate to Source Voltage	±20	±20	V
Maximum Power Dissipation	25	25	W
Linear Derating Factor	0.2	0.2	W/oC
Operating and Storage Temperature	-55 to 150	-55 to 150	°C
Maximum Temperature for Soldering			
Leads at 0.063in (1.6mm) from Case for 10s	300	300	°C
Package Body for 10s, See Techbrief 334	260	260	°C

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

NOTE:

1. $T_J = 25^{\circ}C$ to $125^{\circ}C$.

Electrical Specifications $T_C = 25$ °C, Unless Otherwise Specified

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
Drain to Source Breakdown Voltage RFP2N10	BV _{DSS}	ID = 250μA, VGS = 0	100	-	_	٧
RFP2N08			80	-	-	V
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} = V _{DS} , I _D = 250μA (Figure 8)	2	-	4	V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} = Rated BV _{DSS} , T _C = 25°C	-	-	1	μΑ
		V _{DS} = 0.8 x Rated BV _{DSS} , T _C = 125°C	-	-	25	μΑ
Gate to Source Leakage Current	I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0$	-	-	±100	nΑ
Drain to Source On Resistance (Note 2)	^r DS(ON)	I _D = 2A, V _{GS} = 10V (Figures 6, 7)	-	-	1.05	Ω
Drain to Source On Voltage (Note 2)	V _{DS(ON)}	I _D = 2A, V _{GS} = 10V	-	-	2.1	V
Turn-On Delay Time	^t d(ON)	I_D = 1A, V_{DD} = 50V, R_G = 50 Ω , R_L = 25 Ω , V_{GS} = 10V (Figures 10, 11, 12)	-	17	25	ns
Rise Time	t _r		-	30	45	ns
Turn-Off Delay Time	t _{d(OFF)}		-	30	45	ns
Fall Time	t _f		-	17	25	ns
Input Capacitance	C _{ISS}	V _{GS} = 0V, V _{DS} = 25V, f =1MHz (Figure 9)	-	-	200	pF
Output Capacitance	Coss		-	-	80	pF
Reverse-Transfer Capacitance	C _{RSS}	1	-	-	25	pF
Thermal Resistance Junction to Case	$R_{\theta JC}$		-	-	5	°C/W

Source to Drain Diode Specifications

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
Source to Drain Diode Voltage (Note 2)	V _{SD}	I _{SD} = 2A	-		1.4	٧
Diode Reverse Recovery Time	t _{rr}	I_{SD} = 2A, dI_{SD}/dt = 50A/μs	-	100	•	ns

NOTES:

- 2. Pulse test: pulse width ≤ 300µs, duty cycle ≤ 2%.
- 3. Repetitive rating: pulse width limited by maximum junction temperature.