

CPH3430

Features

- Low ON-resistance.
- Ultrahigh-speed switching.
- 2.5V drive.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		60	V
Gate-to-Source Voltage	V _{GSS}		±10	V
Drain Current (DC)	I _D		2	A
Drain Current (Pulse)	I _{DP}	PW≤10μs, duty cycle≤1%	8	A
Allowable Power Dissipation	P _D	Mounted on a ceramic board (900mm ² X0.8mm)	1	W
Channel Temperature	T _{ch}		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	V(BR)DSS	I _D =1mA, V _{GS} =0	60			V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} =0			1	μA
Gate-to-Source Leakage Current	I _{GSS}	V _{GS} =±8V, V _{DS} =0			±10	μA
Cutoff Voltage	V _{GS(off)}	V _{DS} =10V, I _D =1mA	0.4		1.3	V
Forward Transfer Admittance	y _{fs}	V _{DS} =10V, I _D =1A	1.8	3.6		S
Static Drain-to-Source On-State Resistance	R _{DS(on)1}	I _D =1A, V _{GS} =4V		170	220	mΩ
	R _{DS(on)2}	I _D =1A, V _{GS} =2.5V		190	270	mΩ
Input Capacitance	C _{iss}	V _{DS} =20V, f=1MHz		325		pF
Output Capacitance	C _{oss}	V _{DS} =20V, f=1MHz		29		pF
Reverse Transfer Capacitance	C _{rss}	V _{DS} =20V, f=1MHz		21		pF
Turn-ON Delay Time	t _{d(on)}	See specified Test Circuit.		11		ns
Rise Time	t _r	See specified Test Circuit.		17		ns
Turn-OFF Delay Time	t _{d(off)}	See specified Test Circuit.		40		ns
Fall Time	t _f	See specified Test Circuit.		27		ns

Marking : ZF

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