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## STP36N06 STP36N06FI

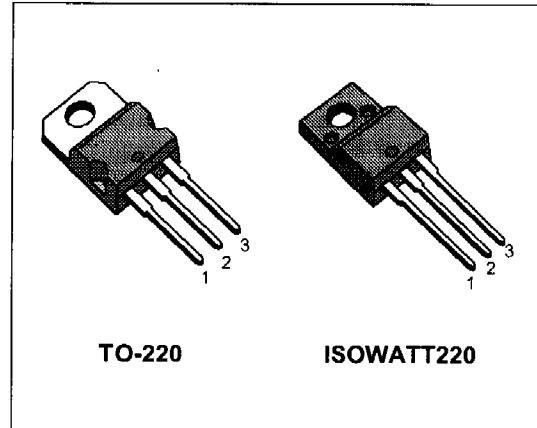
### N - CHANNEL ENHANCEMENT MODE POWER MOS TRANSISTOR

TYPE	V <sub>DSS</sub>	R <sub>D(on)</sub>	I <sub>D</sub>
STP36N06	60 V	< 0.04 Ω	36 A
STP36N06FI	60 V	< 0.04 Ω	21 A

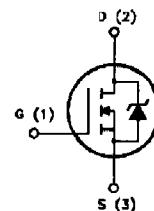
- TYPICAL R<sub>D(on)</sub> = 0.03 Ω
- AVALANCHE RUGGED TECHNOLOGY
- 100% AVALANCHE TESTED
- REPETITIVE AVALANCHE DATA AT 100°C
- LOW GATE CHARGE
- HIGH CURRENT CAPABILITY
- 175°C OPERATING TEMPERATURE
- APPLICATION ORIENTED CHARACTERIZATION

#### APPLICATIONS

- HIGH CURRENT, HIGH SPEED SWITCHING
- SOLENOID AND RELAY DRIVERS
- REGULATORS
- DC-DC & DC-AC CONVERTERS
- MOTOR CONTROL, AUDIO AMPLIFIERS
- AUTOMOTIVE ENVIRONMENT (INJECTION, ABS, AIR-BAG, LAMPDRIVERS, Etc.)



INTERNAL SCHEMATIC DIAGRAM

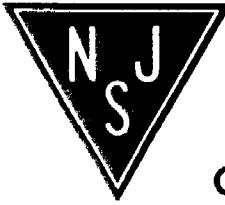


#### ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value		Unit
		STP36N06	STP36N06FI	
V <sub>DS</sub>	Drain-source Voltage (V <sub>Gs</sub> = 0)	60	60	V
V <sub>DGR</sub>	Drain-gate Voltage (R <sub>Gs</sub> = 20 kΩ)	60	60	V
V <sub>GS</sub>	Gate-source Voltage	± 20	± 20	V
I <sub>D</sub>	Drain Current (continuous) at T <sub>c</sub> = 25 °C	36	21	A
I <sub>D</sub>	Drain Current (continuous) at T <sub>c</sub> = 100 °C	25	14	A
I <sub>DM(•)</sub>	Drain Current (pulsed)	144	144	A
P <sub>tot</sub>	Total Dissipation at T <sub>c</sub> = 25 °C	120	40	W
	Derating Factor	0.8	0.27	W/°C
V <sub>ISO</sub>	Insulation Withstand Voltage (DC)	—	2000	V
T <sub>stg</sub>	Storage Temperature	-65 to 175		°C
T <sub>j</sub>	Max. Operating Junction Temperature	175		°C

(\*) Pulse width limited by safe operating area

NJ Semi-Conductors reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by NJ Semi-Conductors is believed to be both accurate and reliable at the time of going to press. However, NJ Semi-Conductors assumes no responsibility for any errors or omissions discovered in its use. NJ Semi-Conductors encourages customers to verify that datasheets are current before placing orders.



## STP36N06/FI

### THERMAL DATA

			TO-220	ISO WATT220
R <sub>thj-case</sub>	Thermal Resistance Junction-case	Max	1.25	3.75 °C/W
R <sub>thc-amb</sub>	Thermal Resistance Junction-ambient	Max	62.5	°C/W
R <sub>thc-sink</sub>	Thermal Resistance Case-sink T <sub>j</sub> Maximum Lead Temperature For Soldering Purpose	Typ	0.5 300	°C/W °C

### AVALANCHE CHARACTERISTICS

Symbol	Parameter	Max Value	Unit
I <sub>AR</sub>	Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by T <sub>j</sub> max, δ < 1%)	36	A
E <sub>AS</sub>	Single Pulse Avalanche Energy (starting T <sub>j</sub> = 25 °C, I <sub>d</sub> = I <sub>AR</sub> , V <sub>DD</sub> = 25 V)	240	mJ
E <sub>AR</sub>	Repetitive Avalanche Energy (pulse width limited by T <sub>j</sub> max, δ < 1%)	60	mJ
I <sub>AR</sub>	Avalanche Current, Repetitive or Not-Repetitive (T <sub>c</sub> = 100 °C, pulse width limited by T <sub>j</sub> max, δ < 1%)	25	A

### ELECTRICAL CHARACTERISTICS (T<sub>case</sub> = 25 °C unless otherwise specified)

#### OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V <sub>(BR)DSS</sub>	Drain-source Breakdown Voltage	I <sub>d</sub> = 250 μA V <sub>GS</sub> = 0	60			V
I <sub>oss</sub>	Zero Gate Voltage Drain Current (V <sub>GS</sub> = 0)	V <sub>DS</sub> = Max Rating V <sub>DS</sub> = Max Rating × 0.8 T <sub>c</sub> = 125 °C		1 10		μA μA
I <sub>oss</sub>	Gate-body Leakage Current (V <sub>DS</sub> = 0)	V <sub>GS</sub> = ± 20 V			± 100	nA

#### ON (\*)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V <sub>G(S)th</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> I <sub>d</sub> = 250 μA	2	2.9	4	V
R <sub>D(S)on</sub>	Static Drain-source On Resistance	V <sub>GS</sub> = 10 V I <sub>d</sub> = 18 A		0.03	0.04	Ω
I <sub>D(on)</sub>	On State Drain Current	V <sub>DS</sub> ≥ I <sub>D(on)</sub> × R <sub>D(S)on</sub> max V <sub>GS</sub> = 10 V	36			A

### DYNAMIC

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
g <sub>fs</sub> (*)	Forward Transconductance	V <sub>DS</sub> ≥ I <sub>D(on)</sub> × R <sub>D(S)on</sub> max I <sub>d</sub> = 18 A	12	16		S
C <sub>iss</sub> C <sub>oss</sub> C <sub>rss</sub>	Input Capacitance Output Capacitance Reverse Transfer Capacitance	V <sub>DS</sub> = 25 V f = 1 MHz V <sub>GS</sub> = 0	1130 480 140	1500 650 200		pF pF pF

**ELECTRICAL CHARACTERISTICS (continued)****SWITCHING ON**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-on Time	$V_{DD} = 25 \text{ V}$ $I_D = 18 \text{ A}$		45	65	ns
$t_r$	Rise Time	$R_G = 50 \Omega$ $V_{GS} = 10 \text{ V}$ (see test circuit, figure 3)		280	400	ns
$(di/dt)_{on}$	Turn-on Current Slope	$V_{DD} = 40 \text{ V}$ $I_D = 36 \text{ A}$		200		$\text{A}/\mu\text{s}$
$R_G$	Total Gate Charge	$R_G = 50 \Omega$ $V_{GS} = 10 \text{ V}$		42	60	$\text{nC}$
$Q_{gs}$	Gate-Source Charge	(see test circuit, figure 5)		11		$\text{nC}$
$Q_{gd}$	Gate-Drain Charge	$V_{DD} = 40 \text{ V}$ $I_D = 36 \text{ A}$ $V_{GS} = 10 \text{ V}$		21		$\text{nC}$

**SWITCHING OFF**

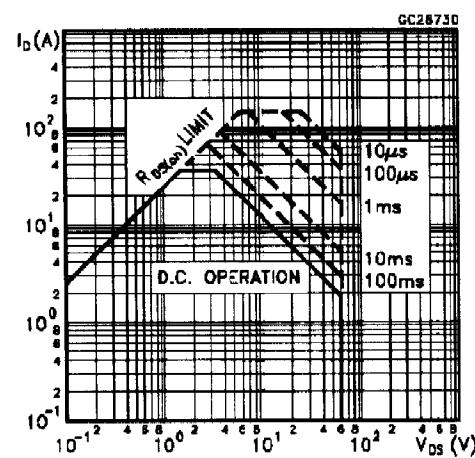
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{r(voff)}$	Off-voltage Rise Time	$V_{DD} = 40 \text{ V}$ $I_D = 36 \text{ A}$		110	160	ns
$t_f$	Fall Time	$R_G = 50 \Omega$ $V_{GS} = 10 \text{ V}$		105	150	ns
$t_c$	Cross-over Time	(see test circuit, figure 5)		220	310	ns

**SOURCE DRAIN DIODE**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$I_{SD}$	Source-drain Current				36	A
$I_{SDM(*)}$	Source-drain Current (pulsed)				144	A
$V_{SD} (*)$	Forward On Voltage	$I_{SD} = 36 \text{ A}$ $V_{GS} = 0$			1.5	V
$t_{rr}$	Reverse Recovery Time	$I_{SD} = 36 \text{ A}$ $di/dt = 100 \text{ A}/\mu\text{s}$ $V_{DD} = 30 \text{ V}$ $T_j = 150^\circ\text{C}$		90		ns
$Q_{rr}$	Reverse Recovery Charge	(see test circuit, figure 5)		0.2		$\mu\text{C}$
$I_{RRM}$	Reverse Recovery Current			4.5		A

(\*) Pulsed: Pulse duration = 300  $\mu\text{s}$ , duty cycle 1.5 %

(\*) Pulse width limited by safe operating area

**Safe Operating Areas For TO-220****Safe Operating Areas For ISOwATT220**