

100mA / 50V Digital transistors (with built-in resistors)

DTC115EEB

Applications

Inverter, Interface, Driver

Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- Each bias resistor is a thin-film resistor. Since they are completely insulated, the input can be negatively biased. The insulation also eliminates most of the parasitic effects.
- Only the on / off conditions need to be set for operation, making the device design easy.

Structure

NPN silicon epitaxial planar digital transistor

Packaging specifications

	Package	Taping
Туре	Code	TR
	Basic ordering unit (pieces)	3000
DTC115EEB		0

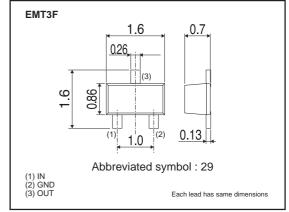
●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Supply voltage	Vcc	50	V
Input voltage	Vin	-10 to 40	V
Collector current	IC(Max.)*1	100	mA
Output Current	lo	20	mA
Power dissipation	PD *2	150	mW
Junction temperature	Tj	150	°C
Range of storage temperature	Tstg	-55 to +150	°C

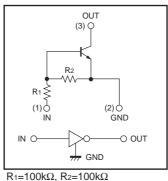
*1 Characteristics of built-in transistor

*2 Each terminal mounted on a recommended land

• Dimensions (Unit : mm)



Inner circuit



•Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
la sut colta na	VI(off)	-	-	500	mV	Vcc=5V, Io=100µA
Input voltage	VI(on)	3.0	-	-	V	Vo=0.3V, Io=1mA
Output voltage	VO(on)	-	100	300	mV	lo/lı=5mA/0.25mA
Input current	h	-	-	0.15	mA	Vi=5V
Output current	IO(off)	-	-	500	nA	Vcc=50V, VI=0V
DC current gain	Gi	82	-	-	-	Vo=5V, Io=5mA
Transition frequency	f⊤ *	-	250	-	MHz	Vce=10V, Ie= -5mA, f=100MHz
Input resistance	R1	70	100	130	kΩ	_
Resistance ratio	R2/R1	0.8	1	1.2	-	-

* Characteristics of built-in transistor

•Electrical characteristic curves

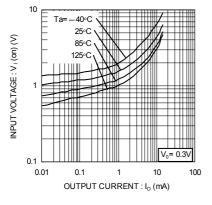


Fig.1 Input Voltage vs. Output Current (ON Characteristics)

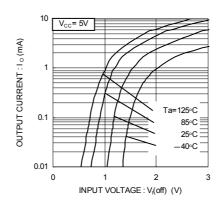


Fig.2 Output Current vs. Input Voltage (OFF Characteristics)

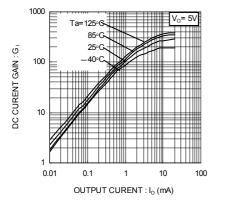


Fig.3 DC Current Gain vs. Output Current

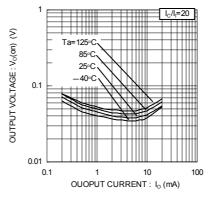


Fig.4 Output Voltage vs. Output Current

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