

## $\mu$ PA2600T1R

N-CHANNEL MOSFET 20 V, 7.0 A, 13.8 m $\Omega$ 

R07DS0998EJ0100 Rev.1.00 Jan 15, 2013

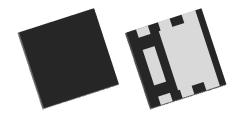
### **Description**

The  $\mu$ PA2600T1R is N-channel MOS Field Effect Transistors for switching application.

This device features a low on-state resistance and excellent switching characteristics, and is suitable for applications such as power switch of portable machine and so on.

### **Features**

- High Drain to Source Voltage
  - -- V<sub>DSS</sub> = 20 V (V<sub>GS</sub> = 0 V, T<sub>A</sub> = 25°C)
- 2.5V drive available
- Low on-state resistance
  - $R_{DS (on)1} = 13.8 \text{ m}\Omega \text{ MAX.} (V_{GS} = 4.5 \text{ V}, I_D = 3.5 \text{ A})$
  - $R_{DS (on)2} = 19.1 \text{ m}\Omega \text{ MAX}.$  ( $V_{GS} = 2.5 \text{ V}, I_D = 3.5 \text{ A}$ )
- Built-in gate protection diode
- Lead-free and Halogen-free



6pinHUSON2020

### **Ordering Information**

Part Number	Package		
μPA2600T1R-E2-AX*1	6pinHUSON2020		

Note: \*1.Pb-free (This product does not contain Pb in the external electrode and other parts.)

### Absolute Maximum Ratings ( $T_A = 25$ °C)

Item	Symbol	Ratings	Unit
Drain to Source Voltage (V <sub>GS</sub> = 0 V)	V <sub>DSS</sub>	20	<b>V</b>
Gate to Source Voltage (V <sub>DS</sub> = 0 V)	$V_{GSS}$	±12	<b>V</b>
Drain Current (DC) (T <sub>C</sub> = 25 °C)	I <sub>D(DC)</sub>	±7.0	Α
Drain Current (pulse) *1	I <sub>D(pulse)</sub>	±28	Α
Total Power Dissipation (5 s) *2	P <sub>T</sub>	2.4	W
Channel Temperature	T <sub>ch</sub>	150	°C
Storage Temperature	T <sub>STG</sub>	-55 to +150	°C

Notes: \*1. PW≤10 μs, Duty Cycle≤1%

\*2. Mounted on glass epoxy board of 25.4mm x 25.4mm x 0.8mmt

### Electrical Characteristics (T<sub>A</sub> = 25°C)

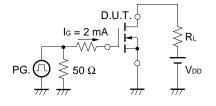
Characteristics	Symbol	MIN.	TYP.	MAX.	Unit	Test Conditions
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			1.0	μA	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V
Gate Leakage Current	I <sub>GSS</sub>			±10	μA	V <sub>GS</sub> =±10 V, V <sub>DS</sub> = 0 V
Gate Cut-off Voltage	V <sub>GS(off)</sub>	0.5		1.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Forward Transfer Admittance *1	y <sub>fs</sub>	6.5			S	$V_{DS} = 10V, I_D = 3.5 A$
Drain to Source On-state	R <sub>DS(on)1</sub>		11.1	13.8	mΩ	$V_{GS}$ = 4.5 V, $I_{D}$ = 3.5 A
Resistance *1	R <sub>DS(on)2</sub>		14.4	19.1	mΩ	$V_{GS} = 2.5 \text{ V}, I_D = 3.5 \text{ A}$
Input Capacitance	C <sub>iss</sub>		870		pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V},$
Output Capacitance	Coss		170		pF	f = 1.0 MHz
Reverse Transfer Capacitance	C <sub>rss</sub>		110		pF	
Turn-on Delay Time	t <sub>d (on)</sub>		12		ns	$I_D = 3.5 \text{ A}, V_{DD} = 10 \text{ V},$ $V_{GS} = 4 \text{ V}, R_G = 6 \Omega$
Rise Time	t <sub>r</sub>		10		ns	
Turn-off Delay Time	t <sub>d (off)</sub>		42		ns	
Fall Time	t <sub>f</sub>		9		ns	1
Total Gate Charge	$Q_G$		7.9		nC	I <sub>D</sub> = 7.0 A , V <sub>DD</sub> = 16 V, V <sub>GS</sub> = 10 V
Gate to Source Charge	Q <sub>GS</sub>		1.7		nC	
Gate to Drain Charge	$Q_{GD}$		2.8		nC	
Body Diode Forward Voltage *1	V <sub>F(S-D)</sub>			1.5	V	I <sub>F</sub> = 7.0 A, V <sub>GS</sub> = 0 V

Note: \*1. Pulsed

### **TEST CIRCUIT 1 SWITCHING TIME**

#### 

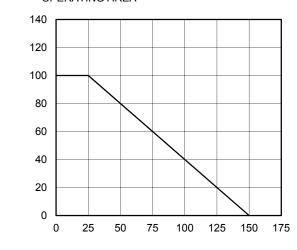
### **TEST CIRCUIT 2 GATE CHARGE**



dT - Percentage of Rated Power - %

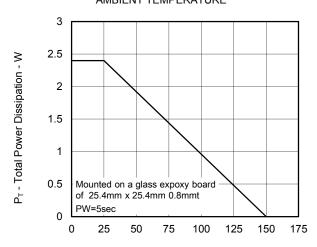
### Typical Characteristics ( $T_A = 25$ °C)

DERATING FACTOR OF FORWARD BIAS SAFE **OPERATING AREA** 



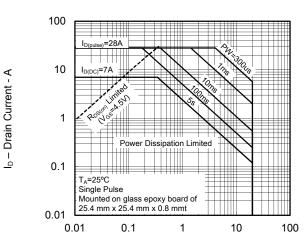
T<sub>A</sub> -Ambient Temperature - °C

#### TOTAL POWER DISSIPATION vs. AMBIENT TEMPERATURE



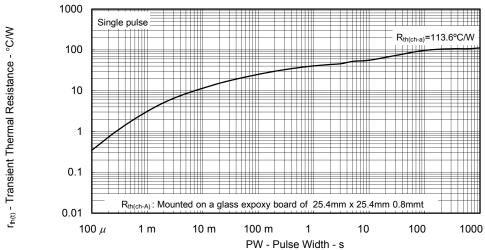
T<sub>A</sub> -Ambient Temperature - °C

#### FORWARD BIAS SAFE OPERATING AREA



V<sub>DS</sub> - Drain to Source Voltage - V

### TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

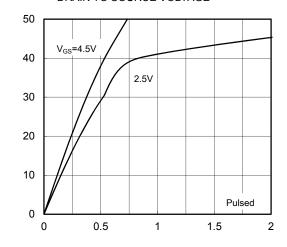


I<sub>D</sub> -Drain Current - A

V<sub>GS(off)</sub> – Gate to Source Cut-off Voltage - V

R<sub>DS(on)</sub> - Drain to Source On-state Resistance - mΩ

## DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



V<sub>DS</sub> - Drain to Source Voltage - V

# 0.001 0.001 0.0001 0.0001 0.0001 0 0.5 1 1.5

10

| y<sub>fs</sub> | - Forward Transfer Admittance - S

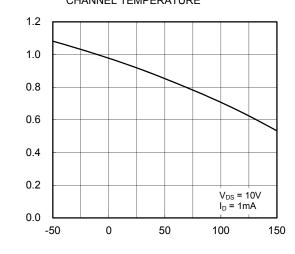
 $R_{\text{DS(on)}}-\text{Drain}$  to Source On-state Resistance -  $m\Omega$ 

FORWARD TRANSFER CHARACTERISTICS

V<sub>GS</sub> - Gate to Source Voltage - V

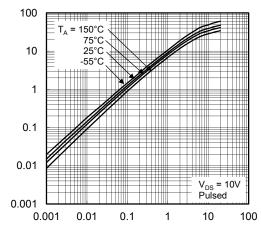
2

## GATE TO SOURCE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



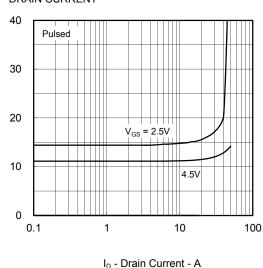
T<sub>ch</sub> - Channel Temperature - °C

## FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT

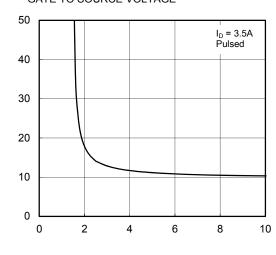


I<sub>D</sub> – Drain Current - A

## DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

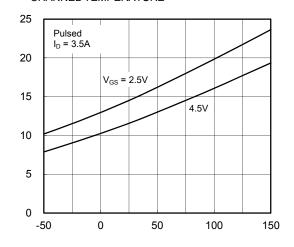


V<sub>GS</sub> - Gate to Source Voltage - V

 $\mathsf{R}_{\mathsf{DS}(\mathsf{on})}\operatorname{-Drain}$  to Source On-state Resistance -  $m\Omega$ 

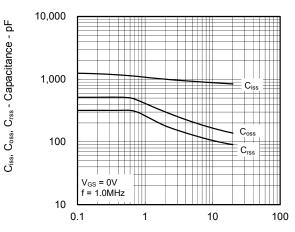
t<sub>d(on)</sub>, t<sub>f</sub>, t<sub>d(off)</sub>, t<sub>r</sub> - Switching Time -ns

## DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



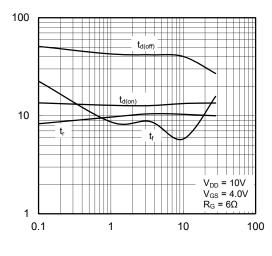
T<sub>ch</sub> - Channel Temperature - °C

## CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



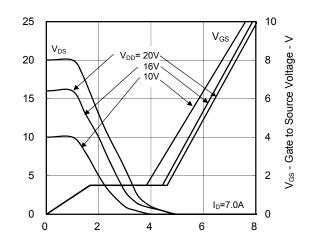
V<sub>DS</sub> - Drain to Source Voltage - V

#### SWITCHING CHARACTERISTICS



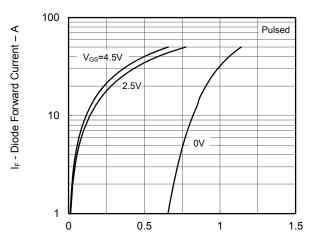
ID - Drain Current - A

#### DYNAMIC INPUT/OUTPUT CHARACTERISTICS



 $\ensuremath{\mathsf{Q}}_G$  - Gate Charge - nC

#### SOURCE TO DRAIN DIODE FORWARD VOLTAGE

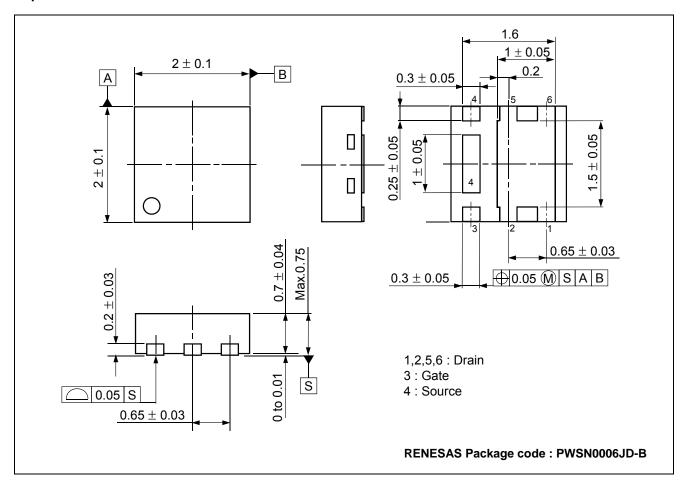


 $V_{F(S\!-\!D)}$  - Drain to Source Voltage - V

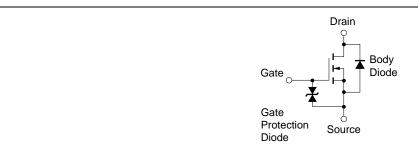
V<sub>DS</sub> - Drain to Source Voltage - V

### Package Drawings (Unit: mm)

### 6pinHUSON2020



### **Equivalent Circuit**



**Remark** The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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