# 74AHC3G14; 74AHCT3G14

# Triple inverting Schmitt trigger Rev. 8 — 13 May 2013

**Product data sheet** 

#### 1. **General description**

74AHC3G14 and 74AHCT3G14 are high-speed Si-gate CMOS devices. They provide three inverting buffers with Schmitt trigger action. These devices are capable of transforming slowly changing input signals into sharply defined, jitter-free output signals.

The AHC device has CMOS input switching levels and supply voltage range 2 V to 5.5 V.

The AHCT device has TTL input switching levels and supply voltage range 4.5 V to 5.5 V.

#### 2. Features and benefits

- Symmetrical output impedance
- High noise immunity
- ESD protection:
  - ◆ HBM JESD22-A114F exceeds 2000 V
  - MM JESD22-A115-A exceeds 200 V
  - ◆ CDM JESD22-C101D exceeds 1000 V
- Low power dissipation
- Balanced propagation delays
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

#### **Applications** 3.

- Wave and pulse shaper for highly noisy environment
- Astable multivibrator
- Monostable multivibrator



# 4. Ordering information

Table 1. Ordering information

Type number	Package	Package											
	Temperature range	Name	Description	Version									
74AHC3G14DP	-40 °C to +125 °C	TSSOP8	plastic thin shrink small outline package; 8 leads;	SOT505-2									
74AHCT3G14DP			body width 3 mm; lead length 0.5 mm										
74AHC3G14DC	–40 °C to +125 °C	VSSOP8	plastic very thin shrink small outline package; 8 leads;	SOT765-1									
74AHCT3G14DC			body width 2.3 mm										
74AHC3G14GT	−40 °C to +125 °C	XSON8	plastic extremely thin small outline package; no leads;	SOT833-1									
74AHCT3G14GT			8 terminals; body 1 $\times$ 1.95 $\times$ 0.5 mm										
74AHC3G14GD	−40 °C to +125 °C	plastic extremely thin small outline package; no leads;	SOT996-2										
74AHCT3G14GD			8 terminals; body $3 \times 2 \times 0.5$ mm										

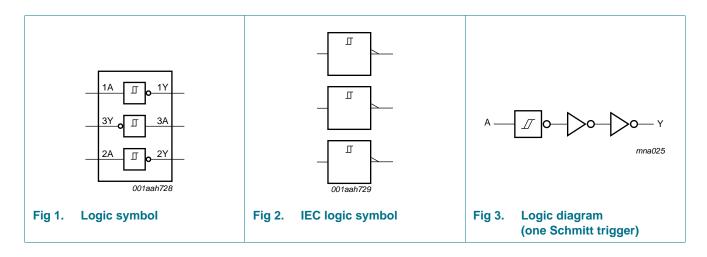
# 5. Marking

Table 2. Marking codes

<u> </u>	
Type number	Marking code <sup>[1]</sup>
74AHC3G14DP	A14
74AHCT3G14DP	C14
74AHC3G14DC	A14
74AHCT3G14DC	C14
74AHC3G14GT	A14
74AHCT3G14GT	C14
74AHC3G14GD	A14
74AHCT3G14GD	C14

<sup>[1]</sup> The pin 1 indicator is located on the lower left corner of the device, below the marking code.

# 6. Functional diagram

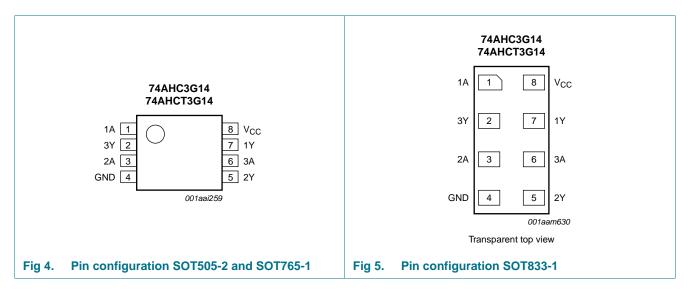


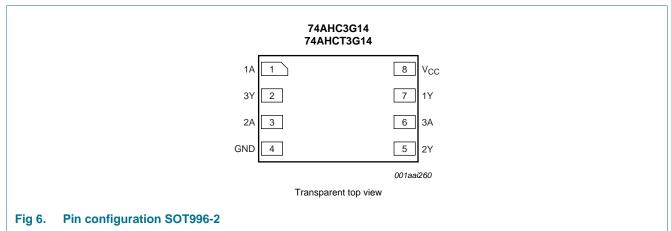
74AHC\_AHCT3G14

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# 7. Pinning information

## 7.1 Pinning





### 7.2 Pin description

Table 3. Pin description

Symbol	Pin	Description
1A, 2A, 3A	1, 3, 6	data input
GND	4	ground (0 V)
1Y, 2Y, 3Y	7, 5, 2	data output
V <sub>CC</sub>	8	supply voltage

# 8. Functional description

Table 4. Function table [1]

Input nA	Output nY
L	Н
Н	L

<sup>[1]</sup> H = HIGH voltage level; L = LOW voltage level

# 9. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{CC}$	supply voltage		-0.5	+7.0	V
VI	input voltage		-0.5	+7.0	V
I <sub>IK</sub>	input clamping current	$V_1 < -0.5 \text{ V}$	-20	-	mA
I <sub>OK</sub>	output clamping current	$V_O < -0.5 \text{ V or } V_O > V_{CC} + 0.5 \text{ V}$	<u>[1]</u> -	±20	mA
I <sub>O</sub>	output current	$-0.5 \text{ V} < \text{V}_{\text{O}} < \text{V}_{\text{CC}} + 0.5 \text{ V}$	-	±25	mA
I <sub>CC</sub>	supply current		-	75	mA
I <sub>GND</sub>	ground current		<b>-75</b>	-	mA
T <sub>stg</sub>	storage temperature		-65	+150	°C
P <sub>tot</sub>	total power dissipation	$T_{amb} = -40  ^{\circ}\text{C}$ to +125 $^{\circ}\text{C}$	[2] -	250	mW

<sup>[1]</sup> The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

# 10. Recommended operating conditions

#### Table 6. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	74	AHC3G	14	74	Unit			
			Min	Тур	Max	Min	Тур	Max	
$V_{CC}$	supply voltage		2.0	5.0	5.5	4.5	5.0	5.5	V
$V_{I}$	input voltage		0	-	5.5	0	-	5.5	V
Vo	output voltage		0	-	$V_{CC}$	0	-	$V_{CC}$	V
T <sub>amb</sub>	ambient temperature		-40	+25	+125	-40	+25	+125	°C

<sup>[2]</sup> For TSSOP8 package: above 55 °C the value of P<sub>tot</sub> derates linearly at 2.5 mW/K. For VSSOP8 package: above 110 °C the value of P<sub>tot</sub> derates linearly at 8 mW/K. For XSON8 packages: above 118 °C the value of P<sub>tot</sub> derates linearly with 7.8 mW/K.

# 11. Static characteristics

Table 7. Static characteristics

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		25 °C		-40 °C	to +85 °C	-40 °C t	Unit	
			Min	Тур	Max	Min	Max	Min	Max	
<b>74AHC</b> 3	G14		'							
V <sub>OH</sub>	HIGH-level	$V_I = V_{T+}$ or $V_{T-}$								
	output voltage	$I_O = -50 \mu A$ ; $V_{CC} = 2.0 \text{ V}$	1.9	2.0	-	1.9	-	1.9	-	V
		$I_O = -50 \mu A; V_{CC} = 3.0 V$	2.9	3.0	-	2.9	-	2.9	-	V
		$I_O = -50 \mu A; V_{CC} = 4.5 V$	4.4	4.5	-	4.4	-	4.4	-	V
		$I_{O} = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.58	-	-	2.48	-	2.40	-	V
		$I_{O} = -8.0 \text{ mA}; V_{CC} = 4.5 \text{ V}$	3.94	-	-	3.8	-	3.70	-	V
$V_{OL}$	LOW-level	$V_I = V_{T+}$ or $V_{T-}$								
	output voltage	$I_O = 50 \mu A; V_{CC} = 2.0 V$	-	0	0.1	-	0.1	-	0.1	V
		$I_O = 50 \mu A; V_{CC} = 3.0 V$	-	0	0.1	-	0.1	-	0.1	V
		$I_O = 50 \mu A$ ; $V_{CC} = 4.5 V$	-	0	0.1	-	0.1	-	0.1	V
		$I_O = 4.0 \text{ mA}$ ; $V_{CC} = 3.0 \text{ V}$	-	-	0.36	-	0.44	-	0.55	V
		$I_O = 8.0 \text{ mA}$ ; $V_{CC} = 4.5 \text{ V}$	-	-	0.36	-	0.44	-	0.55	V
l <sub>l</sub>	input leakage current	V <sub>I</sub> = 5.5 V or GND; V <sub>CC</sub> = 0 V to 5.5 V	-	-	0.1	-	1.0	-	2.0	μΑ
I <sub>CC</sub>	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5 \text{ V}$	-	-	1.0	-	10	-	40	μΑ
Cı	input capacitance		-	1.5	10	-	10	-	10	pF
74AHCT	3G14									
V <sub>OH</sub>	HIGH-level	$V_{I} = V_{T+} \text{ or } V_{T-}; V_{CC} = 4.5 \text{ V}$								
	output voltage	I <sub>O</sub> = -50 μA	4.4	4.5	-	4.4	-	4.4	-	V
		$I_0 = -8.0 \text{ mA}$	3.94	-	-	3.8	-	3.70	-	V
V <sub>OL</sub>	LOW-level	$V_{I} = V_{T+} \text{ or } V_{T-}; V_{CC} = 4.5 \text{ V}$								
	output voltage	Ι <sub>Ο</sub> = 50 μΑ	-	0	0.1	-	0.1	-	0.1	V
		$I_{O} = 8.0 \text{ mA}$	-	-	0.36	-	0.44	-	0.55	V
l <sub>i</sub>	input leakage current	V <sub>I</sub> = 5.5 V or GND; V <sub>CC</sub> = 0 V to 5.5 V	-	-	0.1	-	1.0	-	2.0	μΑ
I <sub>CC</sub>	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5 \text{ V}$	-	-	1.0	-	10	-	40	μΑ
Δl <sub>CC</sub>	additional supply current	per input pin; $V_I = 3.4 \text{ V}$ ; other inputs at $V_{CC}$ or GND; $I_O = 0 \text{ A}$ ; $V_{CC} = 5.5 \text{ V}$	-	-	1.35	-	1.5	-	1.5	mA
Cı	input capacitance		-	1.5	10	-	10	-	10	pF

### 11.1 Transfer characteristics

Table 8. Transfer characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V). See Figure 9 and Figure 10.

Symbol	Parameter	Conditions		25 °C		-40 °C	to +85 °C	-40 °C t	o +125 °C	Unit
			Min	Тур	Max	Min	Max	Min	Max	
<b>74AHC3</b>	G14		'		'	•		'	•	
$V_{T+}$	positive-going threshold	$V_{CC} = 3.0 \text{ V}$	-	-	2.2	-	2.2	-	2.2	V
		$V_{CC} = 4.5 \text{ V}$	-	-	3.15	-	3.15	-	3.15	V
	voltage	$V_{CC} = 5.5 \text{ V}$	-	-	3.85	-	3.85	-	3.85	V
$V_{T-}$	negative-going	$V_{CC} = 3.0 \text{ V}$	0.9	-	-	0.9	-	0.9	-	V
	threshold voltage	$V_{CC} = 4.5 \text{ V}$	1.35	-	-	1.35	-	1.35	-	V
		$V_{CC} = 5.5 \text{ V}$	1.65	-	-	1.65	-	1.65	-	V
V <sub>H</sub>	hysteresis voltage	$V_{CC} = 3.0 \text{ V}$	0.3	-	1.2	0.3	1.2	0.25	1.2	V
		$V_{CC} = 4.5 \text{ V}$	0.4	-	1.4	0.4	1.4	0.35	1.4	V
		$V_{CC} = 5.5 \text{ V}$	0.5	-	1.6	0.5	1.6	0.45	1.6	V
74AHCT	3G14									
V <sub>T+</sub>	positive-going	$V_{CC} = 4.5 \text{ V}$	-	-	2.0	-	2.0	-	2.0	V
	threshold voltage	$V_{CC} = 5.5 V$	-	-	2.0	-	2.0	-	2.0	V
$V_{T-}$	negative-going	$V_{CC} = 4.5 \text{ V}$	0.5	-	-	0.5	-	0.5	-	V
	threshold voltage	V <sub>CC</sub> = 5.5 V	0.6	-	-	0.6	-	0.6	-	V
V <sub>H</sub>	hysteresis	$V_{CC} = 4.5 \text{ V}$	0.4	-	1.4	0.4	1.4	0.35	1.4	V
	voltage	$V_{CC} = 5.5 \text{ V}$	0.4	-	1.6	0.4	1.6	0.35	1.6	V

# 12. Dynamic characteristics

Table 9. Dynamic characteristics

GND = 0 V;  $t_r = t_f \le 3.0$  ns; for test circuit see <u>Figure 8</u>.

Symbol	Parameter	Conditions			25 °C		-40 °C	to +85 °C	-40 °C t	o +125 °C	Unit
			Min	Тур	Max	Min	Max	Min	Max		
<b>74AHC3</b>	G14					•	'				
F	propagation	nA to nY; see Figure 7	<u>[1]</u>								
	delay	$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	[2]								
		C <sub>L</sub> = 15 pF		-	4.2	12.8	1.0	15.0	1.0	16.5	ns
		$C_L = 50 pF$		-	6.0	16.3	1.0	18.5	1.0	20.5	ns
		$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}$	[3]								
		$C_L = 15 pF$		-	3.2	8.6	1.0	10.0	1.0	11.0	ns
		$C_{L} = 50 \text{ pF}$		-	4.6	10.6	1.0	12.0	1.0	13.5	ns
dissipation		per buffer; $C_L = 50 \text{ pF}$ ; $f_i = 1 \text{ MHz}$ ; $V_I = \text{GND to } V_{CC}$	<u>[4]</u>	-	10	-	-	-	-	-	pF

 Table 9.
 Dynamic characteristics ...continued

GND = 0 V;  $t_r = t_f \le 3.0$  ns; for test circuit see <u>Figure 8</u>.

Symbol	Parameter	Conditions			25 °C		-40 °C 1	to +85 °C	–40 °C t	o +125 °C	Unit
				Min	Тур	Max	Min	Max	Min	Max	
74AHCT3G14											
t <sub>pd</sub> propagation delay		nA to nY; V <sub>CC</sub> = 4.5 V to 5.5 V	[1] [3]								
		C <sub>L</sub> = 15 pF		-	4.1	7.0	1.0	8.0	1.0	9.0	ns
		$C_L = 50 pF$		-	5.9	8.5	1.0	10.0	1.0	11.0	ns
C <sub>PD</sub>	power dissipation capacitance	per buffer; $C_L = 50 \text{ pF}$ ; $f_i = 1 \text{ MHz}$ ; $V_I = \text{GND to } V_{CC}$	[4]	-	12	-	-	-	-	-	pF

- [1] t<sub>pd</sub> is the same as t<sub>PLH</sub> and t<sub>PHL</sub>.
- [2] Typical values are measured at  $V_{CC} = 3.3 \text{ V}$ .
- [3] Typical values are measured at  $V_{CC}$  = 5.0 V.
- [4]  $C_{PD}$  is used to determine the dynamic power dissipation  $P_D$  ( $\mu W$ ).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i + \Sigma (C_L \times V_{CC}^2 \times f_o)$  where:

 $f_i$  = input frequency in MHz;

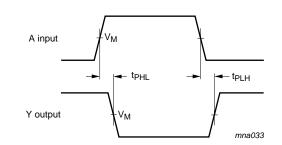
f<sub>o</sub> = output frequency in MHz;

C<sub>L</sub> = output load capacitance in pF;

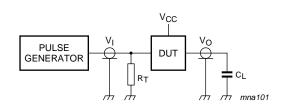
V<sub>CC</sub> = supply voltage in V;

 $\Sigma(C_L \times V_{CC}^2 \times f_o)$  = sum of the outputs.

# 13. Waveforms



The test data is given in Table 10



Test data is given in Table 10.

Definitions for test circuit:

 $C_L$  = Load capacitance.

 $R_T$  = Termination resistance should be equal to output impedance  $Z_o$  of the pulse generator.

Fig 8. Test circuit for measuring switching times

delays

The input (nA) to output (nY) propagation

#### Table 10. Test data

Type number	Input	Output	
	V <sub>I</sub>	V <sub>M</sub>	V <sub>M</sub>
74AHC3G14	GND to V <sub>CC</sub>	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$
74AHCT3G14	GND to 3.0 V	1.5 V	$0.5 \times V_{CC}$

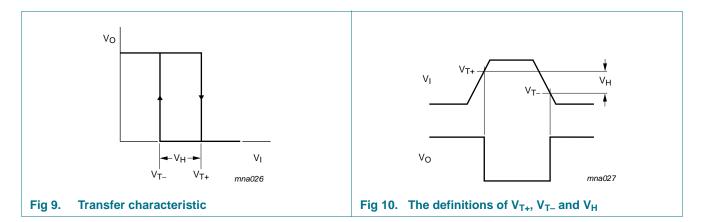
74AHC\_AHCT3G14

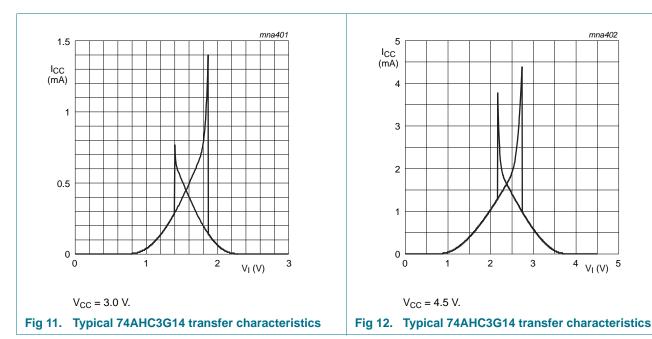
Fig 7.

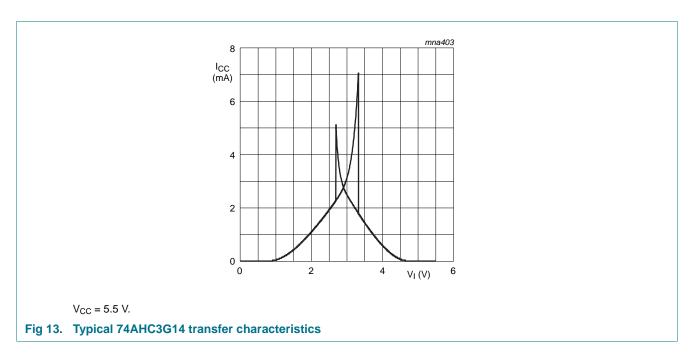
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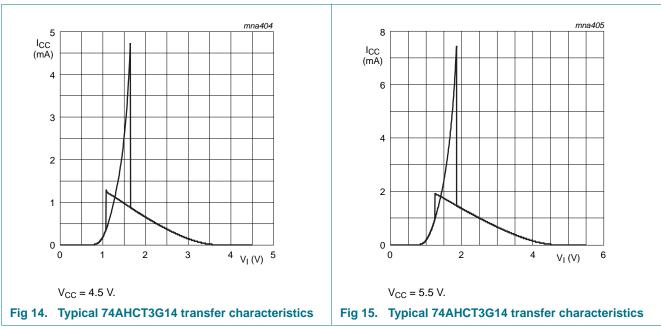
mna402

### 13.1 Transfer characteristic waveforms









# 14. Application information

The slow input rise and fall times cause additional power dissipation, which can be calculated using the following formula:

 $P_{add} = f_i \times (t_r \times \Delta I_{CC(AV)} + t_f \times \Delta I_{CC(AV)}) \times V_{CC}$  where:

 $P_{add}$  = additional power dissipation ( $\mu W$ );

 $f_i = input frequency (MHz);$ 

 $t_r$  = input rise time (ns); 10 % to 90 %;

 $t_f$  = input fall time (ns); 90 % to 10 %;

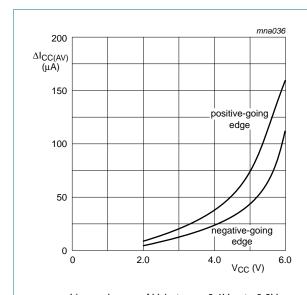
 $\Delta I_{CC(AV)}$  = average additional supply current ( $\mu A$ ).

 $\Delta I_{CC(AV)}$  differs with positive or negative input transitions, as shown in Figure 16 and Figure 17.

For 74AHC3G14 and 74AHCT3G14 used in relaxation oscillator circuit, see Figure 18.

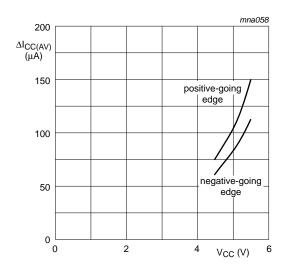
#### Note to the application information:

1. All values given are typical unless otherwise specified.



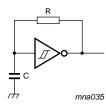
Linear change of  $V_I$  between  $0.1V_{CC}$  to  $0.9V_{CC}$ 

Fig 16. Average additional I<sub>CC</sub> for 74AHC3G14 Schmitt trigger devices



Linear change of V<sub>I</sub> between 0.1V<sub>CC</sub> to 0.9V<sub>CC</sub>

Fig 17. Average additional I<sub>CC</sub> for 74AHCT3G14 Schmitt trigger devices



For 74AHC3G14:  $f = \frac{1}{T} \approx \frac{1}{0.55 \times RC}$ 

For 74AHCT3G14:  $f = \frac{1}{T} \approx \frac{1}{0.60 \times RC}$ 

Fig 18. Relaxation oscillator using the 74AHC3G14 and 74AHCT3G14

# 15. Package outline

TSSOP8: plastic thin shrink small outline package; 8 leads; body width 3 mm; lead length 0.5 mm SOT505-2

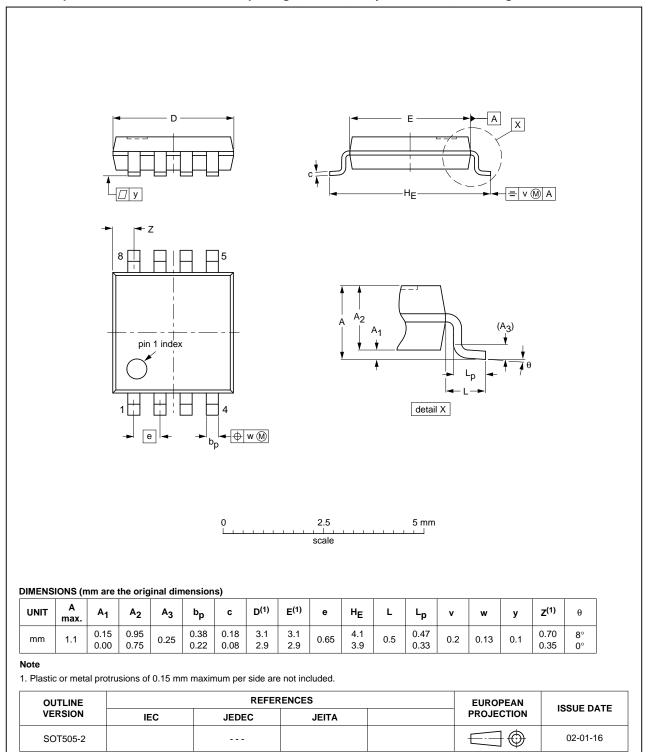
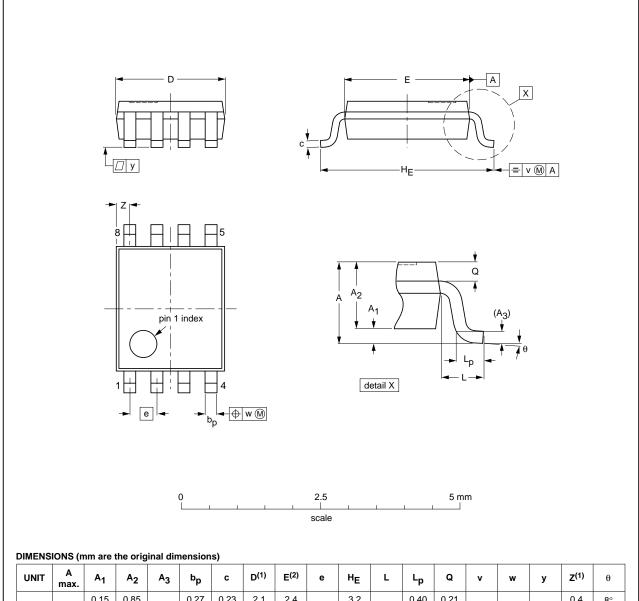


Fig 19. Package outline SOT505-2 (TSSOP8)

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### VSSOP8: plastic very thin shrink small outline package; 8 leads; body width 2.3 mm

SOT765-1



	(					-,												
UNIT	A max.	A <sub>1</sub>	A <sub>2</sub>	А3	bp	С	D <sup>(1)</sup>	E <sup>(2)</sup>	е	HE	L	Lp	Q	v	w	у	Z <sup>(1)</sup>	θ
mm	1	0.15 0.00	0.85 0.60	0.12	0.27 0.17	0.23 0.08	2.1 1.9	2.4 2.2	0.5	3.2 3.0	0.4	0.40 0.15	0.21 0.19	0.2	0.13	0.1	0.4 0.1	8° 0°

- 1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.
- 2. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

OUTLINE VERSION	REFERENCES				EUROPEAN	ISSUE DATE	
	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE	
SOT7	65-1		MO-187				02-06-07

Fig 20. Package outline SOT765-1 (VSSOP8)

74AHC\_AHCT3G14

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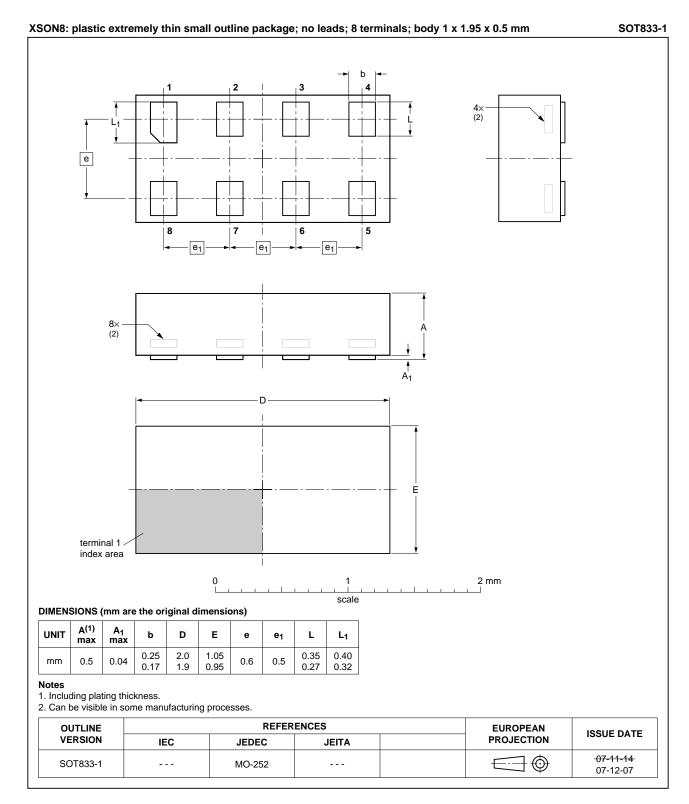


Fig 21. Package outline SOT833-1 (XSON8)

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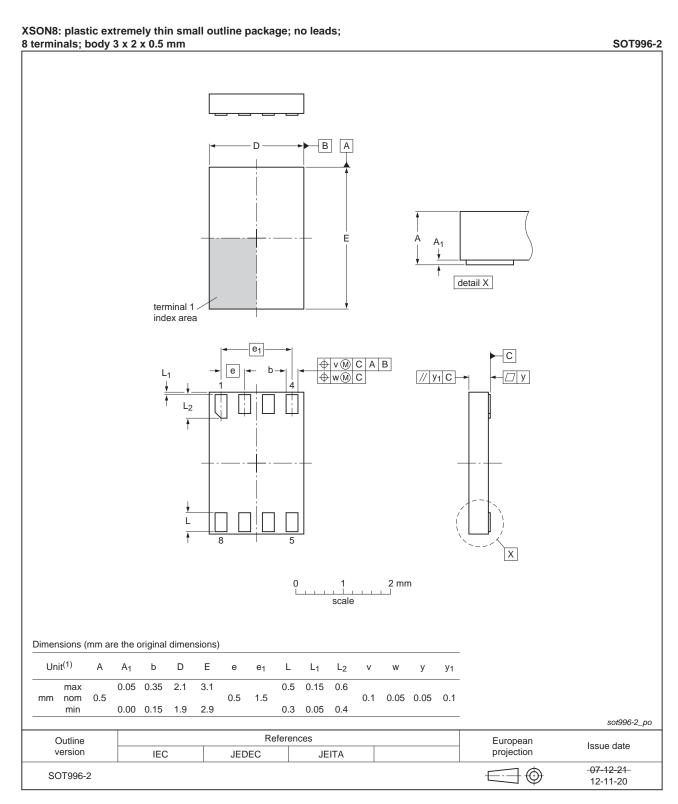


Fig 22. Package outline SOT996-2 (XSON8)

74AHC\_AHCT3G14

# 16. Abbreviations

#### Table 11. Abbreviations

Acronym	Description
CDM	Charged Device Model
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model
TTL	Transistor-Transistor Logic

# 17. Revision history

#### Table 12. Revision history

	•			
Document ID	Release date	Data sheet status	Change notice	Supersedes
74AHC_AHCT3G14 v.8	20130513	Product data sheet	-	74AHC_AHCT3G14 v.7
Modifications:	<ul> <li>For type nu</li> </ul>	mber 74AHC3G14GD and 7	74AHCT3G14GD XSON	I8U has changed to XSON8.
74AHC_AHCT3G14 v.7	20111108	Product data sheet	-	74AHC_AHCT3G14 v.6
Modifications:	<ul> <li>Legal pages</li> </ul>	s updated.		
74AHC_AHCT3G14 v.6	20101118	Product data sheet	-	74AHC_AHCT3G14 v.5
74AHC_AHCT3G14 v.5	20100923	Product data sheet	-	74AHC_AHCT3G14 v.4
74AHC_AHCT3G14 v.4	20090505	Product data sheet	-	74AHC_AHCT3G14 v.3
74AHC_AHCT3G14 v.3	20080617	Product data sheet	-	74AHC_AHCT3G14 v.2
74AHC_AHCT3G14 v.2	20041018	Product specification	-	74AHC_AHCT3G14 v.1
74AHC_AHCT3G14 v.1	20031127	Product specification	-	-

# 18. Legal information

#### 18.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <a href="http://www.nxp.com">http://www.nxp.com</a>.

#### 18.2 Definitions

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#### 19. Contact information

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For sales office addresses, please send an email to: salesaddresses@nxp.com

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