



## ELECTRICAL SPECIFICATIONS

### Absolute Maximum Ratings\*

Storage Temperature .....	-65°C to +150°C
Case Temperature under Bias ...	-65°C to +150°C
Supply Voltage with Respect to V <sub>SS</sub> .....	-0.5V to +6.5V
Voltage on Other Pins with Respect to V <sub>SS</sub> .....	-0.5V to V <sub>CC</sub> + 0.5V

### Recommended Connections

Power and ground connections must be made to multiple V<sub>CC</sub> and V<sub>SS</sub> pins. Every 80C186EA based circuit board should contain separate power (V<sub>CC</sub>) and ground (V<sub>SS</sub>) planes. All V<sub>CC</sub> and V<sub>SS</sub> pins must be connected to the appropriate plane. Pins identified as "N.C." must not be connected in the system. Decoupling capacitors should be placed near the processor. The value and type of decoupling capac-

NOTICE: This data sheet contains preliminary information on new products in production. It is valid for the devices indicated in the revision history. The specifications are subject to change without notice.

*\*WARNING: Stressing the device beyond the "Absolute Maximum Ratings" may cause permanent damage. These are stress ratings only. Operation beyond the "Operating Conditions" is not recommended and extended exposure beyond the "Operating Conditions" may affect device reliability.*

itors is application and board layout dependent. The processor can cause transient power surges when its output buffers transition, particularly when connected to large capacitive loads.

Always connect any unused input pins to an appropriate signal level. In particular, unused interrupt pins (NMI, INT3:0) should be connected to V<sub>SS</sub> to avoid unwanted interrupts. **Leave any unused output pin or any "N.C." pin unconnected.**