

LOW CAPACITANCE BIDIRECTIONAL TVS DIODE

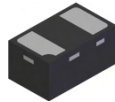
Features

- Provides ESD Protection per IEC 61000-4-2 Standard: Air ±30kV, Contact ±30kV
- 1 Channel of ESD Protection
- Low Channel Input Capacitance
- Typically Used in Cellular Handsets, Portable Electronics, Communication Systems, Computers and Peripherals
- **Lead Free/RoHS Compliant (Note 1)**
- **“Green” Device (Note 2)**

Mechanical Data

- Case: X1-DFN1006-2
- Case Material: Molded Plastic, “Green” Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.001 grams (approximate)

X1-DFN1006-2

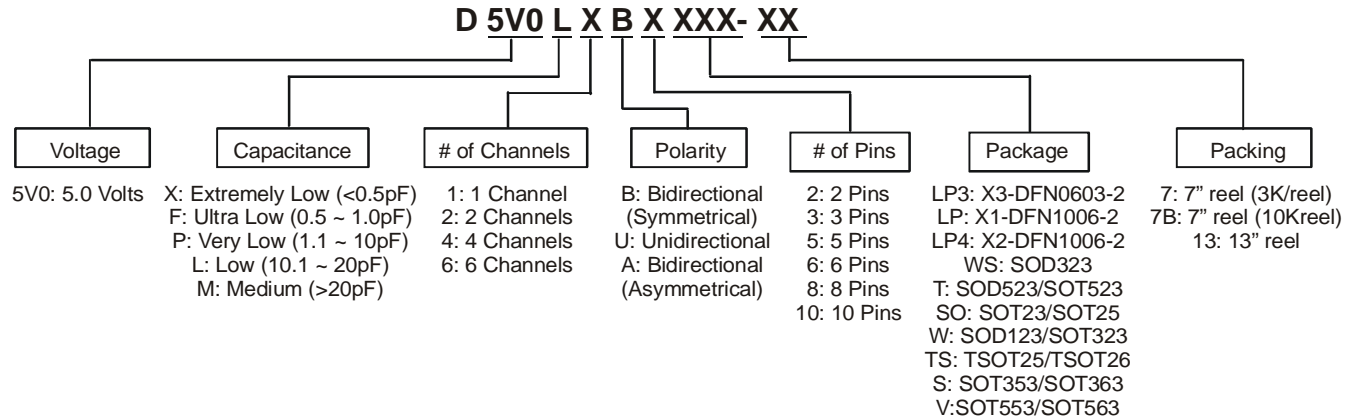


Bottom View



Device Schematic

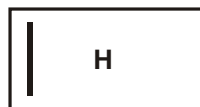
Ordering Information (Note 3)



Part Number	Case	Packaging
D5V0L1B2LP-7B	X1-DFN1006-2	10,000/Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. No purposely added lead. Halogen and Antimony free.
 2. Diodes Inc.'s "Green" policy can be found on our website at <http://www.diodes.com>.
 3. For packaging details, go to our website at <http://www.diodes.com>.

Marking Information



H = Product Type Marking Code
Line Denotes Pin 1

Maximum Ratings @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Power Dissipation	P_{PP}	84	W	8/20 μs , per Fig. 1
Peak Pulse Current	I_{PP}	6	A	8/20 μs , per Fig. 1
ESD Protection – Contact Discharge	$V_{ESD_Contact}$	± 30	kV	IEC 61000-4-2 Standard
ESD Protection – Air Discharge	V_{ESD_Air}	± 30	kV	IEC 61000-4-2 Standard

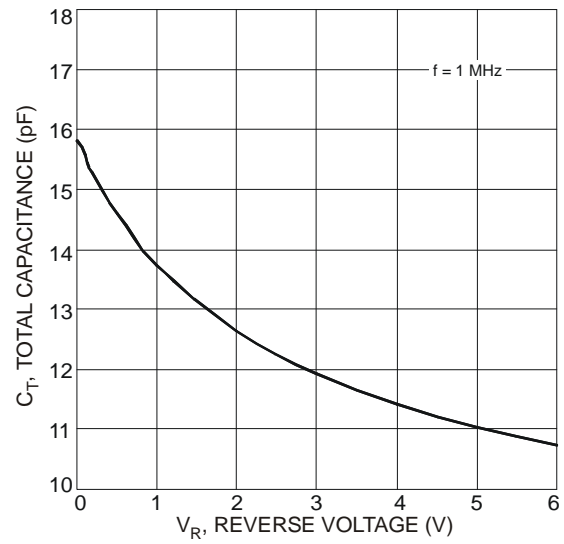
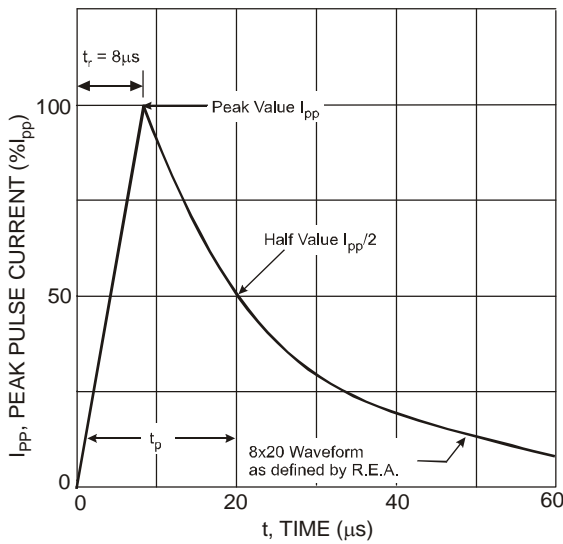
Thermal Characteristics

Characteristic	Symbol	Value	Unit
Package Power Dissipation (Note 4)	P_D	250	mW
Thermal Resistance, Junction to Ambient (Note 4)	$R_{\theta JA}$	500	$^\circ\text{C/W}$
Operating and Storage Temperature Range	T_J, T_{STG}	-65 to +150	$^\circ\text{C}$

Electrical Characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Conditions
Reverse Standoff Voltage	V_{RWM}	-	-	5	V	-
Channel Leakage Current (Note 5)	I_{RM}	-	10	100	nA	$V_{RWM} = 5\text{V}$
Clamping Voltage, Positive Transients	V_{CL}	-	7.0	9.0	V	$I_{PP} = 1\text{A}, t_p = 8/20\mu\text{s}$
		-	8.7	10.7		$I_{PP} = 3\text{A}, t_p = 8/20\mu\text{s}$
		-	10.5	12.0		$I_{PP} = 5\text{A}, t_p = 8/20\mu\text{s}$
		-	11.5	14.0		$I_{PP} = 6\text{A}, t_p = 8/20\mu\text{s}$
Breakdown Voltage	V_{BR}	6	7	8	V	$I_R = 1\text{mA}$
Differential Resistance	R_{DIF}	-	0.2	-	Ω	$I_R = 1\text{A}, t_p = 8/20\mu\text{s}$
Channel Input Capacitance	C_{IN}	-	15	20	pF	$V_R = 0\text{V}, f = 1\text{MHz}$

- Notes:
- Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at <http://www.diodes.com>.
 - Short duration pulse test used to minimize self-heating effect.



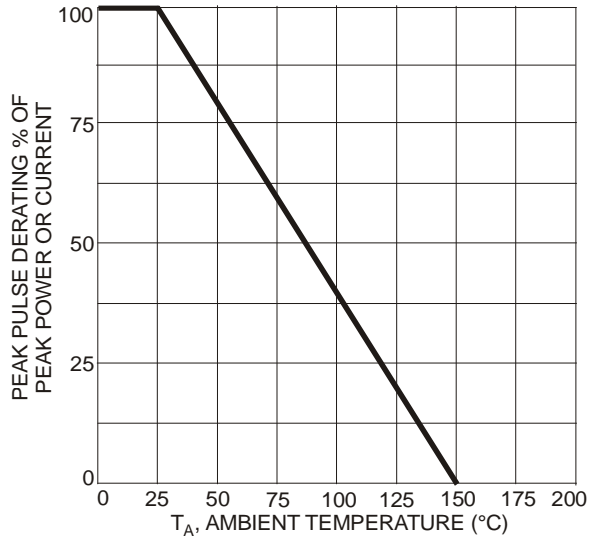


Fig. 3 Pulse Derating Curve

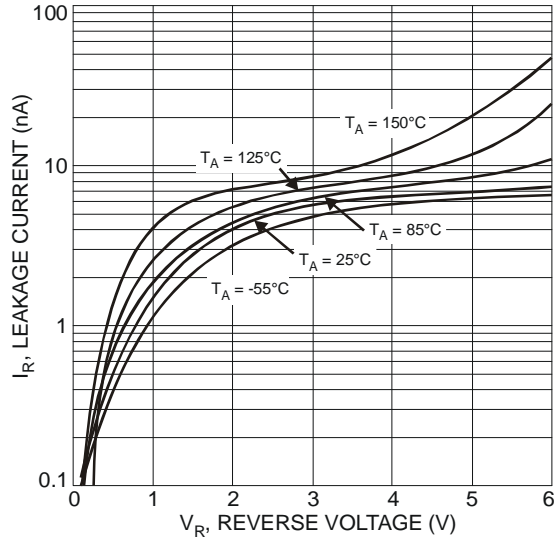
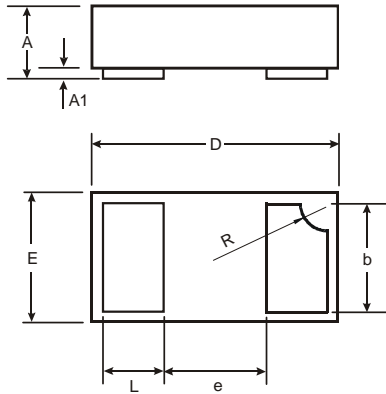


Fig. 4 Typical Reverse Characteristics

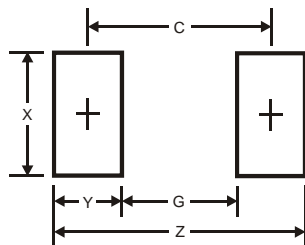
Package Outline Dimensions



X1-DFN1006-2			
Dim	Min	Max	Typ
A	0.47	0.53	0.50
A1	0	0.05	0.03
b	0.45	0.55	0.50
D	0.95	1.075	1.00
E	0.55	0.675	0.60
e	-	-	0.40
L	0.20	0.30	0.25
R	0.05	0.15	0.10

All Dimensions in mm

Suggested Pad Layout



Dimensions	Value (in mm)
Z	1.1
G	0.3
X	0.7
Y	0.4
C	0.7

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