



SPECIFICATION

• Supplier : Samsung electro-mechanics • Samsung P/N : CL03A104KO3NNNC

• Product : Multi-layer Ceramic Capacitor • Description : CAP, 100nF, 16V, ±10%, X5R, 0201

A. Samsung Part Number

<u>CL</u> <u>03</u> <u>A</u> <u>104</u> <u>K</u> <u>O</u> <u>3</u> <u>N</u> <u>N</u> <u>N</u> <u>C</u> ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨ ⑩ ⑪

① Series	Samsung Multi-layer Ceramic Capacitor		
② Size	0201 (inch code)	L: 0.6 ± 0.03 mm	W: $0.3 \pm 0.03 \text{ mm}$
③ Dielectric	X5R	8 Inner electrode	Ni
④ Capacitance	100 nF	Termination	Cu
⑤ Capacitance	±10 %	Plating	Sn 100% (Pb Free)
tolerance		Product	Normal
6 Rated Voltage	16 V	Special	Reserved for future use
① Thickness	$0.3 \pm 0.03 \text{ mm}$	① Packaging	Cardboard Type, 7" reel

B. Samsung Reliablility Test and Judgement condition

	Performance	Test condition	
Capacitance	Within specified tolerance	1klb±10% 1.0±0.2Vrms	
Tan δ (DF)	0.1 max.		
Insulation	10,000Mohm or 100Mohm⋅µF	Rated Voltage 60~120 sec.	
Resistance	Whichever is Smaller		
Appearance	No abnormal exterior appearance	Microscope (×10)	
Withstanding	No dielectric breakdown or	250% of the rated voltage	
Voltage	mechanical breakdown		
Temperature	X5R		
Characterisitcs	(From -55 ℃ to 85 ℃, Capacitance change shoud be within ±15%)		
Adhesive Strength	No peeling shall be occur on the	200g·F, for 10±1 sec.	
of Termination	terminal electrode		
Bending Strength	Capacitance change : within ±12.5%	Bending to the limit (1mm)	
		with 1.0mm/sec.	
Solderability	More than 75% of terminal surface	SnAg3.0Cu0.5 solder	
	is to be soldered newly	245±5℃, 3±0.3sec.	
		(preheating : 80~120°C for 10~30sec.)	
Resistance to	Capacitance change : within ±7.5%	Solder pot : 270±5℃, 10±1sec.	
Soldering heat	Tan δ, IR : initial spec.		

	Performance	Test condition
Vibration Test	Capacitance change: within ±5%	Amplitude : 1.5mm
	Tan δ, IR : initial spec.	From 10Hz to 55Hz (return : 1min.)
		2hours × 3 direction (x, y, z)
Moisture	Capacitance change: within ±12.5%	With rated voltage
Resistance	Tan δ : 0.2 max	40±2℃, 90~95%RH, 500+12/-0hrs
	IR : 12.5M\\overline{\alpha}\cdot \mu \F \text{ or Over}	
High Temperature	Capacitance change: within ±12.5%	With 100% of the rated voltage
Resistance	Tan δ : 0.2 max	Max. operating temperature
	IR : 25MΩ·μF or Over	
		1000+48/-0hrs
Temperature	Capacitance change: within ±7.5%	1 cycle condition
Cycling	Tan δ, IR : initial spec.	Min. operating temperatur → 25°C
		$ ightarrow$ Max. operating temperature $ ightarrow$ 25 $^{\circ}$ C
		5 cycle test

C. Recommended Soldering method :

Reflow (Reflow Peak Temperature : 260+0/-5 $^{\circ}\!\!\mathrm{C}$, 10sec. Max)

^{*} For the more detail Specification, Please refer to the Samsung MLCC catalogue.