## **BL1L Series**

## <u> 1.0 Description</u>

These delay lines offer the same excellent frequency performance as the GL delay lines but come in a solder bump configuration that takes up less board space than the GL series. As with all of TFT's delay lines, these feature stripline conductors on ceramic for high speed, high stability performance.



3.0 Mechanical -Please refer to page 37 for land pattern



## <u>3.0 Electrical</u>

Туре	BL1L (P, Q, R, S)	BL1L (W, X, Y)	
Time delay	0.1- 3.5 ns	0.10 - 0.80 ns	
Time delay increment	0.1 ns step to 3.5 ns	0.1 ns step to 0.80 ns	
Rated current	100 mA	Rise/fall time	200 ps/ns
Insulation resistance	>100 Mohm 50V	DC resistance	<1.0 ohm/ns max
Temp. coefficient of Td	0 to +150 ppm/°C	Rated voltage	100 VDC (1 min)
Time delay tolerance	±0.05 ns	Operating temperature	-40 to + 85°
Characteristic impedance	50 ±5 ohm	Storage temperature	-55 to +125°C





