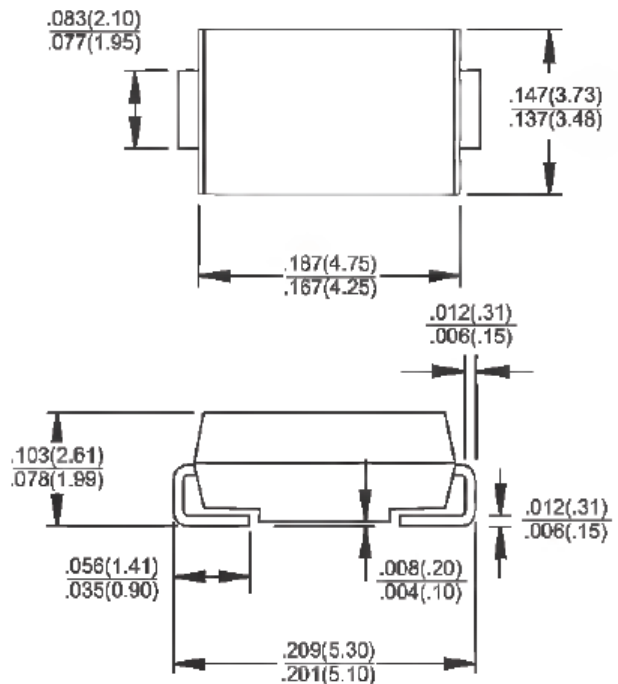




Features

- ✧ For surface mounted application
- ✧ Metal silicon junction, majority carrier conduction
- ✧ Low forward voltage drop
- ✧ Easy pick & place
- ✧ High surge current capability
- ✧ Plastic material used carriers Underwriters Laboratory Classigication 94V-0
- ✧ Epitaxial construction
- ✧ High temperature soldering guaranteed: 260°C/10s at terminals
- ✧ Green compound with suffix "G" on packing code & prefix "G" on datecode



Dimensions in inches and (millimeters)

Marking Diagram



- SL2X = Specific Device Code
- G = Green Compound
- Y = Year
- M = Work Month

Mechanical Data

- ✧ Case: Molded plastic
- ✧ Terminal: Matte tin plating
- ✧ Polarity: Indicated by cathode band
- ✧ Packaging: 12mm tape per EIA STD RS-481
- ✧ Weight: 0.093 grams

Maximum Ratings and Electrical Characteristics

Rating at 25 °C ambient temperature unless otherwise specified.

Single phase, half wave, 60 Hz, resistive or inductive load.

For capacitive load, derate current by 20%

Type Number	Symbol	SSL22	SSL23	SSL24	Unit
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	20	30	40	V
Maximum RMS Voltage	V_{RMS}	14	21	28	V
Maximum DC Blocking Voltage	V_{DC}	20	30	40	V
Maximum Average Forward Rectified Current	$I_{F(AV)}$	2			A
Peak Forward Surge Current, 8.3 ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	I_{FSM}	80			A
Maximum Instantaneous Forward Voltage (Note 1) @ 2 A	V_F	0.41			V
Maximum DC Reverse Current at Rated DC Blocking Voltage	I_R	0.4			mA
		50	60		
Typical Thermal Resistance (Note 2)	$R_{\theta JL}$	25			°C/W
	$R_{\theta JA}$	70			
Marking Code		SL22	SL23	SL24	
Operating Temperature Range	T_J	- 55 to + 125			°C
Storage Temperature Range	T_{STG}	- 55 to + 150			°C

Note 1: Pulse Test with PW=300u sec, 1% Duty Cycle

Note 2: Mount on Cu-Pad Size 10mm × 10mm on P.C.B.

RATINGS AND CHARACTERISTIC CURVES (SSL22 THRU SSL24)

FIG.1 FORWARD CURRENT DERATING CURVE

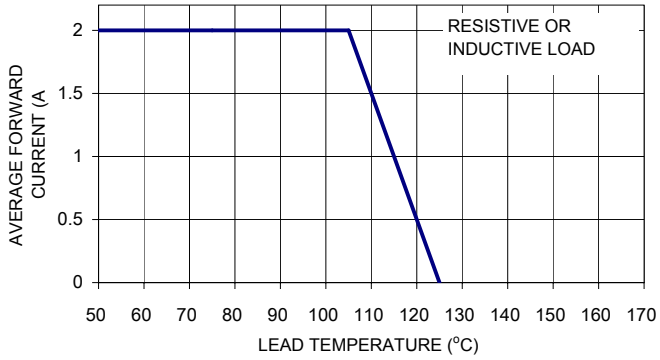


FIG. 2 MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

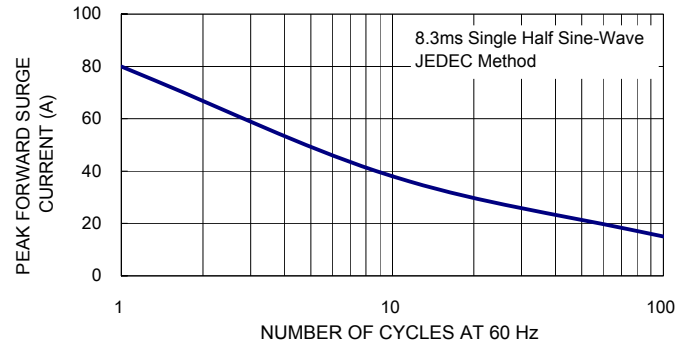


FIG. 3 TYPICAL FORWARD CHARACTERISTICS

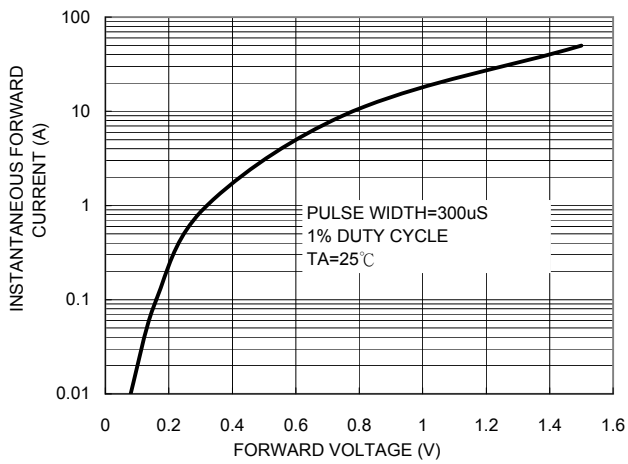


FIG. 4 TYPICAL REVERSE CHARACTERISTICS

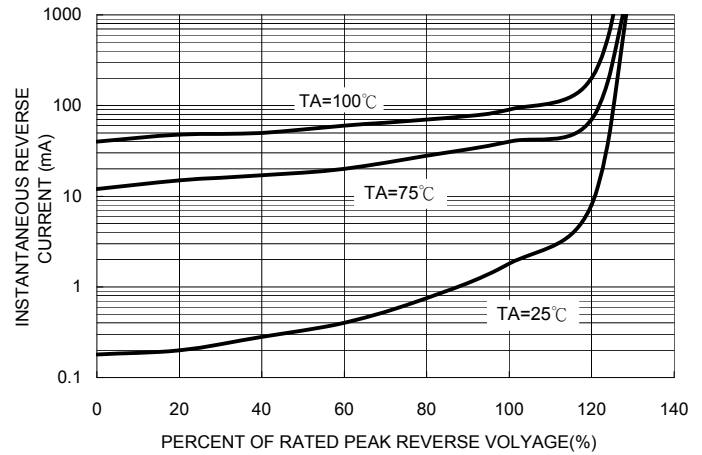


FIG. 5 TYPICAL JUNCTION CAPACITANCE

