MGBR20S150C

Preliminary

DIODE

DUAL MOS GATED BARRIER RECTIFIER

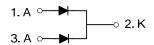
■ DESCRIPTION

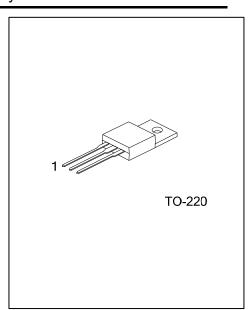
The UTC **MGBR20S150C** is a dua I mos gat ed barrier rectifiers, it uses UT C's a dvanced tech nology to provide cust omers with low forward voltage drop and high switching speed, etc.

■ FEATURES

- * Super low forward voltage drop
- * High switching speed

■ SYMBOL

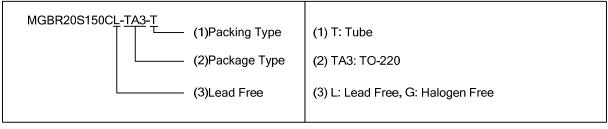




■ ORDERING INFORMATION

	Ordering Number		Dookogo	Pin Assignment			Dooking	
ĺ	Lead Free	Halogen Free	Package	1	2	3	Packing	
ſ	MGBR20S150CL-TA3-T	MGBR20S150CG-TA3-T	TO-220	Α	K	Α	Tube	

Note: Pin Assignment: A: Anode, K: Cathode



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■ ABSOLUTE MAXIMUM RATINGS (PER LEG) (T_A=25°C unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

PARAMETER SYMBOL		RATINGS	UNIT	
DC Blocking Voltage	V _{RM} 150		V	
Working Peak Reverse Voltage	V _{RWM} 150		V	
Peak Repetitive Reverse Voltage	V _{RRM} 150		V	
Average Rectified Output Current Per Device	Per Leg	lo	10 A	
	Total 2		0	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load		I _{FSM} 180		А
Operating Junction Temperature		T _J -65∼	+150	°C
Storage Temperature	T _{STG} -65	+150	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θ_{JA}	62.5	°C/W	
Junction to Case	θ_{JC}	2	°C/W	

■ ELECTRICAL CHARACTERISTICS (PER LEG) (T_A =25°C unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	$V_{(BR)R}$	I _R =0.50mA 150				٧
Forward Valtage Dress	VEM	I _F =10A, T _J =25°C			0.80	V
rward Voltage Drop		I _F =10A, T _J =125°C			0.75	V
akage Current (Note 1)	I DM	V _R =150V, T _J =25°C			100	μΑ
		V _R =150V, T _J =125°C			30	mA

Notes: 1. Short duration pulse test used to minimize self-heating effect.

2. Thermal resistance junction to case mounted on heatsink.

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