

### UMC3N / FMC3A

#### Features

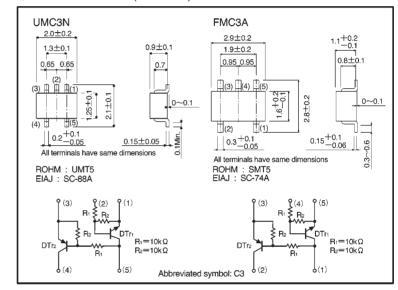
- Both the DTA114E chip and DTC114E chip in a UMT or SMT package.
- 2) Ideal for power switch circuits.
- 3) Mounting cost and area can be cut in half.

# ●Structure Epitaxial planar type NPN/PNP TY transistor

(Built-in resistor type)

The following characteristics apply to both DTr<sub>1</sub> and DTr<sub>2</sub>, however, the "–" sign on DTr<sub>2</sub> values for the PNP type have been omitted.

#### External dimensions (Units: mm)



#### ● Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit	
Supply voltage		Vcc	50	V	
Input voltage		Vin	40	V	
		VIN	-10		
Output current		lo	50	mA	
		IC(Max.)	100		
Power dissipation	UMC3N	Dd	Pd 150(TOTAL)		
	FMC3A	Fu	300 (TOTAL)	mW *2	
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	<b>−55∼+150</b>	Ĉ	

<sup>\*1 120</sup>mW per element must not be exceeded.

<sup>\*2 200</sup>mW per element must not be exceeded.



## UMC3N / FMC3A

#### •Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
lanut valtage	VI (off)	_	_	0.5	.,	Vcc=5V, lo=100 μA	
Input voltage	VI (on)	3	_	_	V	Vo=0.3V, lo=10mA	
Output voltage	Vo(on)	_	0.1	0.3	V	Io=10mA, I:=0.5mA	
Input current	lı	_	_	0.88	mA	V1=5V	
Output current	lo (off)	_	_	0.5	μΑ	Vcc=50V, Vi=0V	
DC current gain	Gı	30	_	_	_	V₀=5V, I₀=5mA	
Transition frequency	fτ	_	250	_	MHz	VcE=10mA, IE=-5mA, f=100MHz*	
Input resistance	R <sub>1</sub>	7	10	13	kΩ	_	
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	0.8	1	1.2	_	_	

<sup>\*</sup> Transition frequency of the device

#### Packaging specifications

	Packaging type	Tap	oing
	Code	TR	T148
Part No.	Basic ordering unit (pieces)	3000	3000
UMC3N		0	_
FMC3A		_	0