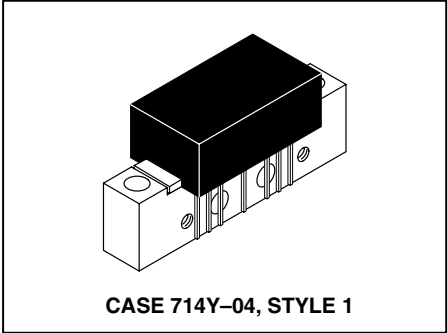


Will be replaced by MHW8185N by end of Q206. N suffix indicates RoHS compliant part.

MHW8185

**860 MHz
 19.4 dB GAIN
 128-CHANNEL
 CATV AMPLIFIER MODULE**



CATV Amplifier Module

Features

- Specified for 77-, 110- and 128-Channel Loading
- Excellent Distortion Performance
- Silicon Bipolar Transistor Technology
- Unconditionally Stable Under All Load Conditions

Applications

- CATV Systems Operating in the 40 to 860 MHz Frequency Range
- Output Stage Amplifier in Optical Nodes, Line Extenders and Trunk Distribution Amplifiers for CATV Systems
- Driver Amplifier in Linear General Purpose Applications

Description

- 24 Vdc Supply, 40 to 860 MHz, CATV Forward Power Doubler Amplifier Module

Table 1. Maximum Ratings

Rating	Symbol	Value	Unit
RF Voltage Input (Single Tone)	V_{in}	+70	dBmV
DC Supply Voltage	V_{CC}	+28	Vdc
Operating Case Temperature Range	T_C	-20 to +100	°C
Storage Temperature Range	T_{stg}	-40 to +100	°C

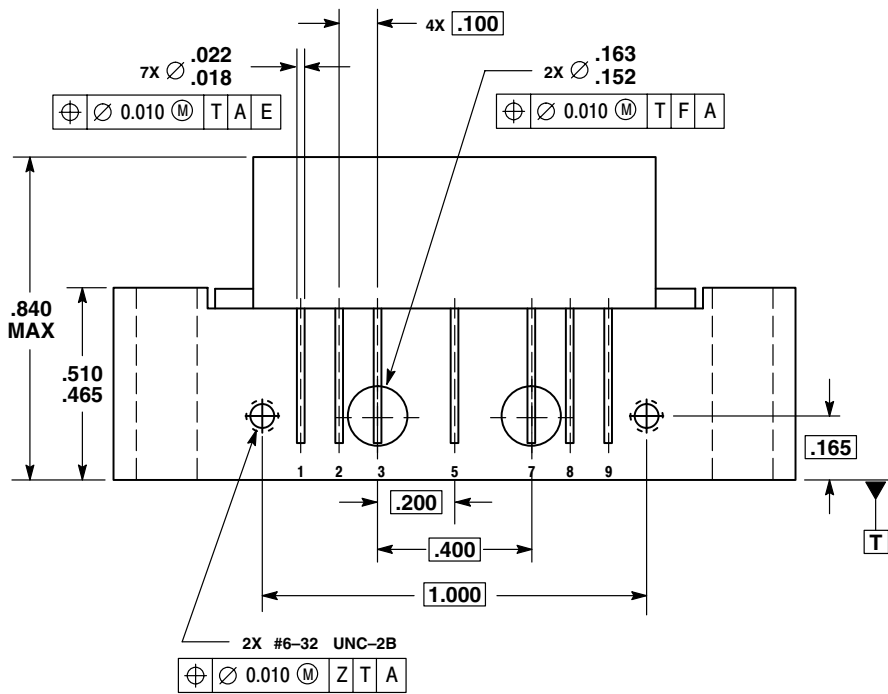
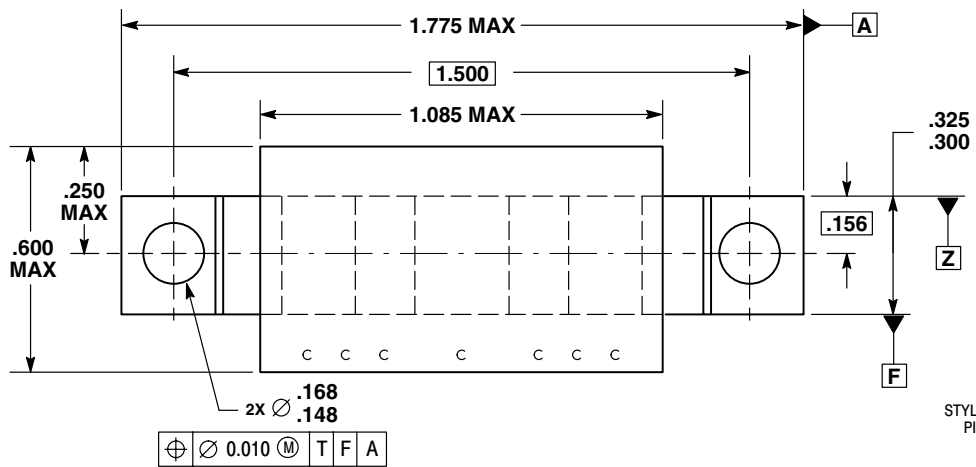
Table 2. Electrical Characteristics ($V_{CC} = 24$ Vdc, $T_C = +30^\circ\text{C}$, 75 Ω system unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
Frequency Range	BW	40	—	860	MHz
Power Gain	G_p	18.3	18.8	19.3	dB
		19	19.4	20.5	
Slope	S	0	.5	1.5	dB
Gain Flatness (40–860 MHz, Peak to Valley)	G_F	—	0.3	1.0	dB
Return Loss — Input/Output ($Z_o = 75$ Ohms)	IRL/ORL				
		19	—	—	dB
		—	—	0.006	dB/MHz
Composite Second Order					dBc
($V_{out} = +40$ dBmV/ch., Worst Case)	CSO_{128}	—	-70	-62	
($V_{out} = +44$ dBmV/ch., Worst Case)	CSO_{110}	—	-72	-64	
	CSO_{77}	—	-80	-68	
Cross Modulation Distortion @ Ch 2					dBc
($V_{out} = +40$ dBmV/ch., FM = 55 MHz)	XMD_{128}	—	-72	-64	
($V_{out} = +44$ dBmV/ch., FM = 55 MHz)	XMD_{110}	—	-67	-63	
	XMD_{77}	—	-70	-68	

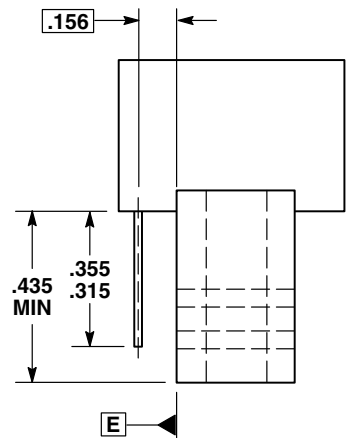
Table 2. Electrical Characteristics ($V_{CC} = 24 \text{ Vdc}$, $T_C = +30^\circ\text{C}$, 75Ω system unless otherwise noted) (continued)

Characteristic		Symbol	Min	Typ	Max	Unit
Composite Triple Beat ($V_{out} = +40 \text{ dBmV/ch.}$, Worst Case) ($V_{out} = +44 \text{ dBmV/ch.}$, Worst Case)	128-Channel FLAT	CTB_{128}	—	-67	-64	dBc
	110-Channel FLAT	CTB_{110}	—	-64	-62	
	77-Channel FLAT	CTB_{77}	—	-71	-69	
Noise Figure	50 MHz	NF	—	5.0	6.0	dB
	550 MHz		—	5.8	—	
	750 MHz		—	6.2	—	
	860 MHz		—	7.0	8.0	
DC Current ($V_{DC} = 24 \text{ V}$, $T_C = 30^\circ\text{C}$)		I_{DC}	365	400	435	mA

PACKAGE DIMENSIONS



- STYLE 1:
 PIN 1. RF INPUT
 2. GROUND
 3. GROUND
 4. DELETED
 5. VDC
 6. DELETED
 7. GROUND
 8. GROUND
 9. RF OUTPUT



- NOTES:
 1. INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: INCH.

**CASE 714Y-04
 ISSUE E**

How to Reach Us:

Home Page:

www.freescale.com

E-mail:

support@freescale.com

USA/Europe or Locations Not Listed:

Freescale Semiconductor
Technical Information Center, CH370
1300 N. Alma School Road
Chandler, Arizona 85224
+1-800-521-6274 or +1-480-768-2130
support@freescale.com

Europe, Middle East, and Africa:

Freescale Halbleiter Deutschland GmbH
Technical Information Center
Schatzbogen 7
81829 Muenchen, Germany
+44 1296 380 456 (English)
+46 8 52200080 (English)
+49 89 92103 559 (German)
+33 1 69 35 48 48 (French)
support@freescale.com

Japan:

Freescale Semiconductor Japan Ltd.
Headquarters
ARCO Tower 15F
1-8-1, Shimo-Meguro, Meguro-ku,
Tokyo 153-0064
Japan
0120 191014 or +81 3 5437 9125
support.japan@freescale.com

Asia/Pacific:

Freescale Semiconductor Hong Kong Ltd.
Technical Information Center
2 Dai King Street
Tai Po Industrial Estate
Tai Po, N.T., Hong Kong
+800 2666 8080
support.asia@freescale.com

For Literature Requests Only:

Freescale Semiconductor Literature Distribution Center
P.O. Box 5405
Denver, Colorado 80217
1-800-441-2447 or 303-675-2140
Fax: 303-675-2150
LDCForFreescaleSemiconductor@hibbertgroup.com

Information in this document is provided solely to enable system and software implementers to use Freescale Semiconductor products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits or integrated circuits based on the information in this document.

Freescale Semiconductor reserves the right to make changes without further notice to any products herein. Freescale Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Freescale Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters that may be provided in Freescale Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals", must be validated for each customer application by customer's technical experts. Freescale Semiconductor does not convey any license under its patent rights nor the rights of others. Freescale Semiconductor products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Freescale Semiconductor product could create a situation where personal injury or death may occur. Should Buyer purchase or use Freescale Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold Freescale Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Freescale Semiconductor was negligent regarding the design or manufacture of the part.

Freescale™ and the Freescale logo are trademarks of Freescale Semiconductor, Inc. All other product or service names are the property of their respective owners.
© Freescale Semiconductor, Inc. 2006. All rights reserved.

