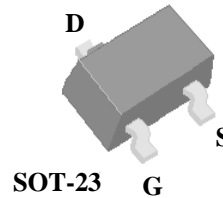
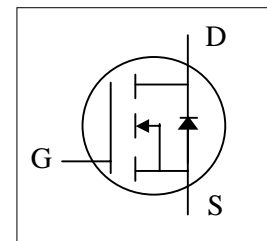


AP2302AGN-HF

- ▼ Capable of 2.5V gate drive
- ▼ Lower Gate Charge
- ▼ Surface mount package
- ▼ RoHS Compliant & Halogen-Free



BV_{DSS}	20V
$R_{DS(ON)}$	42m Ω
I_D	4.6A



Description

Advanced Power MOSFETs utilized advanced processing techniques to achieve the lowest possible on-resistance, extremely efficient and cost-effectiveness device.

The SOT-23 package is widely used for all commercial-industrial applications.

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	20	V
V_{GS}	Gate-Source Voltage	± 8	V
$I_D @ T_A = 25^\circ C$	Continuous Drain Current ³ , $V_{GS} @ 4.5V$	4.6	A
$I_D @ T_A = 70^\circ C$	Continuous Drain Current ³ , $V_{GS} @ 4.5V$	3.7	A
I_{DM}	Pulsed Drain Current ¹	20	A
$P_D @ T_A = 25^\circ C$	Total Power Dissipation	1.38	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Value	Unit
Rthj-a	Maximum Thermal Resistance, Junction-ambient ³	90	$^\circ C/W$



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Electrical Characteristics @T_j=25°C (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	20	-	-	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =4.5V, I _D =4A	-	-	42	mΩ
		V _{GS} =2.5V, I _D =3A	-	-	60	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	0.3	-	1.2	V
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =4A	-	14	-	S
I _{DSS}	Drain-Source Leakage Current	V _{DS} =16V, V _{GS} =0V	-	-	10	uA
I _{GSS}	Gate-Source Leakage	V _{GS} =±8V, V _{DS} =0V	-	-	±100	nA
Q _g	Total Gate Charge ²	I _D =4A	-	6.5	10.5	nC
Q _{gs}	Gate-Source Charge	V _{DS} =10V	-	1	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =4.5V	-	2.5	-	nC
t _{d(on)}	Turn-on Delay Time ²	V _{DS} =10V	-	9	-	ns
t _r	Rise Time	I _D =1A	-	12	-	ns
t _{d(off)}	Turn-off Delay Time	R _G =3.3Ω	-	16	-	ns
t _f	Fall Time	V _{GS} =5V	-	5	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V	-	300	480	pF
C _{oss}	Output Capacitance	V _{DS} =20V	-	85	-	pF
C _{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	80	-	pF
R _g	Gate Resistance	f=1.0MHz	-	2	-	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V _{SD}	Forward On Voltage ²	I _S =1.2A, V _{GS} =0V	-	-	1.2	V
t _{rr}	Reverse Recovery Time ²	I _S =4A, V _{GS} =0V,	-	20	-	ns
Q _{rr}	Reverse Recovery Charge	dI/dt=100A/μs	-	10	-	nC