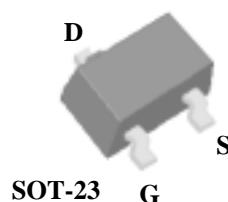


AP2301GN

- ▼ Simple Drive Requirement
- ▼ Small Package Outline
- ▼ Surface Mount Device

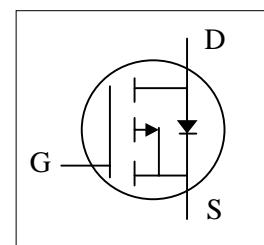


BV_{DSS}	-20V
$R_{DS(ON)}$	130mΩ
I_D	- 2.6A

Description

The Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, low on-resistance and cost-effectiveness.

The SOT-23 package is universally preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	- 20	V
V_{GS}	Gate-Source Voltage	± 12	V
$I_D @ T_A=25^\circ C$	Continuous Drain Current ³	-2.6	A
$I_D @ T_A=70^\circ C$	Continuous Drain Current ³	-2.1	A
I_{DM}	Pulsed Drain Current ^{1,2}	-10	A
$P_D @ T_A=25^\circ C$	Total Power Dissipation	1.38	W
	Linear Derating Factor	0.01	W/°C
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Value	Unit
R_{thj-a}	Thermal Resistance Junction-ambient ³	Max. 90	°C/W

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Electrical Characteristics@ $T_j=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=-250\mu\text{A}$	-20	-	-	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_j$	Breakdown Voltage Temperature Coefficient	Reference to 25°C , $I_{\text{D}}=-1\text{mA}$	-	-0.1	-	$\text{V}/^\circ\text{C}$
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance ²	$V_{\text{GS}}=-5\text{V}$, $I_{\text{D}}=-2.8\text{A}$	-	-	130	$\text{m}\Omega$
		$V_{\text{GS}}=-2.8\text{V}$, $I_{\text{D}}=-2.0\text{A}$	-	-	190	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{D}}=-250\mu\text{A}$	-0.5	-	-	V
g_{fs}	Forward Transconductance	$V_{\text{DS}}=-5\text{V}$, $I_{\text{D}}=-2.8\text{A}$	-	4.4	-	S
I_{DSS}	Drain-Source Leakage Current ($T_j=25^\circ\text{C}$)	$V_{\text{DS}}=-20\text{V}$, $V_{\text{GS}}=0\text{V}$	-	-	-1	uA
	Drain-Source Leakage Current ($T_j=70^\circ\text{C}$)	$V_{\text{DS}}=-16\text{V}$, $V_{\text{GS}}=0\text{V}$	-	-	-10	uA
I_{GSS}	Gate-Source Leakage	$V_{\text{GS}}=\pm 12\text{V}$	-	-	± 100	nA
Q_g	Total Gate Charge ²	$I_{\text{D}}=-2.8\text{A}$	-	5.2	10	nC
Q_{gs}	Gate-Source Charge	$V_{\text{DS}}=-6\text{V}$	-	1.36	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	$V_{\text{GS}}=-5\text{V}$	-	0.6	-	nC
$t_{\text{d(on)}}$	Turn-on Delay Time ²	$V_{\text{DS}}=-15\text{V}$	-	5.2	-	ns
t_r	Rise Time	$I_{\text{D}}=-1\text{A}$	-	9.7	-	ns
$t_{\text{d(off)}}$	Turn-off Delay Time	$R_G=6\Omega$, $V_{\text{GS}}=-10\text{V}$	-	19	-	ns
t_f	Fall Time	$R_D=15\Omega$	-	29	-	ns
C_{iss}	Input Capacitance	$V_{\text{GS}}=0\text{V}$	-	295	-	pF
C_{oss}	Output Capacitance	$V_{\text{DS}}=-6\text{V}$	-	170	-	pF
C_{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	65	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
I_s	Continuous Source Current (Body Diode)	$V_D=V_G=0\text{V}$, $V_S=-1.2\text{V}$	-	-	-1	A
I_{SM}	Pulsed Source Current (Body Diode) ¹		-	-	-10	A
V_{SD}	Forward On Voltage ²	$T_j=25^\circ\text{C}$, $I_s=-1.6\text{A}$, $V_{\text{GS}}=0\text{V}$	-	-	-1.2	V