DC/DC regulator
Input 3.0 - 5.5 V
Output 16 A

Contents

Key Features

- Wide input, 3.0-5.5 Vdc
- Programmable output, 0.75 - 3.6 Vdc
- Under voltage protection
- Short circuit protection
- Remote sense
- Remote On/Off
- Design for Environment (DfE)
- European Commission Directive 2002/95/EC (RoHs) compliant

The PMC series of surface mount DC/DC regulators (POL) are intended to be used as local distributed power sources in distributed power architecture level 4. The high efficiency and high reliability of the PMC series makes them particularly suited for the communications equipment of today and tomorrow.

These products are manufactured using the most advanced technologies and materials to comply with environmental requirements. Designed to meet high reliability requirements of systems manufacturers, the PMC responds to world-class specifications.

Ericsson Power Modules is an ISO 9001/14001 certified supplier.

ERICSSON

Datasheet

Product Program

V_I	V_O/I_O max	P_O max	Ordering No.	Comment
	Output 1			
3.0-5.5 V*	0.75-3.6 V/16 A	52.8 W	PMC 4518T WS	Standard version
3.0-5.5 V	1.0 V/16 A	16.0 W	PMC 4118NA WS	On request
3.0-5.5 V	1.2 V/16 A	19.2 W	PMC 4118LA WS	On request
3.0-5.5 V	1.5 V/16 A	24.0 W	PMC 4218H WS	On request
3.0-5.5 V	1.8 V/16 A	28.8 W	PMC 4218G WS	On request
3.0-5.5 V	2.5 V/16 A	40.0 W	PMC 4419 WS	On request
3.8-5.5 V	3.3 V/16 A	52.8 W	PMC 4510 WS	On request

Option	Suffix	Example
Negative Remote Control logic	N	PMC 4518T WSN

* Input voltage limited to 3.8-5.5V for 3.3 Vout and for output voltages of 3.3V and 4.5-5.5 for output voltages above 3.3V.
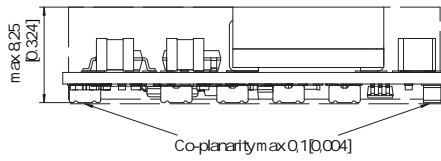
Ordering Information

Delivery option	M.o.q.	Suffix	Example
Tape & Reel	200 pcs	/C	PMC 4xxxT WS /C

Mechanical Data

Bottom View

Co-planarity max 0,1 [0,004]
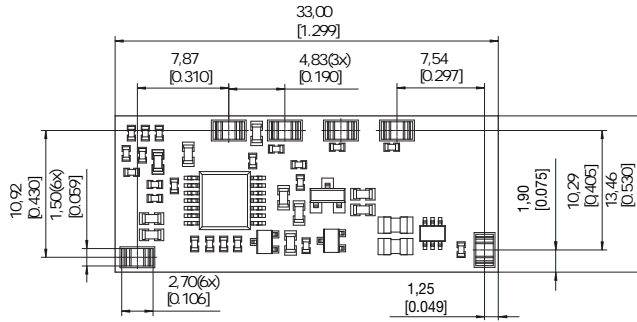
Dimensions in mm [inch]
Tolerances (unless specified):
x,xx ±0,25 [0,01]
Pin true position within 0,4 [0,016]
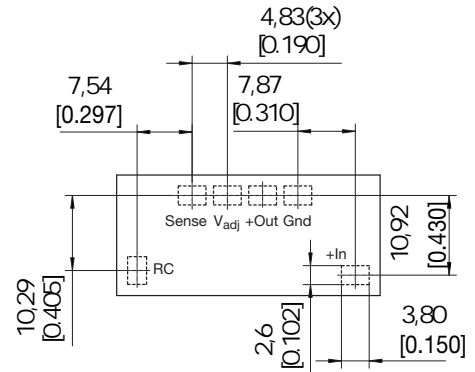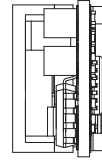
Recommended footprint.
Top View

Connections

Pin	Designation	Function
1	RC	Remote Control
2	+ In	Positive input
3	Gnd	Ground
4	+ Out	Positive output
5	Vadj	External output adjust
6	+ Sense	Positive remote sense

+Sense Vadj +Out Gnd

RC   +In
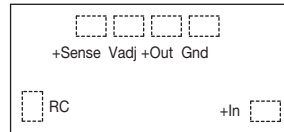
Weight

7 g

Pins

Material: Copper
Plating: Flash gold over nickel

Absolute Maximum Ratings

Characteristics		min	typ	max	Unit
T_{ref}	Operating Reference Temperature, see pg. 27	-45		+115	℃
T_S	Storage temperature	-55		+125	℃
V_I	Input voltage	-0.3		+5.5	Vdc

Stress in excess of Absolute Maximum Ratings may cause permanent damage.
Absolute Maximum Ratings, sometimes referred to as no destruction limits, are
normally tested with one parameter at a time exceeding the limits of Output data
or Electrical Characteristics. If exposed to stress above these limits, function and
performance may degrade in an unspecified manner.

Input

T_{ref} = -30 ... +90 ℃, V_I = 3.0...5.5 V unless otherwise specified
Typ values specified at: T_{ref} = +25 ℃, $V_I$nom, $I_o$max = 16 A

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		3.0		5.5	Vdc
V_{Ioff}	Turn-off input voltage	Ramp from higher voltage, V_{out} = 1.0- 2.5 V, V_{in} = 3.3 V		2.3		Vdc
		Ramp from higher voltage, V_{out} = 3.3 V, V_{in} = 5.0 V		3.4		
V_{Ion}	Turn-on input voltage	Ramp from lower voltage, V_{out} = 1.0- 2.5 V, V_{in} = 3.3 V		2.4		Vdc
		Ramp from lower voltage, V_{out} = 3.3 V, V_{in} = 5.0 V		3.5		
C_I	Input capacitance			20		µF
P_{Ii}	Input idling power	I_o = 0 A, V_I = 5.5 V			680	mW
P_{RC}	Input stand-by power (RC active)	Non operation, V_I = 5 V			7.5	mW
V_{Iac}	Input ripple [1]	20 Hz ... 5 MHz, $I_o$max, V_I = 5 V		400		mV

[1] Measured with 2 x 22 µF ceramic capacitors

Fundamental Circuit Diagram
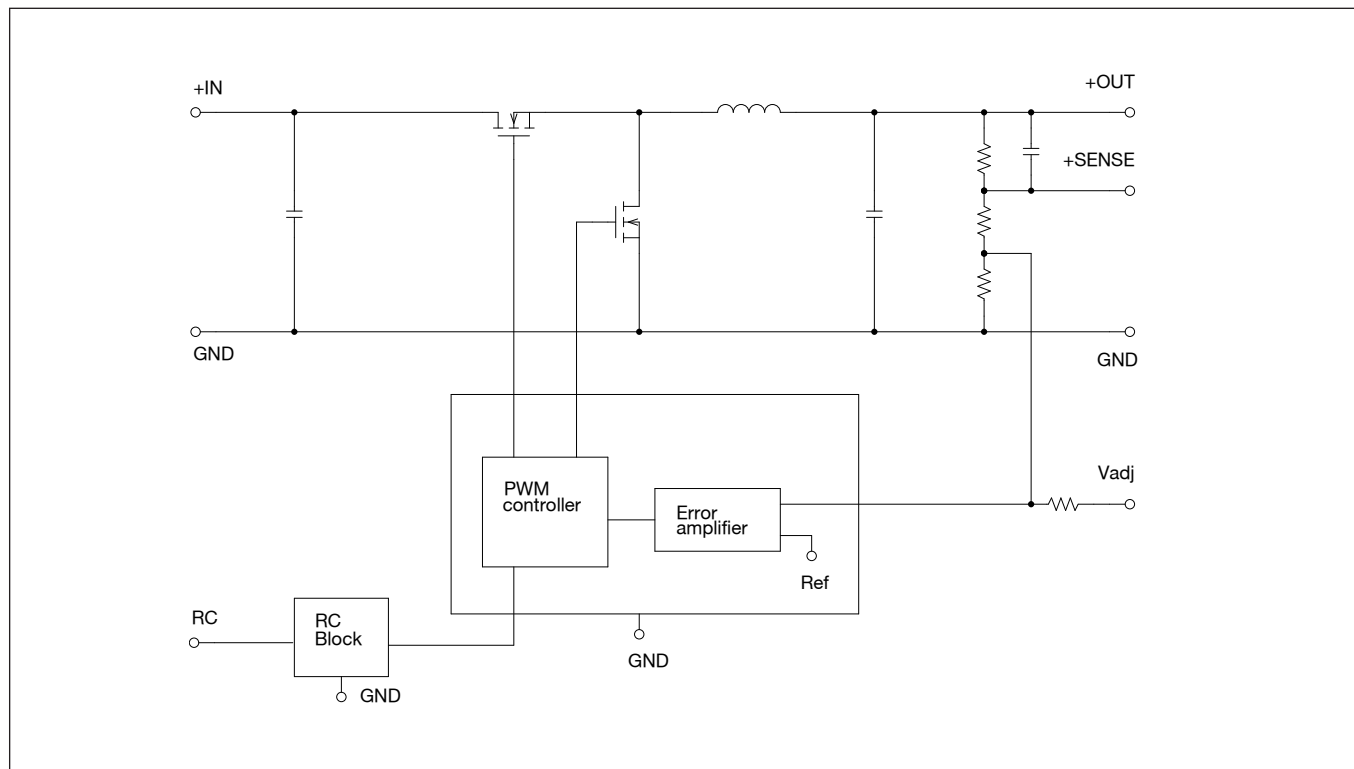
Product Qualification Specification

Characteristics			
Random Vibration	JESD 22-B103-B	Frequency Acceleration density	2 ... 500 Hz 0.008 ... 0.2 g^2/Hz
Sinusoidal vibration	JESD 22-B103-B	Frequency Acceleration	10 ... 1000 Hz 10 g
Mechanical shock (half sinus)	JESD 22-B104-B	Peak acceleration Duration	200 g 1.5 ms
Lead integrity	JESD 22-B105-C	Weight of 1000 g	All terminals
Temperature cycling	JESD 22-A104-B	Temperature Number of cycles	-40 ... +125 °C 300
Accelerated damp heat	JESD 22-A101-B	Temperature Humidity Duration Bias	+85 °C 85 % RH 1000 hours max input voltage
Solderability	IEC 60068-2-54 (Aged according to JESD 22-A101-B, 240h no bias)	Solder immersion depth Time for onset of wetting Wetting force	1 mm < 4 s > 100 mN/m
Cold (in operation)	IEC 60068-2-1A, test A_d	Temperature Duration	-45 °C 72 h
High temperature storage	JESD 22-A103-B	Temperature Duration	+125 °C 1000 h

Safety Specification

General information.

Ericsson Power Modules DC/DC converters and DC/DC regulators are designed in accordance with safety standards IEC/EN/UL 60 950, *Safety of Information Technology Equipment.*

IEC/EN/UL60950 contains requirements to prevent injury or damage due to the following hazards:

- *Electrical shock*
- *Energy hazards*
- *Fire*
- *Mechanical and heat hazards*
- *Radiation hazards*
- *Chemical hazards*

On-board DC-DC converters are defined as component power supplies. As components they cannot fully comply with the provisions of any Safety requirements without "Conditions of Acceptability". It is the responsibility of the installer to ensure that the final product housing these components complies with the requirements of all applicable Safety standards and Directives for the final product.

Component power supplies for general use should comply with the requirements in IEC60950, EN60950 and UL60950 "Safety of information technology equipment".

There are other more product related standards, e.g. IEC61204-7 "Safety standard for power supplies", IEEE802.3af "Ethernet LAN/MAN Data terminal equipment power", and ETS300132-2 "Power supply interface at the input to telecommunications equipment; part 2: DC", but all of these standards are based on IEC/EN/UL60950 with regards to safety.

Ericsson Power Modules DC/DC converters and DC/DC regulators are UL 60 950 recognized and certified in accordance with EN 60 950.

The flammability rating for all construction parts of the products meets UL 94V-0.

The products should be installed in the end-use equipment, in accordance with the requirements of the ultimate application. Normally the output of the DC/DC converter is considered as SELV (Safety Low Voltage) and the input source must be isolated by minimum Double or Reinforced Insulation from the primary circuit (AC mains) in accordance with IEC/EN/UL 60 950.

Isolated DC/DC converters.

It is recommended that a fast blow fuse with a rating twice the maximum input current per selected product be used at the input of each DC/DC converter. If an input filter is used in the circuit the fuse should be placed in front of the input filter.
In the rare event of a component problem in the input filter or in the DC/DC converter that imposes a short circuit on the input source, this fuse will provide the following functions:

- Isolate the faulty DC/DC converter from the input power source so as not to affect the operation of other parts of the system.
- Protect the distribution wiring from excessive current and power loss thus preventing hazardous overheating.

The galvanic isolation is verified in an electric strength test. The test voltage (V_{ISO}) between input and output is 1500 Vdc or 2250 Vdc for 60 seconds (refer to product specification). Leakage current is less than 1μA at nominal input voltage.

24 V dc systems.

The input voltage to the DC/DC converter is SELV (Safety Extra Low Voltage) and the output remains SELV under normal and abnormal operating conditions.

48 and 60 V dc systems.

If the input voltage to Ericsson Power Modules DC/DC converter is 75 V dc or less, then the output remains SELV (Safety Extra Low Voltage) under normal and abnormal operating conditions.

Single fault testing in the input power supply circuit should be performed with the DC/DC converter connected to demonstrate that the input voltage does not exceed 75 V dc.

If the input power source circuit is a DC power system, the source may be treated as a TNV2 circuit and testing has demonstrated compliance with SELV limits and isolation requirements equivalent to Basic Insulation in accordance with IEC/EN/UL 60 950.

Non-isolated DC/DC regulators.

The input voltage to the DC/DC regulator is SELV (Safety Extra Low Voltage) and the output remains SELV under normal and abnormal operating conditions.

It is recommended that a slow blow fuse with a rating twice the maximum input current per selected product be used at the input of each DC/DC regulator.
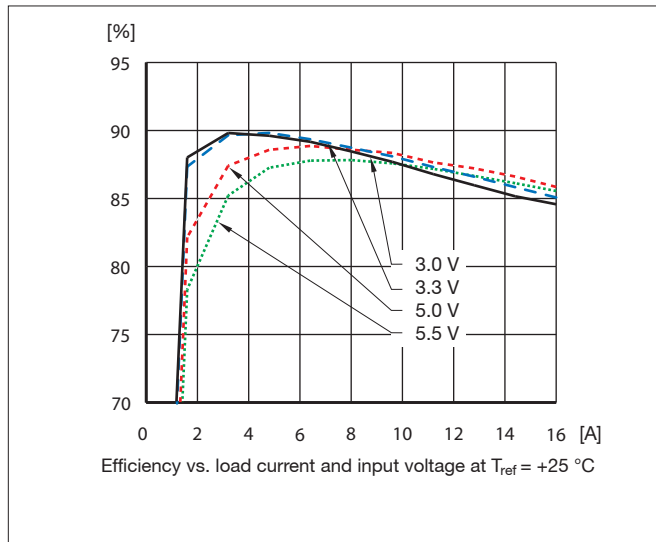
Adjusted to 1.0 V out - Data

T_{ref} = -30 ... +90 °C, V_I = 3.0 ... 5.5 V unless otherwise specified. Input filter 2 x 22 µF, Output filter 1 x 150 µF

Typ values specified at: T_{ref} = +25 °C and $V_I$nom. $I_O$max = 16 A. Note: +Sense connected to +Out

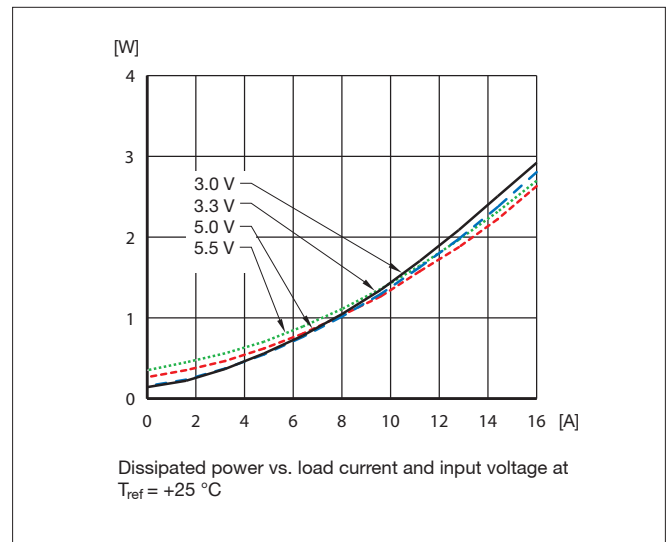| Characteristics		Conditions	Output			Unit
			min	typ	max	
V_{Oi}	Output voltage adjusted setting	T_{ref} = +25 °C, $V_I$nom, $I_O$max, R_{adj} 80 kΩ	0.98	1.00	1.02	V
V_O	Output voltage tolerance band	I_O = 0.01...1.0 x $I_O$max	0.97		1.03	V
	Idling voltage	I_O = 0	0.98		1.02	V
	Line regulation	$V_I$min ... $V_I$max, $I_O$max		11		mV
	Load regulation	I_O = 0.01...1.0 x $I_O$max, $V_I$nom		10		mV
V_{tr}	Load transient voltage deviation	Load step = 0.25 ... 0.75 x $I_O$max, dI/dt = 5 A/µs, C_O = 150 µF, V_I = 3.3 V		120		mV
t_{tr}	Load transient recovery time			60		µs
T_{coeff}	Temperature coefficient	T_{ref} = -30 ... +90 °C, $I_O$max	-0.1	0	+0.1	mV/°C
t_s	Start-up time	From V_I connected to V_O = 0.9 x V_{OI}, I_O = 0.1 ...1.0 x $I_O$max, $V_I$nom		8.5		ms
t_r	Ramp-up time	0.1...0.9 x V_O, I_O = 0.1 ...1.0 x $I_O$max, $V_I$nom		4		ms
t_r	Fall time, V_O x 0.1	I_O = $I_O$max, $V_I$nom		0.2		ms
t_r	Fall time, V_O x 0.1	I_O = 0 A, $V_I$nom		5		s
t_{RC}off	RC shut-down time to V_O x 0.1	I_O = $I_O$max, $V_I$nom		0.2		ms
t_{RC}on	RC start-up time to V_O x 0.9	I_O = $I_O$max, $V_I$nom		8		ms
t_{RC}	RC fall time, V_O x 0.1 ... 0.9	I_O = 0 A, $V_I$nom		10		s
I_O	Output current		0		16	A
$P_O$max	Max output power	At V_O = $V_O$nom	16			W
I_{lim}	Current limit threshold	T_{ref} < T_{ref}max	22	28	35	A
V_{Oac}	Output ripple	20 Hz ... 5 MHz, $I_O$max		10	20	mV$_{p-p}$
η	Efficiency - 50% load	I_O = 0.5 x $I_O$max, V_I = 5 V		88.6		%
η	Efficiency - 100% load	I_O = $I_O$max, V_I = 5 V	84.4	86		%
P_d	Power Dissipation	I_O = $I_O$max, V_I = 5 V		2.6	3.0	W
Fo	Switching frequency	I_O = 0 ... 1.0 x $I_O$max	250	300	350	kHz
I_{sense}	Remote sense current				8	mA
I_I	Static input current	V_I = 3.0 V, I_O = $I_O$max, T_{ref} = 25 °C		6.3		A
MTBF	Predicted reliability	T_{ref} = 40 °C		6		million hours

Adjusted to 1.0 V out - Typical Characteristics

General conditions: Input filter 2 x 22 µF, Output filter 1 x 150 µF

Efficiency

[%]

Efficiency vs. load current and input voltage at T_{ref} = +25 °C

Power Dissipation

[W]

Dissipated power vs. load current and input voltage at T_{ref} = +25 °C

Output Current Derating at 3.3 V input

[A]

3.0 m/s (600 lfm)
2.5 m/s (500 lfm)
2.0 m/s (400 lfm)
1.5 m/s (300 lfm)
1.0 m/s (200 lfm)
Nat. Conv.

Available load current vs. ambient air temperature and airflow at Vin = 3.3 V. See conditions on page 27.

Output Current Derating at 5 V input

[A]

3.0 m/s (600 lfm)
2.5 m/s (500 lfm)
2.0 m/s (400 lfm)
1.5 m/s (300 lfm)
1.0 m/s (200 lfm)
Nat. Conv.

Available load current vs. ambient air temperature and airflow at Vin = 5 V. See conditions on page 27.

Output Characteristics

[V]

3.0 V
3.3 V
5.0 V
5.5 V

Output voltage vs. load current.

Start-Up

Start-up at I_O = 16 A resistive load at T_{ref} = +25 °C, Vin = 3.3 V. Start enabled by connecting V_{in}.
Top trace: output voltage (0.5 V/div.).
Bottom trace: input voltage (2 V/div.).
Time scale: 2 ms/div.

Adjusted to 1.0 V out - Typical Characteristics

General conditions: Input filter 2 x 22 μF, Output filter 1 x 150 μF

Turn Off

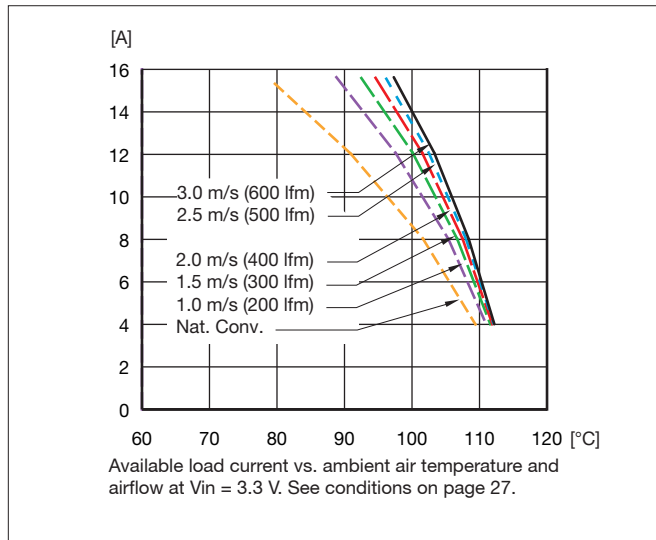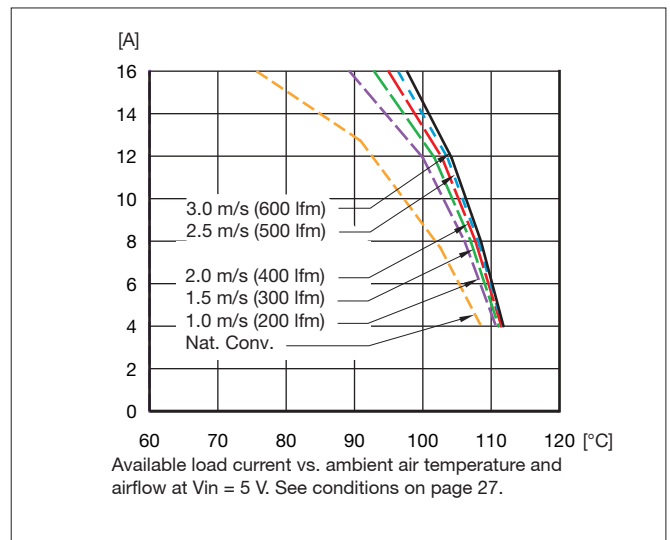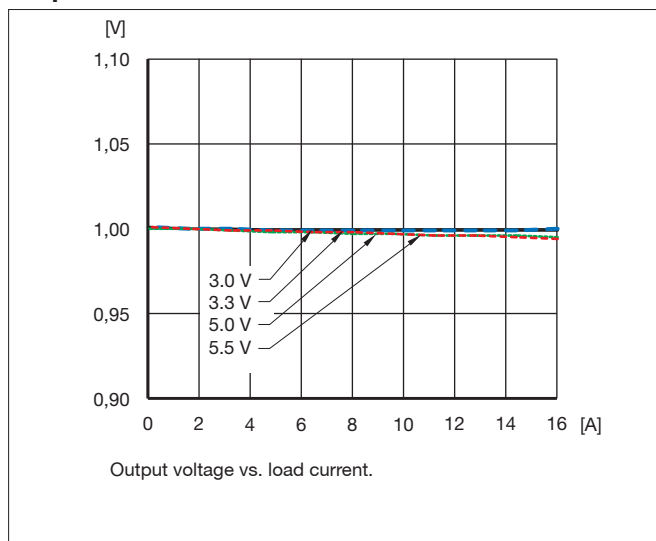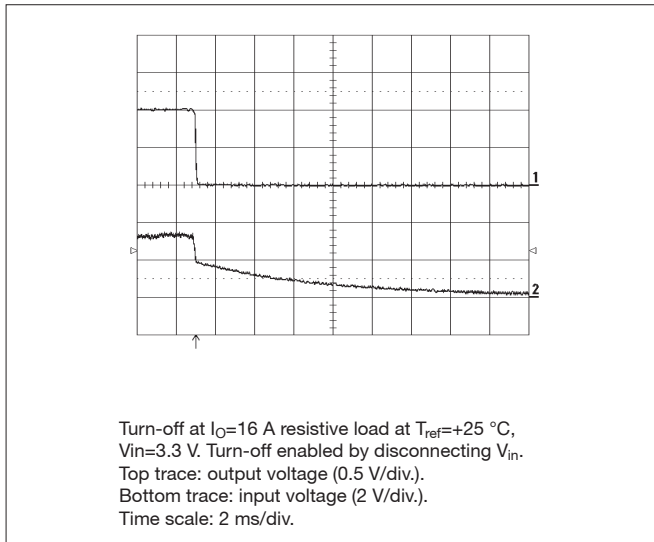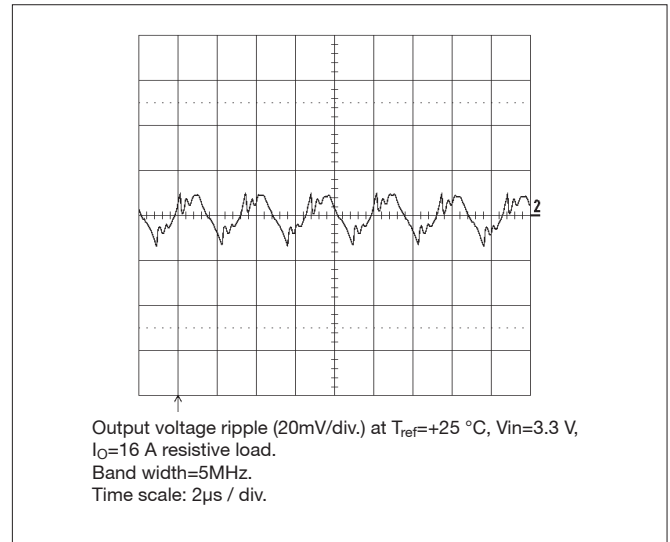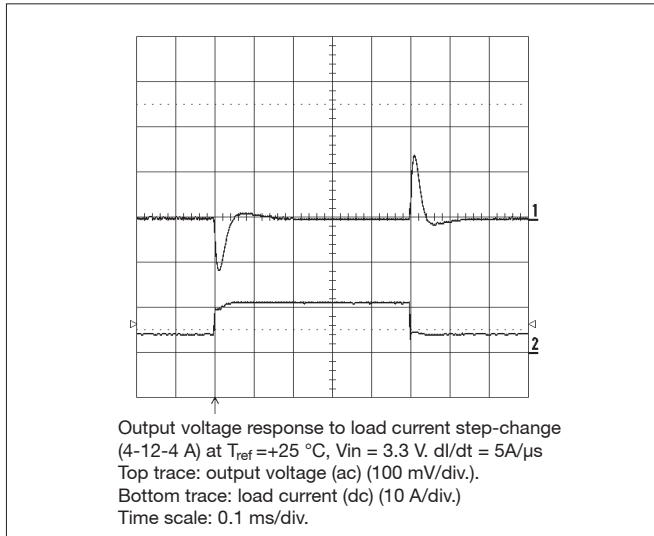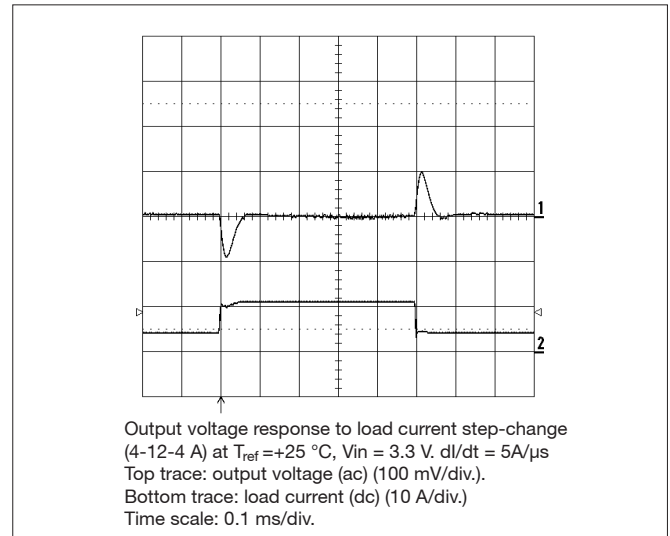Turn-off at I_O=16 A resistive load at T_{ref}=+25 °C,
Vin=3.3 V. Turn-off enabled by disconnecting V_{in}.
Top trace: output voltage (0.5 V/div.).
Bottom trace: input voltage (2 V/div.).
Time scale: 2 ms/div.

Output Ripple

Output voltage ripple (20mV/div.) at T_{ref}=+25 °C, Vin=3.3 V,
I_O=16 A resistive load.
Band width=5MHz.
Time scale: 2μs / div.

Transient with 150 μF output capacitor

Output voltage response to load current step-change
(4-12-4 A) at T_{ref} =+25 °C, Vin = 3.3 V. dI/dt = 5A/μs
Top trace: output voltage (ac) (100 mV/div.).
Bottom trace: load current (dc) (10 A/div.)
Time scale: 0.1 ms/div.

Transient with 300 μF output capacitor

Output voltage response to load current step-change
(4-12-4 A) at T_{ref} =+25 °C, Vin = 3.3 V. dI/dt = 5A/μs
Top trace: output voltage (ac) (100 mV/div.).
Bottom trace: load current (dc) (10 A/div.)
Time scale: 0.1 ms/div.

Adjusted to 1.2 V out - Data

T_{ref} = -30 ... +90 °C, V_I = 3.0 ...5.5 V unless otherwise specified. Input filter 2 x 22 µF, Output filter 1 x 150 µF
Typ values specified at: T_{ref} = +25 °C and $V_I$nom, $I_O$max = 16 A. Note: +Sense connected to +Out

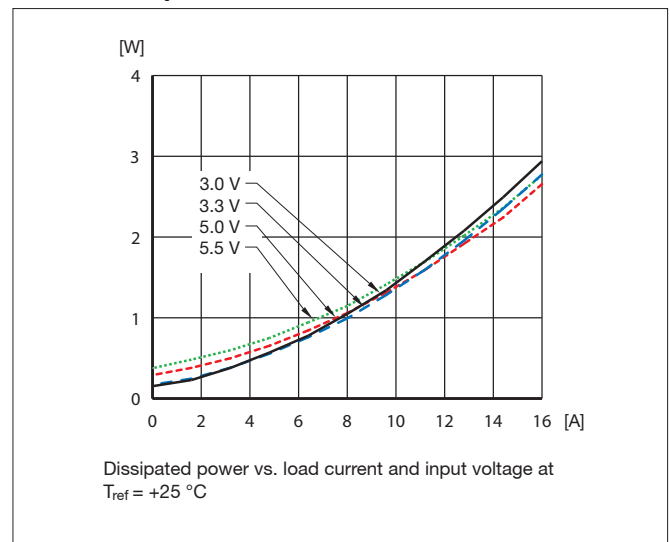| Characteristics		Conditions	Output			Unit
			min	typ	max	
V_{Oi}	Output voltage adjusted setting	T_{ref} = +25 °C, $V_I$nom, $I_O$max, R_{adj} 42 kΩ	1.176	1.20	1.224	V
V_O	Output voltage tolerance band	I_O = 0.01...1.0 x $I_O$max	1.164		1.236	V
	Idling voltage	I_O = 0	1.18		1.22	V
	Line regulation	$V_I$min ... $V_I$max, $I_O$max		11		mV
	Load regulation	I_O = 0.01...1.0 x $I_O$max, $V_I$nom		10		mV
V_{tr}	Load transient voltage deviation	Load step = 0.25 ... 0.75 x $I_O$max, dI/dt = 5 A/µs, C_O = 150 µF, V_I = 3.3 V		140		mV
t_{tr}	Load transient recovery time			60		µs
T_{coeff}	Temperature coefficient	T_{ref} = -30 ... +90°C, $I_O$max	-0.1	0	+0.1	mV/°C
t_s	Start-up time	From V_I connected to V_O = 0.9 x V_{Oi}, I_O = 0.1 ...1.0 x $I_O$max, $V_I$nom		8.5		ms
t_r	Ramp-up time	0.1...0.9 x V_O, I_O = 0.1 ...1.0 x $I_O$max, $V_I$nom		4		ms
t_r	Fall time, V_O x 0.1	I_O = $I_O$max, $V_I$nom		0.2		ms
t_r	Fall time, V_O x 0.1	I_O = 0 A, $V_I$nom		5		s
t_{RC}off	RC shut-down time to V_O x 0.1	I_O = $I_O$max, $V_I$nom		0.2		ms
t_{RC}on	RC start-up time to V_O x 0.9	I_O = $I_O$max, $V_I$nom		8		ms
t_{RC}	RC fall time, V_O x 0.1 ... 0.9	I_O = 0 A, $V_I$nom		5		s
I_O	Output current		0		16	A
$P_O$max	Max output power	At V_O = $V_O$nom	19.2			W
I_{lim}	Current limit threshold	T_{ref} < T_{ref}max	22	28	35	A
V_{Oac}	Output ripple	20 Hz ... 5 MHz, $I_O$max		10	20	mV$_{p-p}$
η	Efficiency - 50% load	I_O = 0.5 x $I_O$max, V_I = 5 V		90.1		%
η	Efficiency - 100% load	I_O = $I_O$max, V_I = 5 V	86.2	87.8		%
P_d	Power Dissipation	I_O = $I_O$max, V_I = 5 V		2.7	3.1	W
Fo	Switching frequency	I_O = 0 ... 1.0 x $I_O$max	250	300	350	kHz
I_{sense}	Remote sense current				8	mA
I_I	Static input current	V_I = 3.0 V, I_O = $I_O$max, T_{ref} = 25 °C		7.5		A
MTBF	Predicted reliability	T_{ref} = 40 °C		6		million hours

Adjusted to 1.2 V - Typical Characteristics

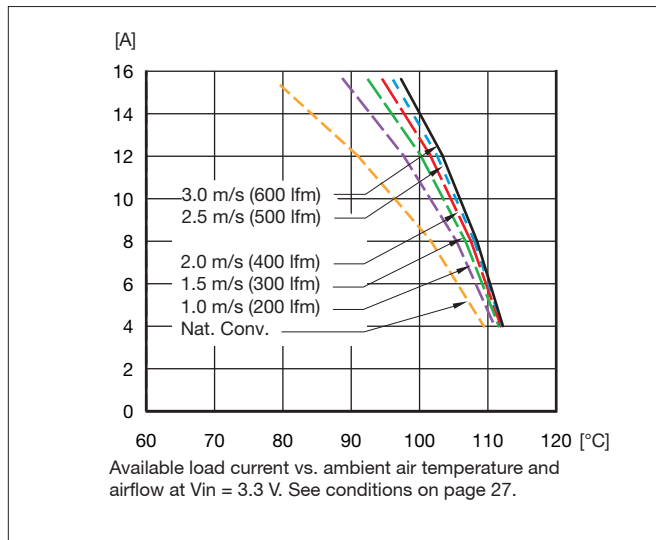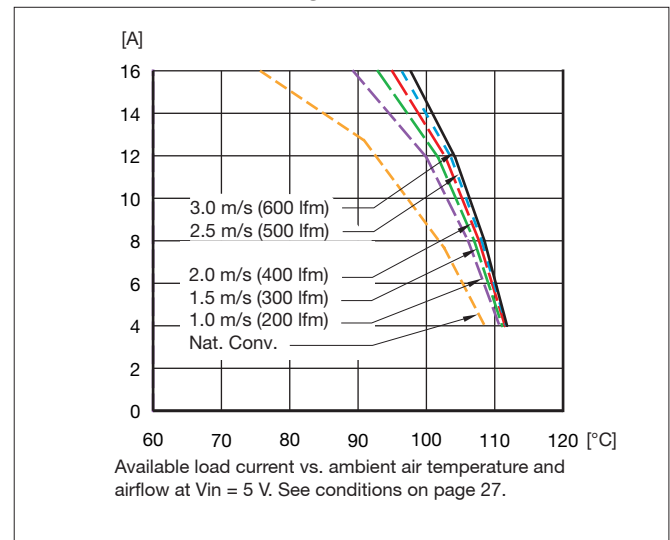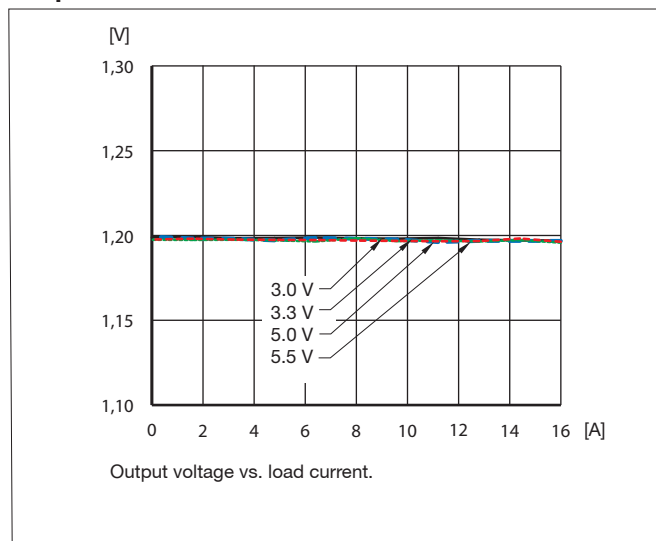General conditions: Input filter 2 x 22 µF, Output filter 1 x 150 µF

Efficiency

Efficiency vs. load current and input voltage at T_{re} = +25 °C

Power Dissipation

Dissipated power vs. load current and input voltage at T_{ref} = +25 °C

Output Current Derating at 3.3 V input

Available load current vs. ambient air temperature and airflow at Vin = 3.3 V. See conditions on page 27.

Output Current Derating at 5 V input

Available load current vs. ambient air temperature and airflow at Vin = 5 V. See conditions on page 27.

Output Characteristic

Output voltage vs. load current.

Start-Up

Start-up at I_O = 16 A resistive load at T_{ref} = +25 °C, Vin = 3.3 V. Start enabled by connecting V_{in}.
Top trace: output voltage (0.5 V/div.).
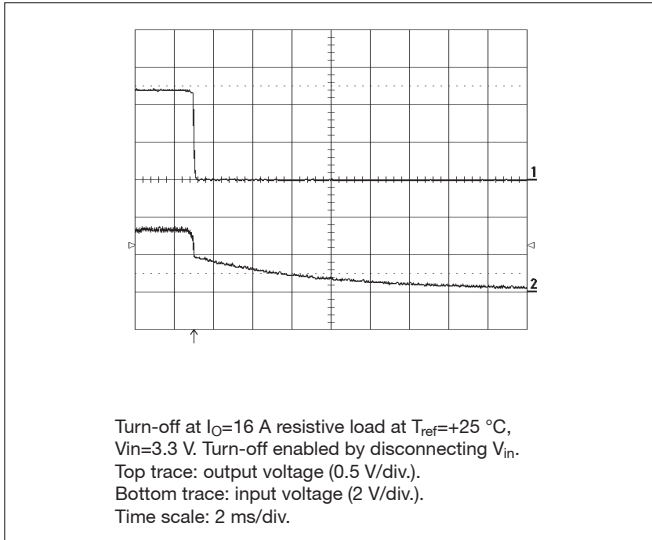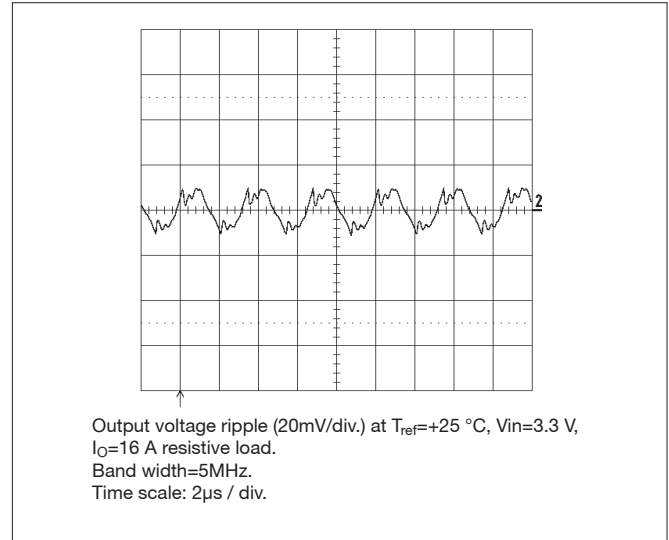Bottom trace: input voltage (2 V/div.).
Time scale: 2 ms/div.

Adjusted to 1.2 V - Typical Characteristics

General conditions: Input filter 2 x 22 µF, Output filter 1 x 150 µF

Turn Off

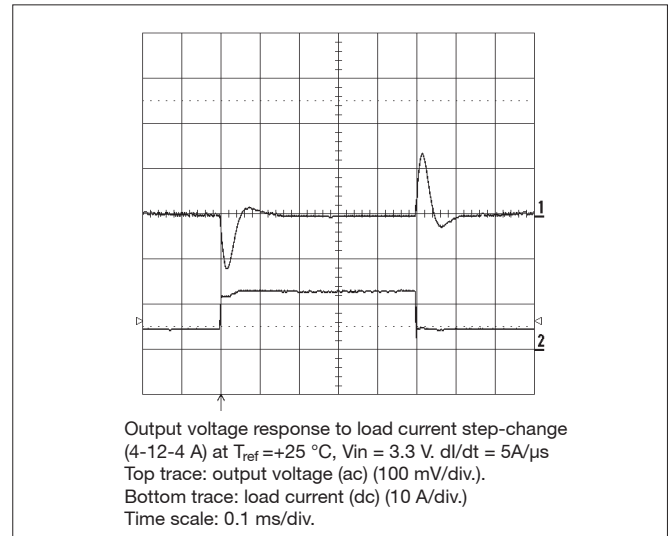Turn-off at I_O=16 A resistive load at T_{ref}=+25 °C,
Vin=3.3 V. Turn-off enabled by disconnecting V_{in}.
Top trace: output voltage (0.5 V/div.).
Bottom trace: input voltage (2 V/div.).
Time scale: 2 ms/div.

Output Ripple

Output voltage ripple (20mV/div.) at T_{ref}=+25 °C, Vin=3.3 V,
I_O=16 A resistive load.
Band width=5MHz.
Time scale: 2µs / div.

Transient with 150 µF output capacitor

Output voltage response to load current step-change
(4-12-4 A) at T_{ref} =+25 °C, Vin = 3.3 V. dI/dt = 5A/µs
Top trace: output voltage (ac) (100 mV/div.).
Bottom trace: load current (dc) (10 A/div.)
Time scale: 0.1 ms/div.

Transient with 300 µF output capacitor

Output voltage response to load current step-change
(4-12-4 A) at T_{ref} =+25 °C, Vin = 3.3 V. dI/dt = 5A/µs
Top trace: output voltage (ac) (100 mV/div.).
Bottom trace: load current (dc) (10 A/div.)
Time scale: 0.1 ms/div.

Adjusted to 1.5 V out - Data

T_{ref} = -30 ... +90 °C, V_I = 3.0 ...5.5 V unless otherwise specified. Input filter 2 x 22 µF, Output filter 1 x 150 µF
Typ values specified at: T_{ref} = +25 °C and $V_I$nom. $I_O$max = 16 A. Note: +Sense connected to +Out

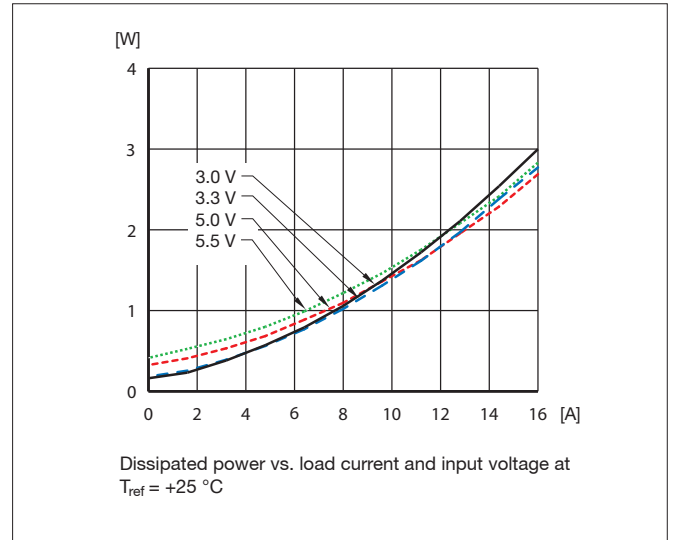| Characteristics		Conditions	Output			Unit
			min	typ	max	
V_{Oi}	Output voltage adjusted setting	T_{ref} = +25 °C, $V_I$nom, $I_O$max, R_{adj} 23 kΩ	1.47	1.5	1.53	V
V_O	Output voltage tolerance band	I_O = 0.01...1.0 x $I_O$max	1.455		1.545	V
	Idling voltage	I_O = 0	1.48		1.52	V
	Line regulation	$V_I$min ... $V_I$max, $I_O$max		11		mV
	Load regulation	I_O = 0.01...1.0 x $I_O$max, $V_I$nom		10		mV
V_{tr}	Load transient voltage deviation	Load step = 0.25 ... 0.75 x $I_O$max, dI/dt = 5 A/µs, C_O = 150 µF, V_I = 3.3 V		120		mV
t_{tr}	Load transient recovery time			60		µs
T_{coeff}	Temperature coefficient	T_{ref} = -30 ... +90 °C, $I_O$max	-0.1	0	+0.1	mV/°C
t_s	Start-up time	From V_I connected to V_O = 0.9 x V_{OI}, I_O = 0.1 ...1.0 x $I_O$max, $V_I$nom		8.5		ms
t_r	Ramp-up time	0.1...0.9 x V_O, I_O = 0.1 ...1.0 x $I_O$max, $V_I$nom		4		ms
t_r	Fall time, V_O x 0.1	I_O = $I_O$max, $V_I$nom		0.2		ms
t_r	Fall time, V_O x 0.1	I_O = 0 A, $V_I$nom		5		s
t_{RC}off	RC shut-down time to V_O x 0.1	I_O = $I_O$max, $V_I$nom		0.2		ms
t_{RC}on	RC start-up time to V_O x 0.9	I_O = $I_O$max, $V_I$nom		8		ms
t_{RC}	RC fall time, V_O x 0.1 ... 0.9	I_O = 0 A, $V_I$nom		5		s
I_O	Output current		0		16	A
$P_O$max	Max output power	At V_O = $V_O$nom	24			W
I_{lim}	Current limit threshold	T_{ref} < T_{ref}max	22	28	35	A
V_{Oac}	Output ripple	20Hz ... 5MHz, $I_O$max		10	20	mV$_{p-p}$
η	Efficiency - 50% load	I_O = 0.5 x $I_O$max, V_I = 5 V		91.6		%
η	Efficiency - 100% load	I_O = $I_O$max, V_I = 5 V	88.3	89.9		%
P_d	Power Dissipation	I_O = $I_O$max, V_I = 5 V		2.7	3.2	W
Fo	Switching frequency	I_O = 0 ... 1.0 x $I_O$max	250	300	350	kHz
I_{sense}	Remote sense current				8	mA
I_I	Static input current	V_I = 3.0 V, I_O = $I_O$max, T_{ref} = 25 °C		9.0		A
MTBF	Predicted reliability	T_{ref} = 40 °C		6		million hours

Adjusted to 1.5V out - Typical Characteristics

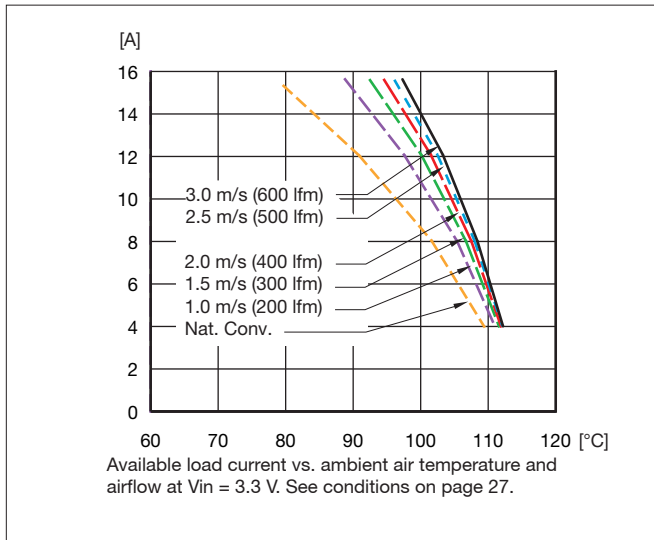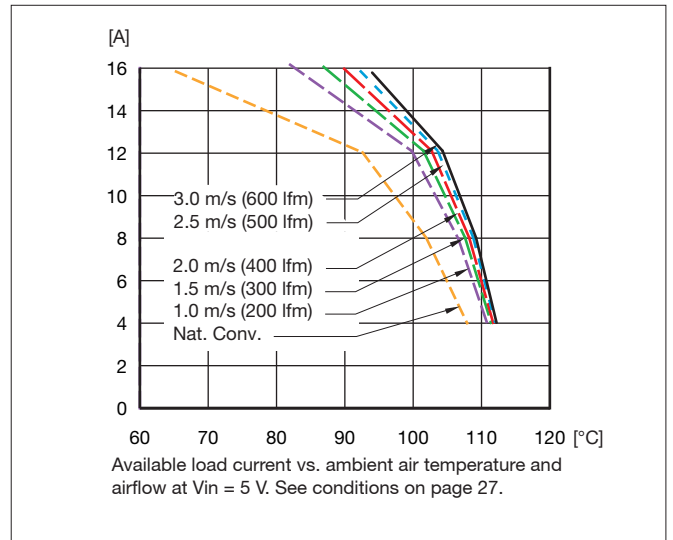General conditions: Input filter 2 x 22 µF, Output filter 1 x 150 µF

Efficiency

[%]

3.0 V
3.3 V
5.0 V
5.5 V

Efficiency vs. load current and input voltage at T_{ref} = +25 °C

Power Dissipation

[W]

3.0 V
3.3 V
5.0 V
5.5 V

Dissipated power vs. load current and input voltage at T_{ref} = +25 °C

Output Current Derating at 3.3 V input

[A]

3.0 m/s (600 lfm)
2.5 m/s (500 lfm)
2.0 m/s (400 lfm)
1.5 m/s (300 lfm)
1.0 m/s (200 lfm)
Nat. Conv.

Available load current vs. ambient air temperature and airflow at Vin = 3.3 V. See conditions on page 27.

Output Current Derating at 5 V input

[A]

3.0 m/s (600 lfm)
2.5 m/s (500 lfm)
2.0 m/s (400 lfm)
1.5 m/s (300 lfm)
1.0 m/s (200 lfm)
Nat. Conv.

Available load current vs. ambient air temperature and airflow at Vin = 5 V. See conditions on page 27.

Output Characteristic

[V]

3.0 V
3.3 V
5.0 V
5.5 V

Output voltage vs. load current.

Start-Up

Start-up at I_O = 16 A resistive load at T_{ref} = +25 °C, Vin = 3.3 V. Start enabled by connecting V_{in}.
Top trace: output voltage (0.5 V/div.).
Bottom trace: input voltage (2 V/div.).
Time scale: 2 ms/div.

Adjusted to 1.5V out - Typical Characteristics

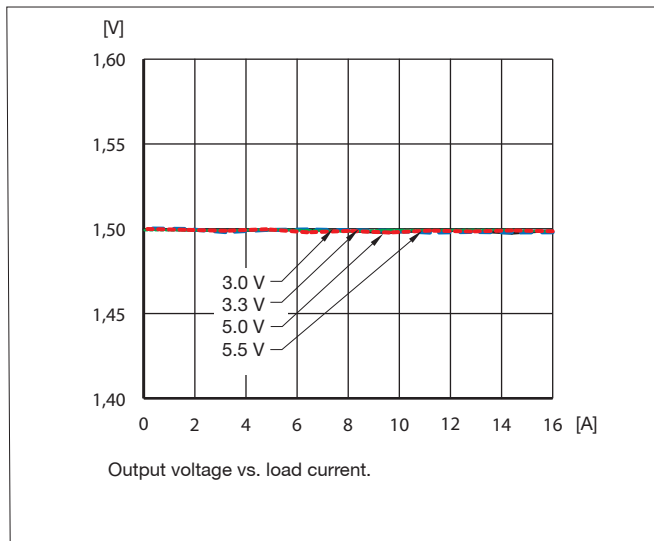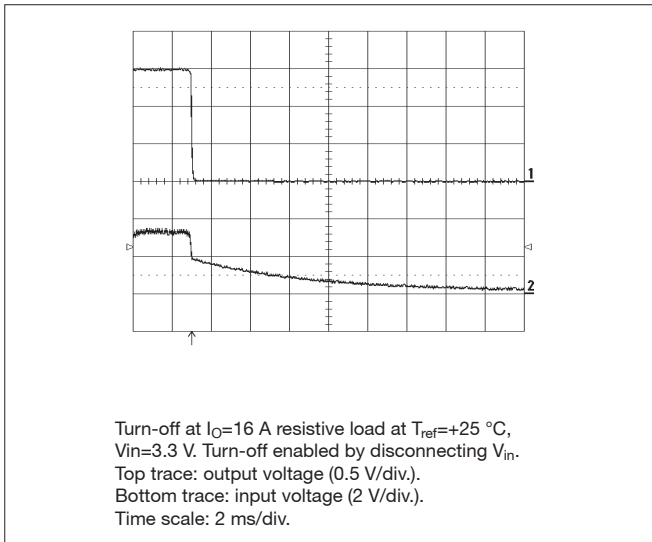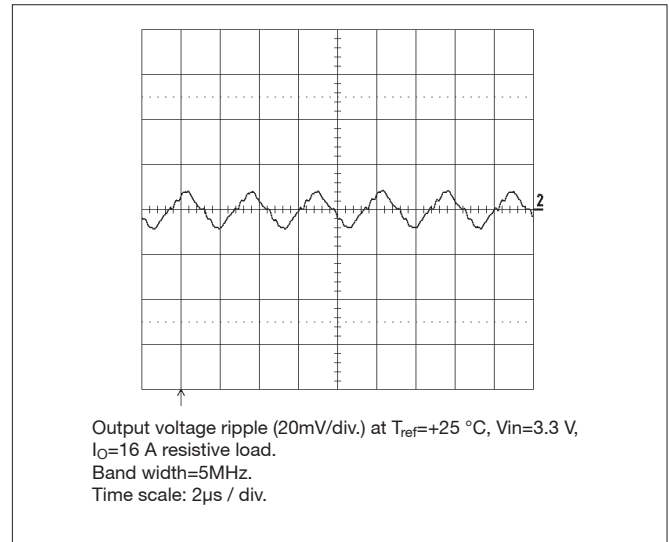General conditions: Input filter 2 x 22 µF, Output filter 1 x 150 µF

Turn Off

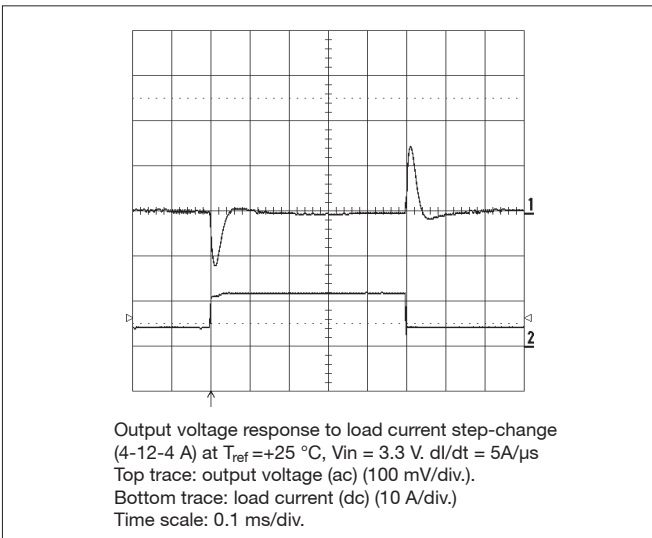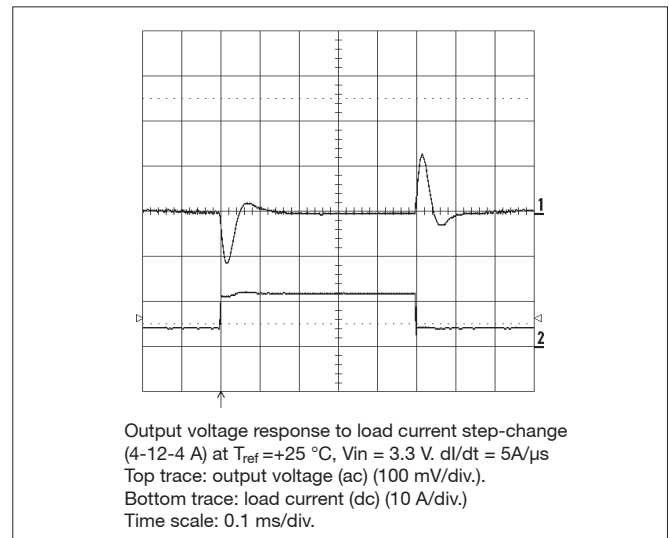Turn-off at I_O=16 A resistive load at T_{ref}=+25 °C,
Vin=3.3 V. Turn-off enabled by disconnecting V_{in}.
Top trace: output voltage (0.5 V/div.).
Bottom trace: input voltage (2 V/div.).
Time scale: 2 ms/div.

Output Ripple

Output voltage ripple (20mV/div.) at T_{ref}=+25 °C, Vin=3.3 V,
I_O=16 A resistive load.
Band width=5MHz.
Time scale: 2µs / div.

Transient with 150 µF output capacitor

Output voltage response to load current step-change
(4-12-4 A) at T_{ref} =+25 °C, Vin = 3.3 V. dI/dt = 5A/µs
Top trace: output voltage (ac) (100 mV/div.).
Bottom trace: load current (dc) (10 A/div.)
Time scale: 0.1 ms/div.

Transient with 300 µF output capacitor

Output voltage response to load current step-change
(4-12-4 A) at T_{ref} =+25 °C, Vin = 3.3 V. dI/dt = 5A/µs
Top trace: output voltage (ac) (100 mV/div.).
Bottom trace: load current (dc) (10 A/div.)
Time scale: 0.1 ms/div.

Adjusted to 1.8 V out - Data

T_{ref} = –30…+90 °C, V_I = 3.0 ... 5.5 V unless otherwise specified. Input filter 2 x 22 µF, Output filter 1 x 150 µF
Typ values specified at: T_{ref} = +25 °C and $V_I$nom. $I_O$max = 16 A. Note: +Sense connected to +Out

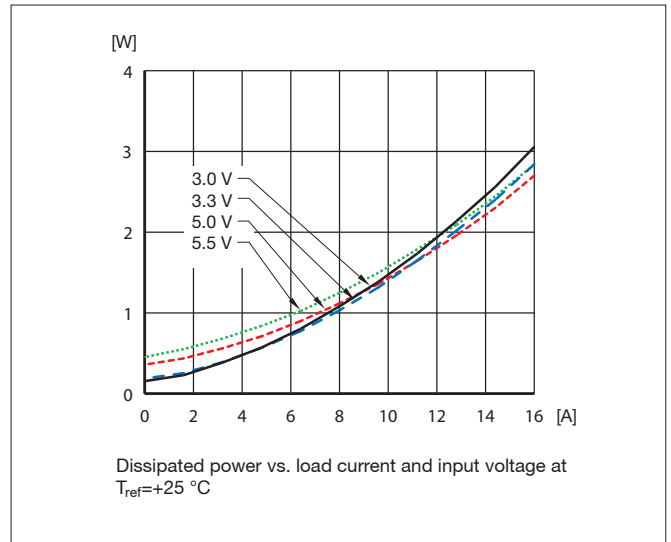| Characteristics		Conditions	Output			Unit
			min	typ	max	
V_{Oi}	Output voltage adjusted setting	T_{ref} = +25 °C, $V_I$nom, $I_O$max, R_{adj} 15 kΩ	1.764	1.80	1.836	V
V_O	Output voltage tolerance band	I_O = 0.01...1.0 x $I_O$max	1.746		1.854	V
	Idling voltage	I_O = 0	1.78		1.82	V
	Line regulation	$V_I$min ... $V_I$max, $I_O$max		11		mV
	Load regulation	I_O = 0.01...1.0 x $I_O$max, $V_I$nom		10		mV
V_{tr}	Load transient voltage deviation	Load step = 0.25 ... 0.75 x $I_O$max, dI/dt = 5 A/µs, C_O = 150 µF, V_I = 3.3 V		140		mV
t_{tr}	Load transient recovery time			60		µs
T_{coeff}	Temperature coefficient	T_{ref} = -30 ... +90 °C, $I_O$max	-0.1	0	+0.1	mV/°C
t_s	Start-up time	From V_I connected to V_O = 0.9 x V_{OI}, I_O = 0.1 ...1.0 x $I_O$max, $V_I$nom		8.5		ms
t_r	Ramp-up time	0.1...0.9 x V_O, I_O = 0.1 ...1.0 x $I_O$max, $V_I$nom		4		ms
t_r	Fall time, V_O x 0.1	I_O = $I_O$max, $V_I$nom		0.2		ms
t_r	Fall time, V_O x 0.1	I_O = 0 A, $V_I$nom		5		s
t_{RC}off	RC shut-down time to V_O x 0.1	I_O = $I_O$max, $V_I$nom		0.2		ms
t_{RC}on	RC start-up time to V_O x 0.9	I_O = $I_O$max, $V_I$nom		8		ms
t_{RC}	RC fall time, V_O x 0.1 ... 0.9	I_O = 0 A, $V_I$nom		5		s
I_O	Output current		0		16	A
$P_O$max	Max output power	At V_O = $V_O$nom	28.8			W
I_{lim}	Current limit threshold	T_{ref} < T_{ref}max	22	28	35	A
V_{Oac}	Output ripple	20 Hz ... 5 MHz, $I_O$max		14	24	mV$_{p-p}$
η	Efficiency - 50% load	I_O = 0.5 x $I_O$max, V_I = 5 V		93		%
η	Efficiency - 100% load	I_O = $I_O$max, V_I = 5 V	89.5	91.5		%
P_d	Power Dissipation	I_O = $I_O$max, V_I = 5 V		2.6	3.4	W
Fo	Switching frequency	I_O = 0 ... 1.0 x $I_O$max	250	300	350	kHz
I_{sense}	Remote sense current				8	mA
I_I	Static input current	V_I = 3.0 V, I_O = $I_O$max, T_{ref} = 25 °C		11.1		A
MTBF	Predicted reliability	T_{ref} = 40 °C		6		million hours

Adjusted to 1.8 V - Typical Characteristics

General conditions: Input filter 2 x 22 µF, Output filter 1 x 150 µF

Efficiency
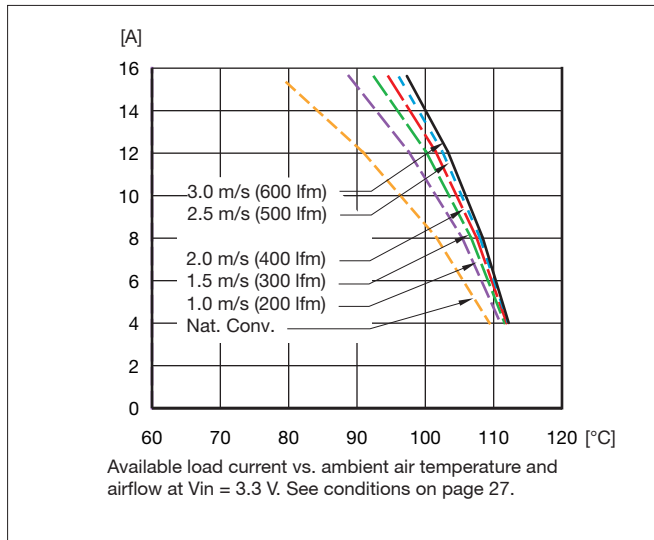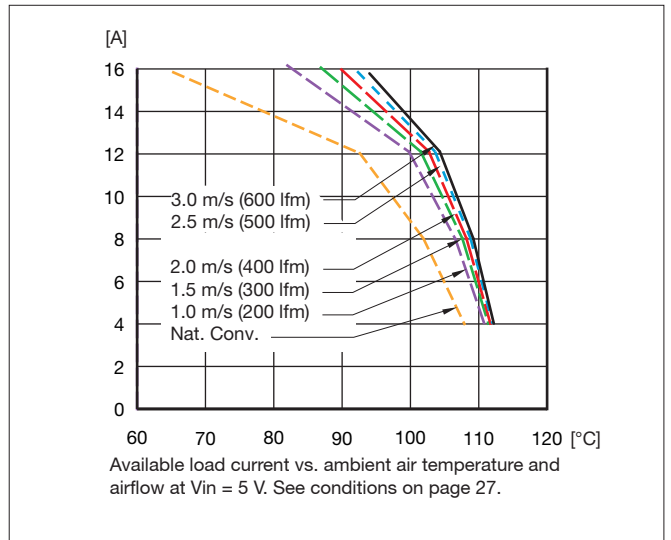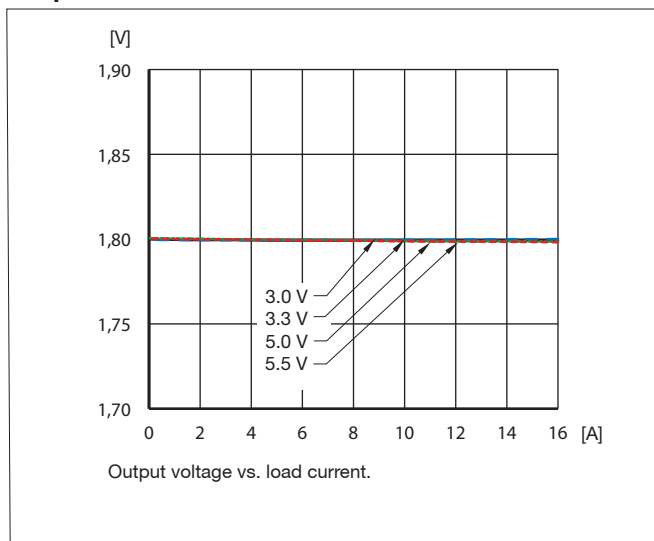
[%]

3.0 V
3.3 V
5.0 V
5.5 V

Efficiency vs. load current and input voltage at T_{ref} = +25 °C

Power Dissipation

[W]

3.0 V
3.3 V
5.0 V
5.5 V

Dissipated power vs. load current and input voltage at T_{ref}=+25 °C

Output Current Derating at 3.3 V input

[A]

3.0 m/s (600 lfm)
2.5 m/s (500 lfm)
2.0 m/s (400 lfm)
1.5 m/s (300 lfm)
1.0 m/s (200 lfm)
Nat. Conv.

Available load current vs. ambient air temperature and airflow at Vin = 3.3 V. See conditions on page 27.

Output Current Derating at 5 V input

[A]

3.0 m/s (600 lfm)
2.5 m/s (500 lfm)
2.0 m/s (400 lfm)
1.5 m/s (300 lfm)
1.0 m/s (200 lfm)
Nat. Conv.

Available load current vs. ambient air temperature and airflow at Vin = 5 V. See conditions on page 27.

Output Characteristic

[V]

3.0 V
3.3 V
5.0 V
5.5 V

Output voltage vs. load current.

Start-Up

Start-up at I_O = 16 A resistive load at T_{ref} = +25 °C, Vin = 3.3 V. Start enabled by connecting V_{in}.
Top trace: output voltage (0.5 V/div.).
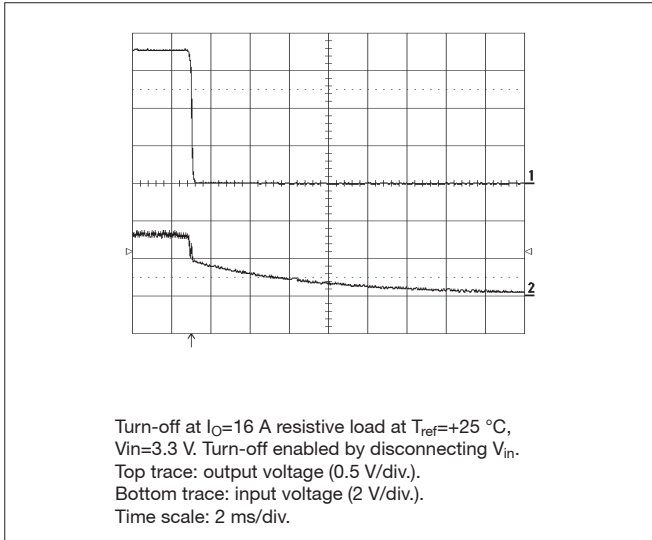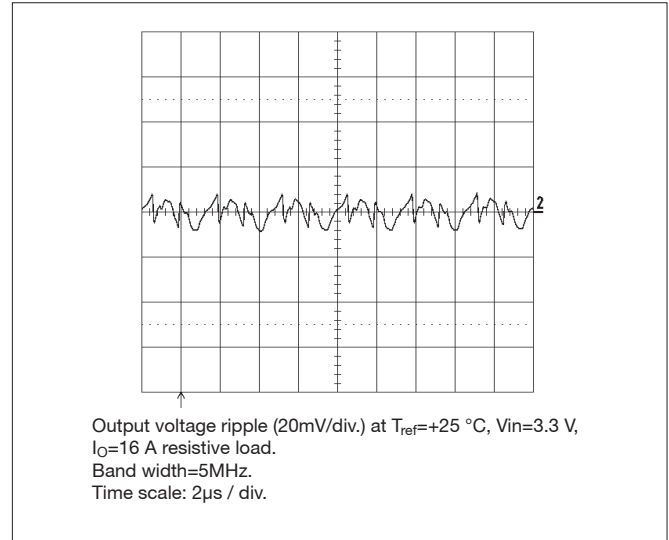Bottom trace: input voltage (2 V/div.).
Time scale: 2 ms/div.

Adjusted to 1.8 V out - Typical Characteristics

General conditions: Input filter 2 x 22 μF, Output filter 1 x 150 μF

Turn Off

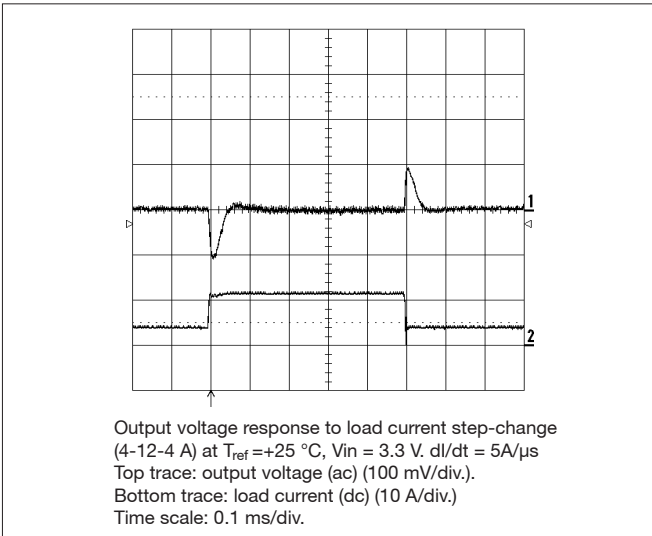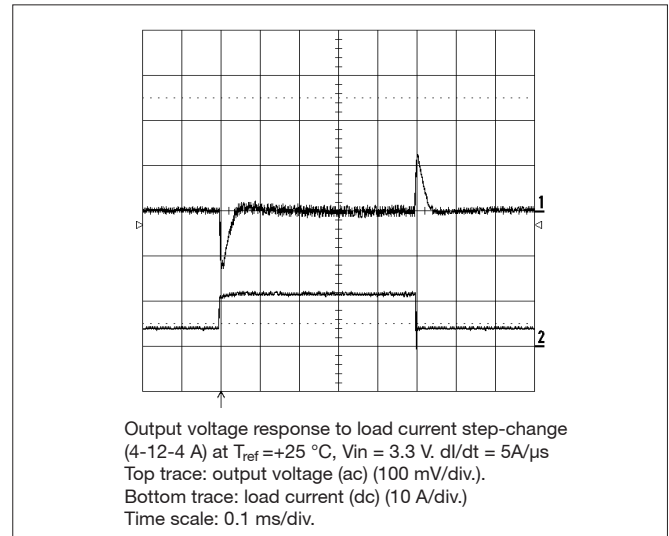Turn-off at I_O=16 A resistive load at T_{ref}=+25 °C,
Vin=3.3 V. Turn-off enabled by disconnecting V_{in}.
Top trace: output voltage (0.5 V/div.).
Bottom trace: input voltage (2 V/div.).
Time scale: 2 ms/div.

Output Ripple

Output voltage ripple (20mV/div.) at T_{ref}=+25 °C, Vin=3.3 V,
I_O=16 A resistive load.
Band width=5MHz.
Time scale: 2μs / div.

Transient with 150 μF output capacitor

Output voltage response to load current step-change
(4-12-4 A) at T_{ref} =+25 °C, Vin = 3.3 V. dI/dt = 5A/μs
Top trace: output voltage (ac) (100 mV/div.).
Bottom trace: load current (dc) (10 A/div.)
Time scale: 0.1 ms/div.

Transient with 300 μF output capacitor

Output voltage response to load current step-change
(4-12-4 A) at T_{ref} =+25 °C, Vin = 3.3 V. dI/dt = 5A/μs
Top trace: output voltage (ac) (100 mV/div.).
Bottom trace: load current (dc) (10 A/div.)
Time scale: 0.1 ms/div.

Adjusted to 2.5 V out - Data

T_{ref} = -30 ... +90 °C, V_I = 3.0 ...5.5 V unless otherwise specified. Input filter 2 x 22 µF, Output filter 1 x 150 µF
Typ values specified at: T_{ref} = +25 °C and $V_I$nom. $I_O$max = 16 A. Note: +Sense connected to +Out

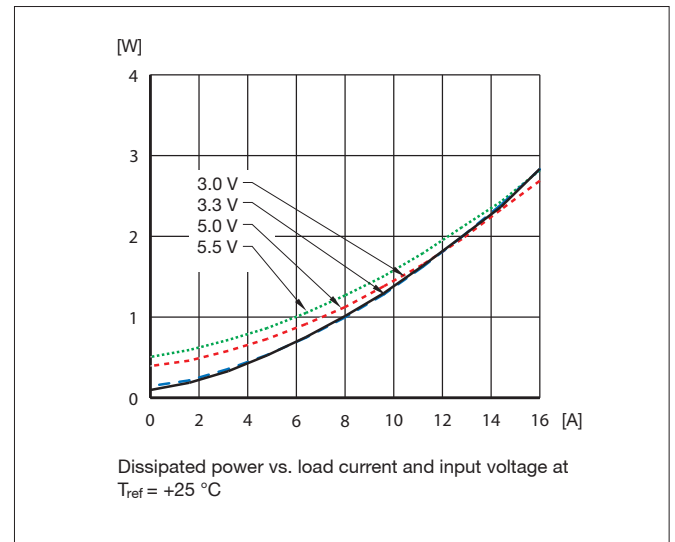| Characteristics		Conditions	Output			Unit
			min	typ	max	
V_{Oi}	Output voltage adjusted setting	T_{ref} = +25 °C, $V_I$nom, $I_O$max, R_{adj} 7 kΩ	2.45	2.5	2.55	V
V_O	Output voltage tolerance band	I_O = 0.01...1.0 x $I_O$max	2.425		2.575	V
	Idling voltage	I_O = 0	2.48		2.52	V
	Line regulation	$V_I$min ... $V_I$max, $I_O$max		11		mV
	Load regulation	I_O = 0.01...1.0 x $I_O$max, $V_I$nom		10		mV
V_{tr}	Load transient voltage deviation	Load step = 0.25 ... 0.75 x $I_O$max, dI/dt = 5 A/µs, C_O = 150 µF, V_I = 3.3 V		200		mV
t_{tr}	Load transient recovery time			60		µs
T_{coeff}	Temperature coefficient	T_{ref} = -30 ... +90 °C, $I_O$max	-0.1	0	+0.1	mV/°C
t_s	Start-up time	From V_I connected to V_O = 0.9 x V_{OI}, I_O = 0.1 ...1.0 x $I_O$max, $V_I$nom		8.5		ms
t_r	Ramp-up time	0.1...0.9 x V_O, I_O = 0.1 ...1.0 x $I_O$max, $V_I$nom		4		ms
t_r	Fall time, V_O x 0.1	I_O = $I_O$max, $V_I$nom		0.2		ms
t_r	Fall time, V_O x 0.1	I_O = 0 A, $V_I$nom		5		s
t_{RC}off	RC shut-down time to V_O x 0.1	I_O = $I_O$max, $V_I$nom		0.2		ms
t_{RC}on	RC start-up time to V_O x 0.9	I_O = $I_O$max, $V_I$nom		8		ms
t_{RC}	RC fall time, V_O x 0.1 ... 0.9	I_O = 0 A, $V_I$nom		5		s
I_O	Output current		0		16	A
$P_O$max	Max output power	At V_O = $V_O$nom	40			W
I_{lim}	Current limit threshold	T_{ref} < T_{ref}max	22	28	35	A
V_{Oac}	Output ripple	20 Hz ... 5 MHz, $I_O$max		20	35	mV$_{p-p}$
η	Efficiency - 50% load	I_O = 0.5 x $I_O$max, V_I = 5 V		94.5		%
η	Efficiency - 100% load	I_O = $I_O$max, V_I = 5 V	92	93.9		%
P_d	Power Dissipation	I_O = $I_O$max, V_I = 5 V		2.6	3.5	W
Fo	Switching frequency	I_O = 0 ... 1.0 x $I_O$max	250	300	350	kHz
I_{sense}	Remote sense current				8	mA
I_I	Static input current	V_I = 3.0 V, I_O = $I_O$max, T_{ref} = 25 °C		14.8		A
MTBF	Predicted reliability	T_{ref} = 40 °C		6		million hours

Adjusted to 2.5 V out - Typical Characteristics

General conditions: Input filter 2 x 22 µF, Output filter 1 x 150 µF

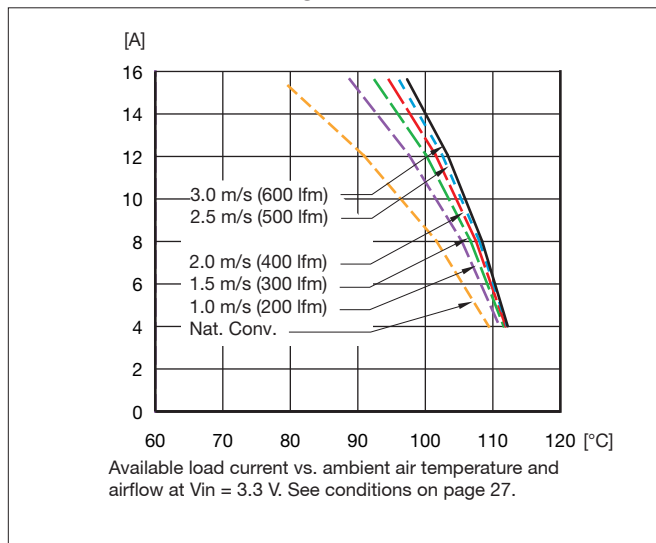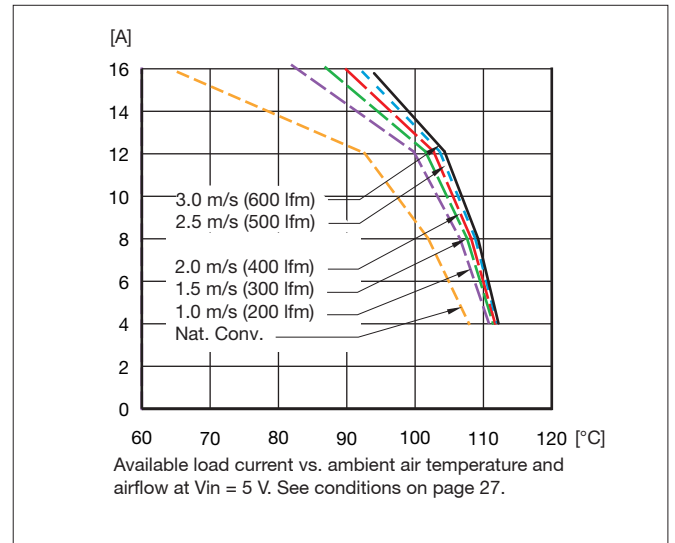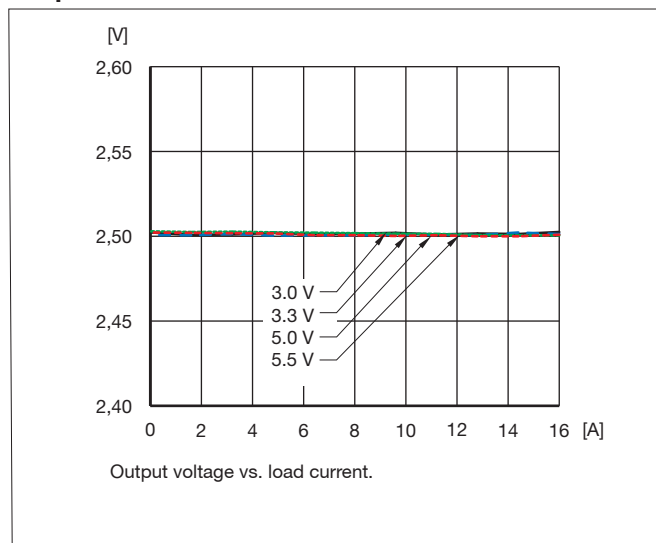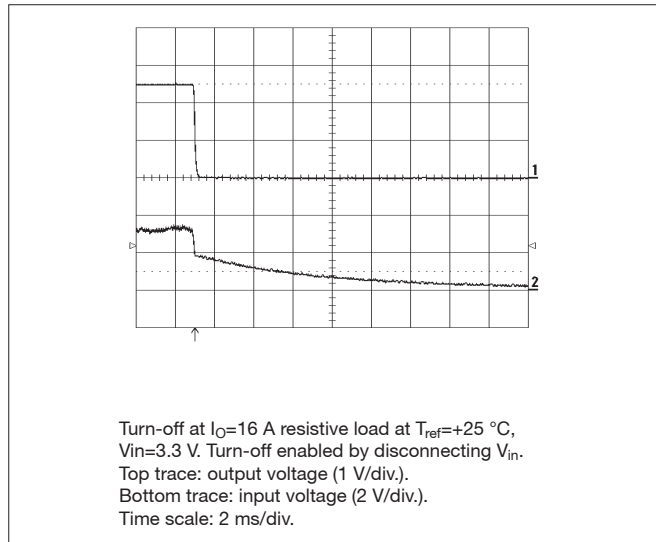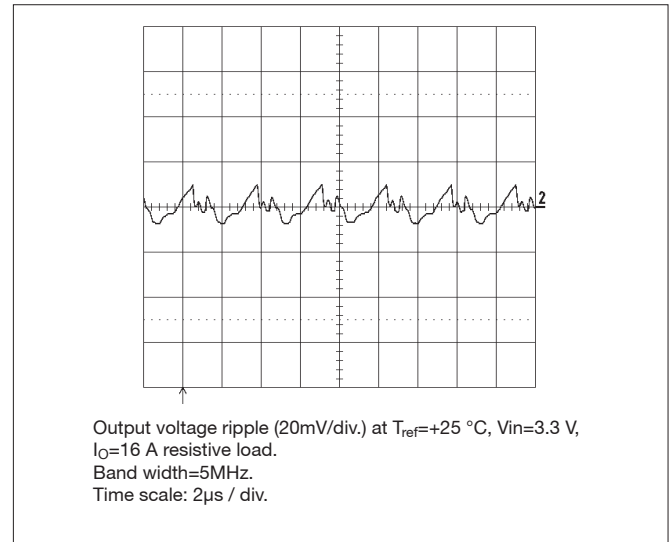### Efficiency

Efficiency vs. load current and input voltage at T_{ref} = +25 °C

Power Dissipation

Dissipated power vs. load current and input voltage at T_{ref} = +25 °C

Output Current Derating at 3.3 V input

3.0 m/s (600 lfm)
2.5 m/s (500 lfm)
2.0 m/s (400 lfm)
1.5 m/s (300 lfm)
1.0 m/s (200 lfm)
Nat. Conv.

Available load current vs. ambient air temperature and airflow at Vin = 3.3 V. See conditions on page 27.

Output Current Derating at 5 V input

3.0 m/s (600 lfm)
2.5 m/s (500 lfm)
2.0 m/s (400 lfm)
1.5 m/s (300 lfm)
1.0 m/s (200 lfm)
Nat. Conv.

Available load current vs. ambient air temperature and airflow at Vin = 5 V. See conditions on page 27.

Output Characteristic

3.0 V
3.3 V
5.0 V
5.5 V

Output voltage vs. load current.

Start-Up

Start-up at I_O = 16 A resistive load at T_{ref} = +25 °C, Vin = 3.3 V. Start enabled by connecting V_{in}.
Top trace: output voltage (1 V/div.).
Bottom trace: input voltage (2 V/div.).
Time scale: 2 ms/div.

Adjusted to 2.5 V out - Typical Characteristics

General conditions: Input filter 2 x 22 µF, Output filter 1 x 150 µF

Turn Off

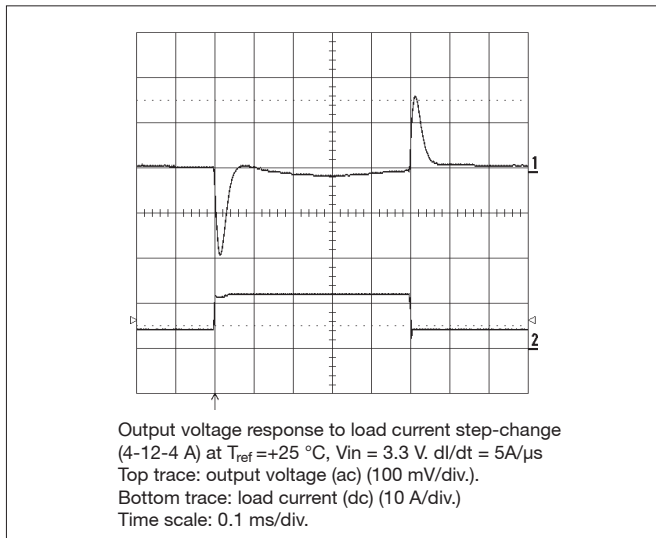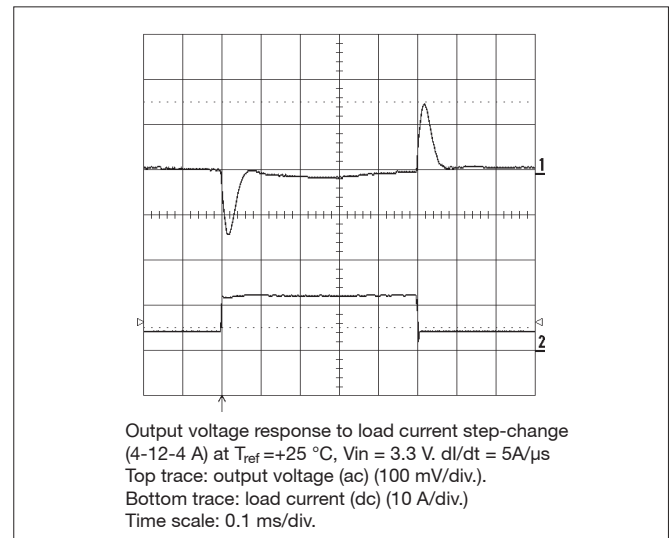Turn-off at I_O=16 A resistive load at T_{ref}=+25 °C, Vin=3.3 V. Turn-off enabled by disconnecting V_{in}.
Top trace: output voltage (1 V/div.).
Bottom trace: input voltage (2 V/div.).
Time scale: 2 ms/div.

Output Ripple

Output voltage ripple (20mV/div.) at T_{ref}=+25 °C, Vin=3.3 V, I_O=16 A resistive load.
Band width=5MHz.
Time scale: 2µs / div.

Transient with 150 µF output capacitor

Output voltage response to load current step-change (4-12-4 A) at T_{ref}=+25 °C, Vin = 3.3 V. dI/dt = 5A/µs
Top trace: output voltage (ac) (100 mV/div.).
Bottom trace: load current (dc) (10 A/div.)
Time scale: 0.1 ms/div.

Transient with 300 µF output capacitor

Output voltage response to load current step-change (4-12-4 A) at T_{ref}=+25 °C, Vin = 3.3 V. dI/dt = 5A/µs
Top trace: output voltage (ac) (100 mV/div.).
Bottom trace: load current (dc) (10 A/div.)
Time scale: 0.1 ms/div.

Adjusted to 3.3 V out - Data

T_{ref} = -30 ... +90 °C, V_I = 3.8 ... 5.5 V unless otherwise specified. Input filter 2 x 22 µF, Output filter 1 x 150 µF

Typ values specified at: T_{ref} = +25 °C and V_I = 5.0 V. $I_O$max = 16 A. Note: +Sense connected to +Out

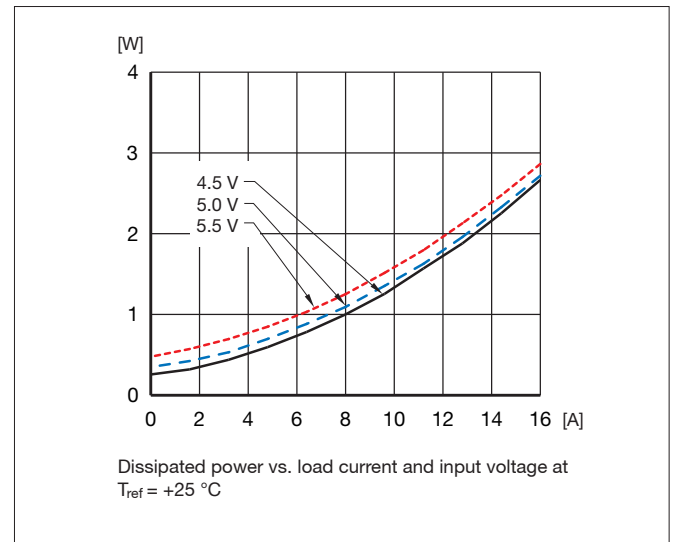| Characteristics		Conditions	Output			Unit
			min	typ	max	
V_{Oi}	Output voltage adjusted setting	T_{ref} = +25 °C, V_I > 3.8 V, $I_O$max, R_{adj} 3.1 kΩ	3.234	3.3	3.366	V
V_O	Output voltage tolerance band	I_O = 0.1...1.0 x $I_O$max	3.201		3.399	V
	Idling voltage	I_O = 0	3.28		3.32	V
	Line regulation	V_I = 5 V ... $V_I$max, $I_O$max		11		mV
	Load regulation	I_O = 0.01...1.0 x $I_O$max, V_I = 5 V		10		mV
V_{tr}	Load transient voltage deviation	Load step = 0.25 ... 0.75 x $I_O$max, dI/dt = 5 A/µs, C_O = 150 µF, V_I = 5 V		140		mV
t_{tr}	Load transient recovery time			60		µs
T_{coeff}	Temperature coefficient	T_{ref} = -30 ... +90 °C, $I_O$max	-0.1	0	+0.1	mV/°C
t_s	Start-up time	From V_I connected to V_O = 0.9 x V_{Oadj}, I_O = 0.1 ...1.0 x $I_O$max, V_I = 5 V		8.5		ms
t_r	Ramp-up time	0.1...0.9 x V_O, I_O = 0.1 ...1.0 x $I_O$max, V_I = 5 V		4		ms
t_r	Fall time, V_O x 0.1	I_O = $I_O$max, V_I = 5 V		0.2		ms
t_r	Fall time, V_O x 0.1	I_O = 0 A, V_I = 5 V		5		s
t_{RC}off	RC shut-down time to V_O x 0.1	I_O = $I_O$max, V_I = 5 V		0.2		ms
t_{RC}on	RC start-up time to V_O x 0.9	I_O = $I_O$max, V_I = 5 V		8		ms
t_{RC}	RC fall time, V_O x 0.1 ... 0.9	I_O = 0 A, V_I = 5 V		5		s
I_O	Output current		0		16	A
$P_O$max	Max output power	At V_O = V_{Onom}	52.8			W
I_{lim}	Current limit threshold	T_{ref} < T_{ref}max	22	28	35	A
V_{Oac}	Output ripple	20 Hz ... 5 MHz, $I_O$max		25	40	mV$_{p-p}$
η	Efficiency - 50% load	I_O = 0.5 x $I_O$max, V_I = 5 V		96		%
η	Efficiency - 100% load	I_O = $I_O$max, V_I = 5 V	93.5	95.4		%
P_d	Power Dissipation	I_O = $I_O$max, V_I = 5 V		2.5	3.7	W
Fo	Switching frequency	I_O = 0 ... 1.0 x $I_O$max	250	300	350	kHz
I_{sense}	Remote sense current				8	mA
I_I	Static input current	V_I = 3.8 V, I_O = $I_O$max, T_{ref} = 25 °C		15.0		A
MTBF	Predicted reliability	T_{ref} = 40 °C		6		million hours

Adjusted to 3.3 V out - Typical Characteristics

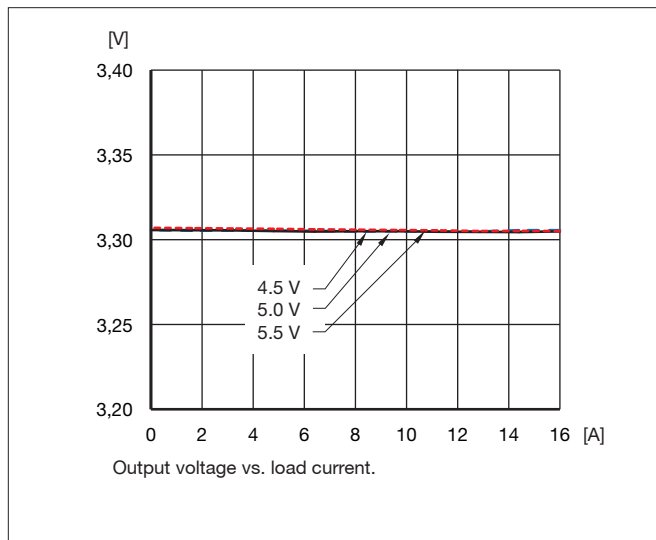General conditions: Input filter 2 x 22 µF, Output filter 1 x 150 µF
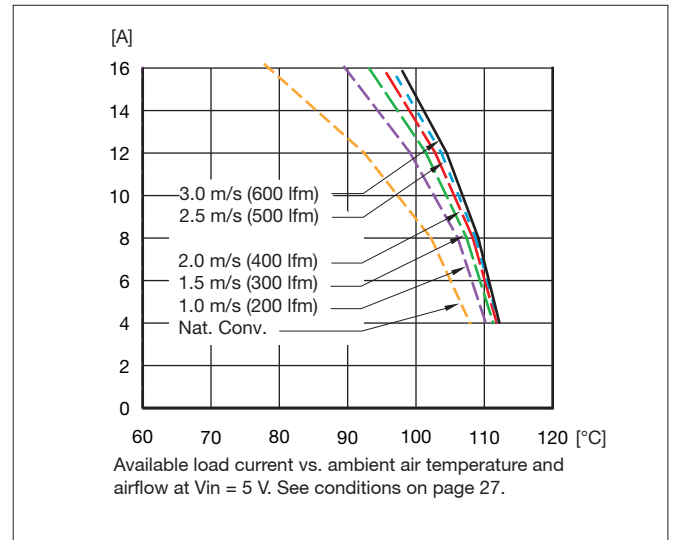
Efficiency

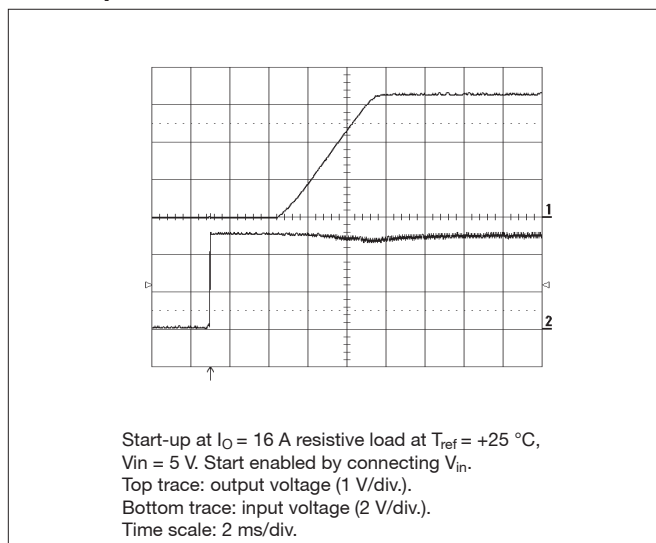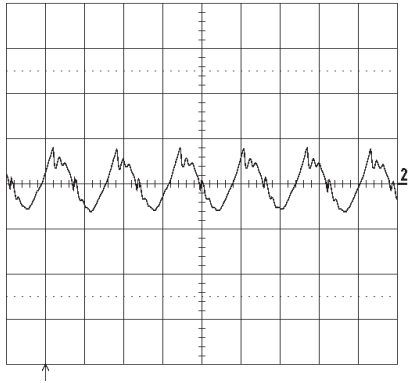Efficiency vs. load current and input voltage at T_{ref} = +25 °C

Power Dissipation

Dissipated power vs. load current and input voltage at T_{ref} = +25 °C

Output Characteristic

Output voltage vs. load current.

Output Current Derating at 5 V input

Available load current vs. ambient air temperature and airflow at Vin = 5 V. See conditions on page 27.

Start-Up

Start-up at I_O = 16 A resistive load at T_{ref} = +25 °C, Vin = 5 V. Start enabled by connecting V_{in}.
Top trace: output voltage (1 V/div.).
Bottom trace: input voltage (2 V/div.).
Time scale: 2 ms/div.

Turn Off

Turn-off at I_O=16 A resistive load at T_{ref}=+25 °C, Vin = 5 V. Turn-off enabled by disconnecting V_{in}.
Top trace: output voltage (1 V/div.).
Bottom trace: input voltage (2 V/div.).
Time scale: 2 ms/div.
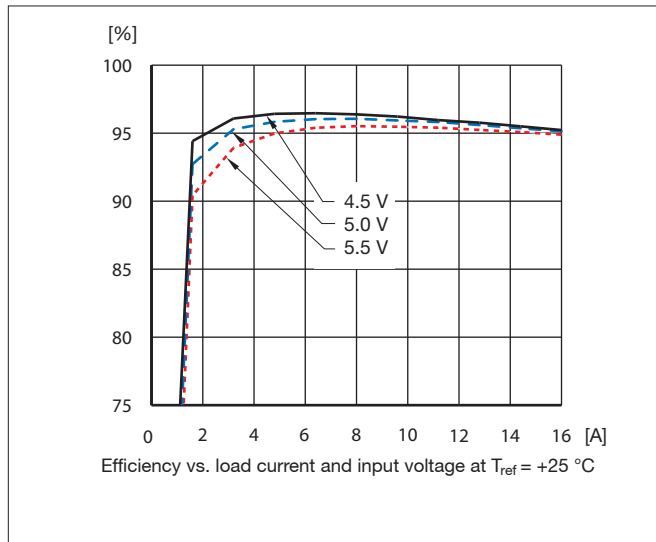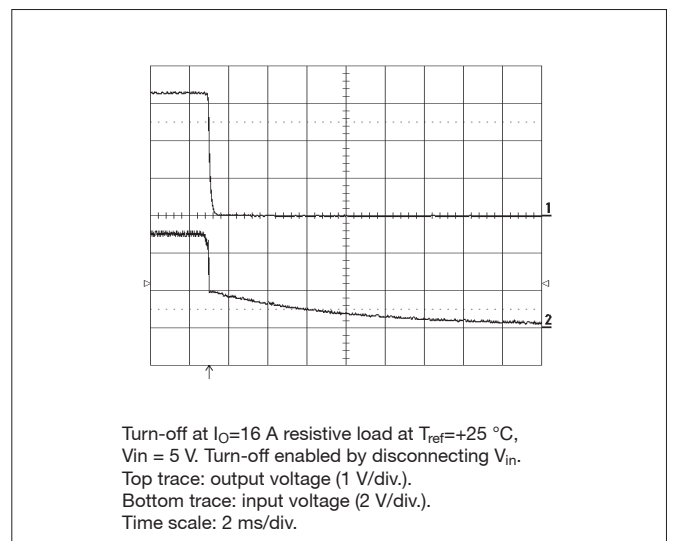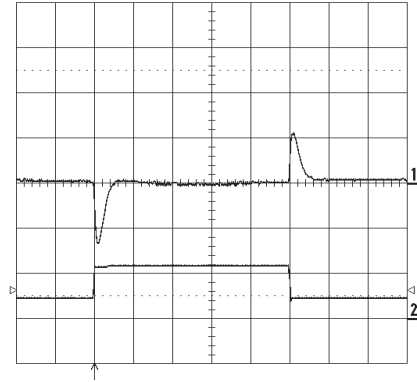
Adjusted to 3.3 V out - Typical Characteristics

General conditions: Input filter 2 x 22 µF, Output filter 1 x 150 µF

Output Ripple

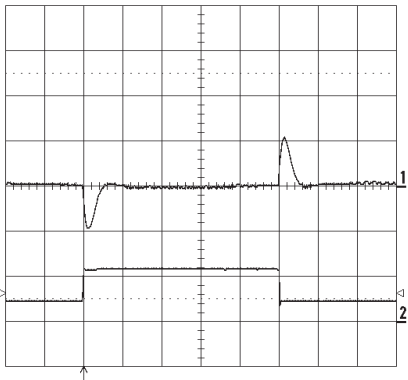Output voltage ripple (20mV/div.) at T_{ref}= +25 °C, Vin = 5 V,
I_O = 16 A resistive load.
Band width = 5 MHz.
Time scale: 2 µs / div.

Transient with 150 µF output capacitor

Output voltage response to load current step-change
(4-12-4 A) at T_{ref} = +25 °C, Vin = 5 V. dI/dt = 5 A/µs
Top trace: output voltage (ac) (100 mV/div.).
Bottom trace: load current (dc) (10 A/div.)
Time scale: 0.1 ms/div.

### Transient with 300 µF output capacitor

Output voltage response to load current step-change
(4-12-4 A) at T_{ref} =+25 °C, Vin = 5 V. dI/dt = 5A/µs
Top trace: output voltage (ac) (100 mV/div.).
Bottom trace: load current (dc) (10 A/div.)
Time scale: 0.1 ms/div.

EMC Specification

Layout Recommendation

The radiated EMI performance of the DC/DC regulator will be optimised by including a ground plane in the PCB area under the DC/DC regulator. This approach will return switching noise to ground as directly as possible, with improvements to both emission and susceptibility.
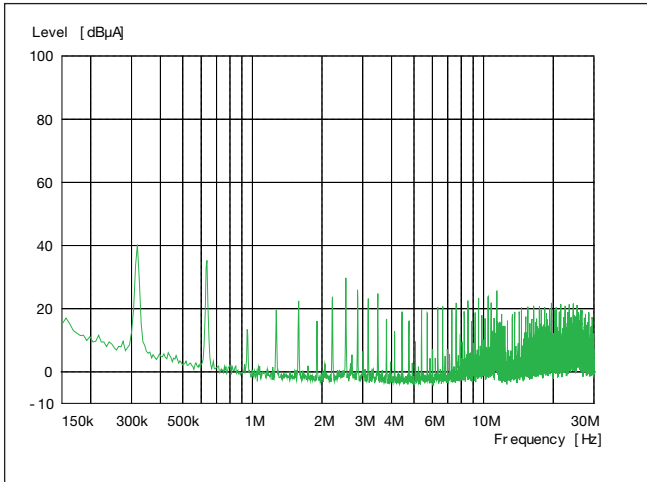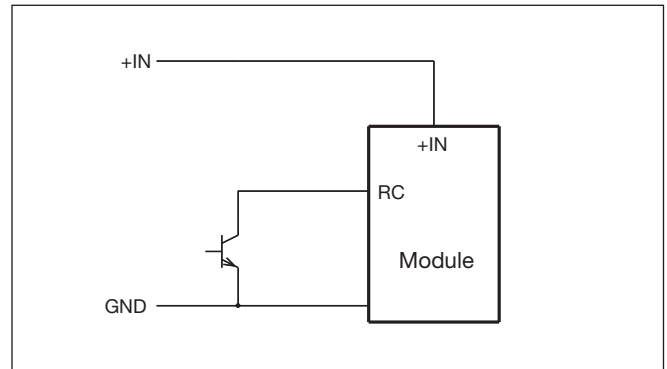

Operating Information

Remote Control (RC)

The RC pin may be used to turn on or turn off the regulator using a suitable open collector function.
Turn off is achieved by connecting the RC pin to ground.

The regulator will run in normal operation when the RC pin is left open.

RC	Regulator condition	min	typ	max	Unit
Low level referenced to GND	OFF	-0.3		0.3	V
Open	ON	1.7		5	V

Remote Sense

All PMC 4000 Series DC/DC regulators have a positive remote sense pin that can be used to compensate for moderate amounts of resistance in the distribution system and allow for voltage regulation at the load or other selected point. The remote sense line will carry very little current and does not need a large cross sectional area. However, the sense line on the PCB should be located close to a ground trace or ground plane. The remote sense circuitry will compensate for up to 10% voltage drop between the sense voltage and the voltage at the output pins from $V_O$nom. If the remote sense is not needed the sense pin should be left open.

Operating Information

Output Voltage Adjust (V_{adj})

The output voltage can be set by means of an external resistor, connected to the V_{adj} pin. Nominal output voltage 0.75 V is set by leaving the V_{adj} pin open. Adjustment can only be made to increase the output voltage setting.

To increase:
Connect a resistor between (Vadj) and (Gnd). The output voltage increases with decreasing resistor value as shown in the table below. Note that the maximum output voltage 3.63 V may not be exceeded.

$$R_{ext} \text{ up (kohm)} = (21.007 / (V_O - 0.75225)) - 5.1$$

Output Voltage (V)	Resistor (ohm)
0.75	Open
1.0	79.691 k
1.2	41.817 k
1.5	22.990 k
1.8	14.949 k
2.5	6.919 k
3.3	3.145 k

Increase

Circuit configuration for output voltage adjust
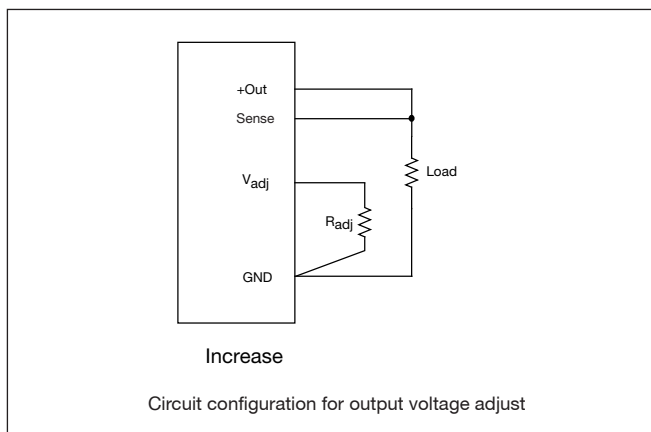
Current Limit Protection

The PMC 4000 Series DC/DC regulators include current limiting circuitry that allows them to withstand continuous overloads or short circuit conditions on the output. The current limit is of hick-up mode type.
The regulator will resume normal operation after removal of the overload. The load distribution system should be designed to carry the maximum output short circuit current specified.

Input And Output Impedance

The impedance of both the power source and the load will interact with the impedance of the DC/DC regulator. It is most important to have a low characteristic impedance, both at the input and output, as the regulators have a low energy storage capability. Use capacitors across the input if the source inductance is greater than 4.7 µH. Suitable input capacitors are 22 µF - 220 µF low ESR ceramics.

Minimum Required External Capacitors

External input capacitors are required to increase the lifetime of the internal capacitors and to further reduce the input ripple. A minimum of 44 µF external input capacitance with low ESR should be added.

A minimum of 150 µF external output capacitance , low ESR, should be added for the converter to operate properly at full load.

Maximum Capacitive Load

When powering loads with significant dynamic current requirements, the voltage regulation at the load can be improved by addition of decoupling capacitance at the load. The most effective technique is to locate low ESR ceramic capacitors as close to the load as possible, using several capacitors to lower the total ESR. These ceramic capacitors will handle short duration high-frequency components of dynamic load changes. In addition, higher values of capacitors (electrolytic capacitors) should be used to handle the mid-frequency components. It is equally important to use good design practice when configuring the DC distribution system.

Low resistance and low inductance PCB layouts and cabling should be used. Remember that when using remote sensing, all resistance (including the ESR), inductance and capacitance of the distribution system is within the feedback loop of the regulator. This can affect on the regulators compensation and the resulting stability and dynamic response performance.

Very low ESR and high capacitance must be used with care. A "rule of thumb" is that the total capacitance must never exceed typically 500-700 µF if only low ESR (< 2 mΩ) ceramic capacitors are used. If more capacitance is needed, a combination of low ESR type and electrolytic capacitors should be used, otherwise the stability will be affected.

The PMC 4000 series regulator can accept up to 8 mF of capacitive load on the output at full load. This gives <500 µF/A of I_O. When using that large capacitance it is important to consider the selection of output capacitors; the resulting behavior is a combination of the amount of capacitance and ESR.

Operating Information

A combination of low ESR and output capacitance exceeding 8 mF can cause the regulator into over current protection mode (hick-up) due to high start up current. The output filter must therefore be designed without exceeding the above stated capacitance levels if the ESR is lower than 30-40 mΩ.

Parallel Operation

The PMC 4000 Series DC/DC regulators can be connected in parallel with a common input. Paralleling is accomplished by connecting the output voltage pins directly and using a load sharing device on the input. Layout considerations should be made to avoid load imbalance.
For more details on paralleling, please consult your local applications support.

Input Undervoltage Lockout

The PMC 4000 Series DC/DC regulators are equipped with a lockout function for low input voltage.When the input voltage is below the undervoltage lockout limit of the regulator it will shut off. When the input voltage increases above the lockout level the regulator will turn on.

Thermal Considerations

General

The PMC 4000 Series DC/DC regulators are designed to operate in a variety of thermal environments, however sufficient cooling should be provided to help ensure reliable operation. Heat is removed by conduction, convection and radiation to the surrounding environment. Increased airflow enhances the heat transfer via convection.
Proper cooling can be verified by measuring the temperature at the reference point (T_{ref}).
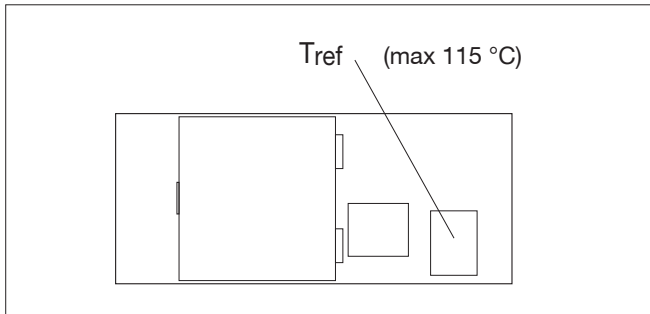
The PMC 4000 thermal testing is performed with the product mounted on an FR4 board 254 × 254 mm with 8 layers of 35 μm copper.
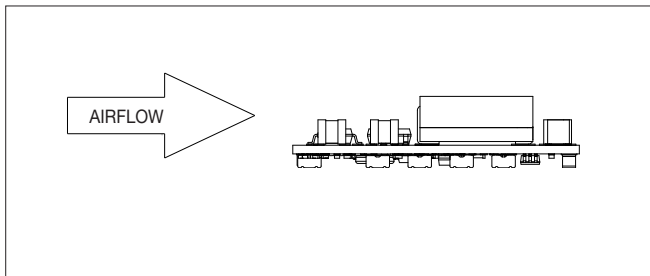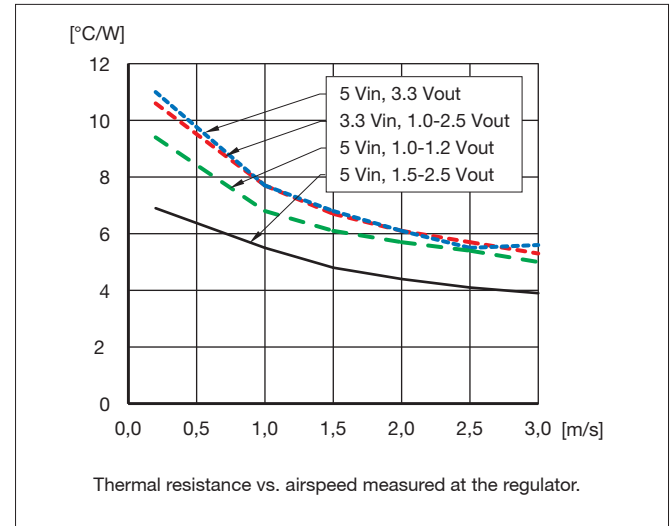

Calculation of ambient temperature

By using the thermal resistance the maximum allowed ambient temperature can be calculated.

A. The powerloss is calculated by using the formula
$((1/\eta) - 1) \times$ output power = power losses.
η = efficiency of regulator. Example: 95% = 0.95

B. Find the value of the thermal resistance $R_{th\ Tref-A}$ in the diagram by using the airflow speed at the module. Take the thermal resistance × powerloss to get the temperature increase.

Thermal resistance vs. airspeed measured at the regulator.

C. Max allowed calculated ambient temperature is:
Max T_{ref} of DC/DC regulator - temperature increase.

Example: 1.8 V output at 1m/s, full load, 3.3 V in:

A. $((1/0.915) - 1) \times 28.8$ W = 2.7 W

B. 2.7 W × 7.5 °C/W = 20.3 °C

C. 115 °C - 20.3 °C = max ambient temperature is 94.7 °C

The real temperature will be dependent on several factors, like PCB size and type, direction of airflow, air turbulence etc. It is recommended to verify the temperature by testing.

Soldering Information

The PMC series DC/DC regulators are intended for reflow soldering processes. Extra precautions must be taken when reflow soldering the module. Neglecting the soldering information given below may result in permanent damage or significant degradation of the power module performance. No responsibility is assumed if these recommendations are not strictly followed.

The module can be reflow soldered using vapour phase reflow (VPR) or forced convection reflow.

To ensure proper soldering of the regulators the temperature should be monitored on interconnection pin GND. The interconnection GND is considered as representative due to the heavy copper path characterisation. A thermocouple can be attached to the pin GND by means of a suitable adhesive or heat conductive paste, see the mechanical data on page 4.

The reflow profile should be optimised to avoid solder paste drying and overheating of the module. Most important is to ensure that the interconnection pins on the coldest aera reach sufficient soldering temperature for sufficiently long time. A sufficiently extended soak time is recommended to ensure an even temperature throughout the PCB, for both small and large components. To reduce the risk of overheating the power module, it is also recommended to minimise the time in reflow as much as possible.

High temperature solders - Reflow profile

Ramp up, ramp-down rate			
Pre-heat	Soak zone	Reflow zone	Cooling
max 3℃/s	max 0.5℃/s	max 3℃/s	max 4℃/s
Temperature interval, time			
	Soak zone	Reflow zone	
	150-200℃, 60-180 s	Above 220℃, 30-80 s	
Peak temperature, time			
		Reflow zone	
		235-245℃, 10-30 s	

For lead free solder processes (solder melting point 217°C), the PMC series is qualified for MSL 1 according to JEDEC standard "J-STD-020c". During reflow, the module temperature must not exceed +245 °C at any time.

Low temperature solder - reflow profile

Ramp up, ramp-down rate			
Pre-heat	Soak zone	Reflow zone	Cooling
max 3℃/s	max 0.5℃/s	max 3℃/s	max 4℃/s
Temperature interval, time			
	Soak zone	Reflow zone	
	130-170℃, 60-120 s	Above 183℃, 30-80 s	
Peak temperature, time			
		Reflow zone	
		210-225℃, 10-30 s	

For conventional Sn-Pb solder processes (solder melting point 179°C -183°C), The PMC series is qualified for MSL 1 according to JEDEC standard "J-STD-020c". During reflow, the module temperature must not exceed +225 °C at any time.

Pin temperatures, graph of the 4-zones of reflow soldering.
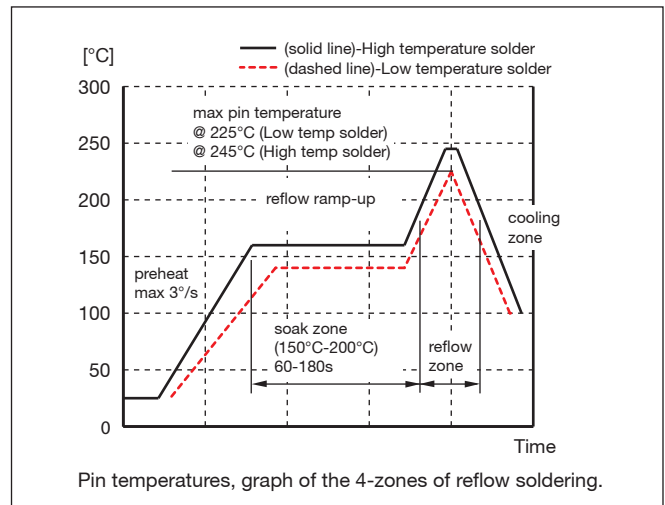
Delivery Package Information

The PMC 4000 series regulators are delivered in antistatic tape & reel (EIA standards 481-2).

Tape & reel specification:

Material:	Conductive
Tape width:	44 mm [1.73 in.]
Tape pitch:	24 mm [0.95 in.]
Total pocket height:	9.1 mm [0.36 in.]
Reel diameter:	330 mm [13 in.]
Reel capacity:	200 pieces
Full reel weight:	typ. 2.0 kg
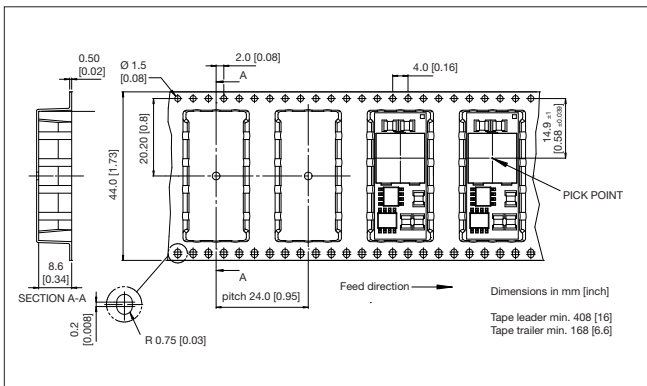
Reliability

The Mean Time Between Failure (MTBF) of the PMC 4000 series DC/DC regulator family is calculated to be greater than 6 million hours at full output power and a reference temperature of +40 °C using TelCordia SR 332.

Compatibility with RoHS requirements

The products are compatible with the relevant clauses and requirements of the RoHS directive 2002/95/EC and have a maximum concentration value of 0.1% by weight in homogeneous materials for lead, mercury, hexavalent chromium, PBB and PBDE and of 0.01% by weight in homogeneous materials for cadmium.

Exemptions in the RoHS directive utilized in Ericsson Power Modules products include:
• Lead in high melting temperature type solder (used to solder the die in semiconductor packages)
• Lead in glass of electronics components and in electronic ceramic parts (e.g. fill material in chip resistors)
• Lead as an alloying element in copper alloy containing up to 4% lead by weight (used in connection pins made of Brass)

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