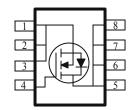
### N-Channel 30-V (D-S) MOSFET

These miniature surface mount MOSFETs utilize a high cell density trench process to provide low  $r_{DS(on)}$  and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$r_{DS(on)} m(\Omega)$ $I_D (A$			
30	13.5	13		
	$20 @ V_{GS} = 4.5V$	11		

- Low r<sub>DS(on)</sub> provides higher efficiency and extends battery life
- Low thermal impedance copper leadframe SOIC-8 saves board space
- Fast switching speed
- High performance trench technology





ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C UNLESS OTHERWISE NOTED)						
Parameter			Limit	Units		
Drain-Source Voltage			30	V		
Gate-Source Voltage			±20	V		
Continuous Drain Current <sup>a</sup>	$T_A=25^{\circ}C$	T	±13			
Continuous Drain Current	$T_A=25^{\circ}C$ $T_A=70^{\circ}C$	П	±11	A		
Pulsed Drain Current <sup>b</sup>			±50			
Continuous Source Current (Diode Conduction) <sup>a</sup>		$I_S$	2.3	A		
D Dii4ia	$T_A=25^{\circ}C$	D <sub></sub>	3.1	W		
Power Dissipation <sup>a</sup>	$T_A=25^{\circ}C$ $T_A=70^{\circ}C$	I D	2.2	<b>* *</b>		
Operating Junction and Storage Temperature Range		$T_{J}, T_{stg}$	-55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Maximum	Units			
Maximum Junction-to-Case <sup>a</sup>	t <= 5 sec	$R_{ heta JC}$	25	°C/W		
Maximum Junction-to-Ambient <sup>a</sup>	t <= 5 sec	$R_{\theta JA}$	50	°C/W		

#### Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

Parameter	Samula al	Test Conditions	Limits			Unit	
rarameter	Symbol Test Conditions		Min	Тур	Max	Unit	
Static							
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \text{ uA}$	1			V	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0 \text{ V}, V_{GS} = 20 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 55^{\circ}\text{C}$			25	uA	
On-State Drain Current <sup>A</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	20			A	
Drain-Source On-Resistance <sup>A</sup>	r <sub>DS(on)</sub>	$V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$ $V_{GS} = 4.5 \text{ V}, I_D = 8 \text{ A}$			13.5	mΩ	
Forward Tranconductance <sup>A</sup>	$g_{\mathrm{fs}}$	$V_{DS} = 15 \text{ V}, I_D = 10 \text{ A}$		40		S	
Diode Forward Voltage	$V_{\mathrm{SD}}$	$I_S = 2.3 \text{ A}, V_{GS} = 0 \text{ V}$		0.7		V	
Dynamic <sup>b</sup>							
Total Gate Charge	$Q_{g}$	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V},$		12.5			
Gate-Source Charge	$Q_{gs}$	$I_{DS} = 13 \text{ V}, \text{ V}_{GS} = 4.3 \text{ V},$ $I_{D} = 10 \text{ A}$		2.6		nC	
Gate-Drain Charge	$Q_{gd}$	1p - 10 A		4.6		1	
Input Capacitance	$C_{iss}$	V = 15 V V = 0 V		1191			
Output Capacitance	$C_{oss}$	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V},$ f = 1 MHz		412		pF	
Reverse Transfer Capacitance	$C_{rss}$	I – HVIIIZ		160		1	
Turn-On Delay Time	$t_{d(on)}$			20			
Rise Time	$t_{\rm r}$	$V_{DD} = 25 \text{ V}, R_L = 25 \Omega, I_D = 1 \text{ A},$		9		nS	
Turn-Off Delay Time	$t_{d(off)}$	$V_{GEN} = 10 \text{ V}$		70		113	
Fall-Time	$t_{\mathrm{f}}$			20			

#### Notes

- a. Pulse test:  $PW \le 300us duty cycle \le 2\%$ .
- b. Guaranteed by design, not subject to production testing.

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### Typical Electrical Characteristics (N-Channel)

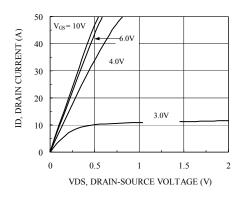


Figure 1. On-Region Characteristics

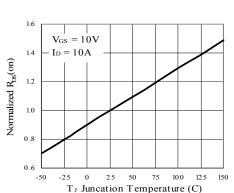


Figure 3. On-Resistance Variation with Temperature

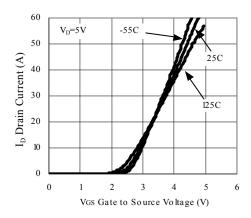


Figure 5. Transfer Characteristics

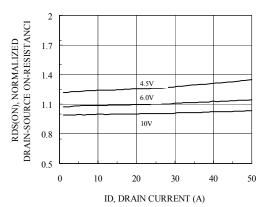


Figure 2. On-Resistance with Drain Current

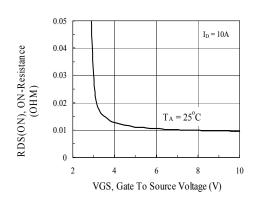


Figure 4. On-Resistance Variation with Gate to Source Voltage

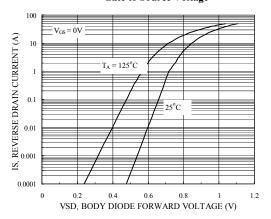


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature

## Typical Electrical Characteristics (N-Channel)

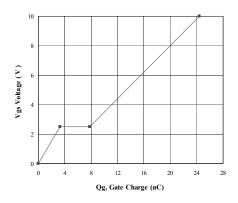


Figure 7. Gate Charge Characteristics

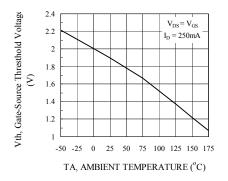


Figure 9. Threshold Vs Ambient Temperature

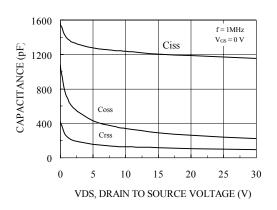


Figure 8. Capacitance Characteristics

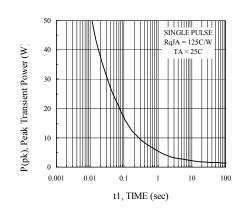


Figure 10. Single Pulse Maximum Power Dissipation

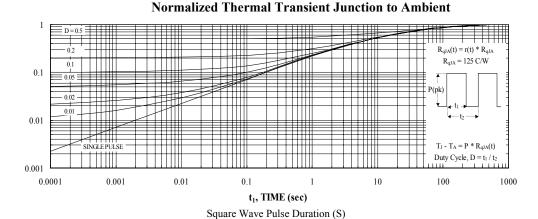
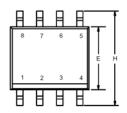
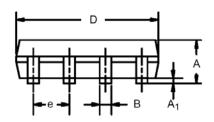


Figure 11. Transient Thermal Response Curve

# Package Information

SO-8: 8LEAD





	MILLIMETERS INC		HES	
Dim	Min	Max	Min	Max
Α	1.35	1.75	0.053	0.069
A <sub>1</sub>	0.10	0.20	0.004	0.008
В	0.35	0.51	0.014	0.020
С	0.19	0.25	0.0075	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
е	1.27 BSC		0.050 BSC	
Н	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°

