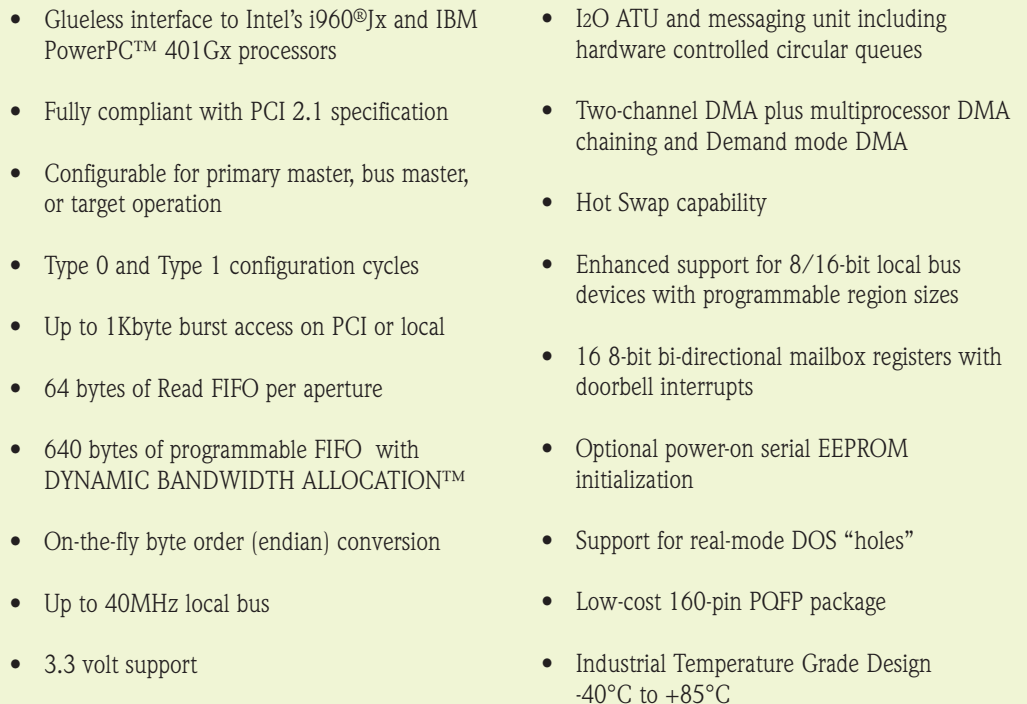


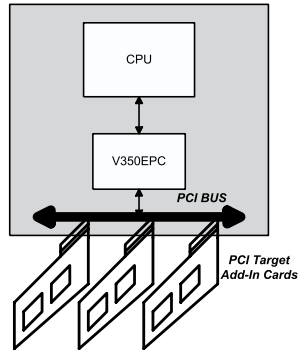


For 32-bit Multiplexed Address/Data Applications

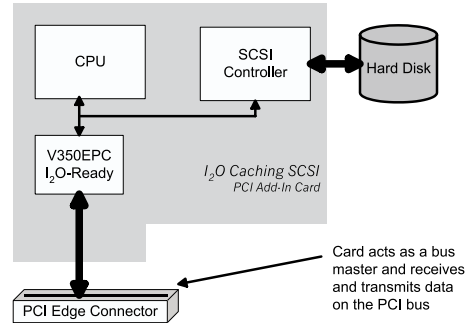


COST EFFECTIVE SOLUTIONS THAT SIMPLIFY EMBEDDED SYSTEM DESIGN!

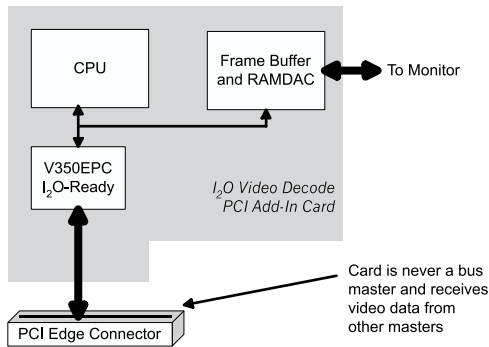
TYPICAL APPLICATIONS



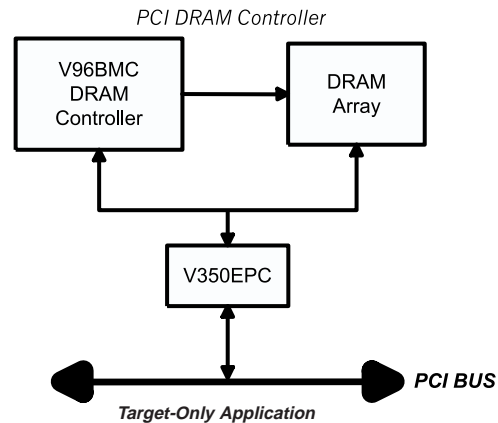
System Master Application



Intelligent Bus Master Application



Intelligent Target-Only Application

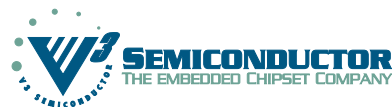


Two unique features of the V350EPC make it the highest performance PCI bridge on the market. One is the size of the FIFO structure; 640 bytes of FIFO storage gives you plenty of buffering to handle continuous transfers of large data streams without tying up the local or PCI buses. The DYNAMIC BANDWIDTH ALLOCATION™ feature of the FIFOs allows you to adjust the “draining” and “filling” of the read and write FIFOs to most efficiently meet the requirements of the data streams. You can balance your bus utilization to maximize overall system performance. The priority of reads, writes, and FIFO flushes can be independently programmed to maintain strict data coherency. The V350EPC provides the most flexible, highest performance, and economical way to add PCI connectivity to applications like imaging and networking. Whether you’re creating a system with the i960 or PowerPC CPU as the PCI master or designing an intelligent add-in card, or even a processor-less application, the V350EPC from V3 Semiconductor is the ideal choice.

ABOUT V3 SEMICONDUCTOR

V3 Semiconductor has been providing embedded systems designers with innovative products since 1987. V3 products are supported throughout North America, Europe, the Middle East and the Far East by a network of distributors and manufacturers’ representatives.

For more information on the V350EPC, or our other i960® and PowerPC™ processors support components, please contact:



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