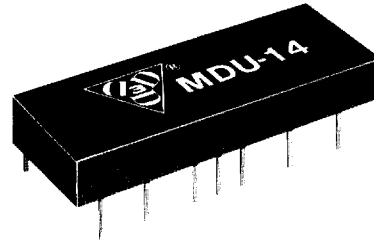


Multiple Digital Delay Units

SERIES: MDU-14
(24 pins DIP) 100K ECL Interfaced



Features:

- Input & output buffered.
- 4 delay lines/package.
- Compatible with ECL circuits.

Specifications:

- Total delay tolerance: $\pm 5\%$ or 1 ns whichever is greater.
- Rise time: 1 ns typ.
- Input pulse width: 40% of total delay.
- Input PRR: $3 \times PW$.
- Input pulse voltage: .8V p-p (-.9V to -1.7V).
- Supply Voltage (Vee): $-5V \pm .7V$.
- Supply Current (Vee): 100 ma typ.
- Operating temperature: $0^{\circ}C$ to $-85^{\circ}C$.
- Temperature coefficient: 100 PPM/ $^{\circ}C$.
- Fan-out capabilities: 70 ECL loads.
- DC parameters: See ECL-100K Logic Table on Page 6.

Part No.	Delay Per Line (ns)
MDU-14-2	2
MDU-14-2.5	2.5
MDU-14-3	3
MDU-14-4	4
MDU-14-5	5
MDU-14-6	6
MDU-14-7	7
MDU-14-8	8
MDU-14-9	9
MDU-14-10	10
MDU-14-12.5	12.5
MDU-14-15	15
MDU-14-20	20
MDU-14-25	25

Other delay times on request.

