



Part: P3203ZA
Series: Four-Port Balanced Three-chip Protector
Technology:SIDACtor® Devices

PARAMETERS		
Package Type	Pkg Type	SIP
Mount Method	Mount	Through Hole
Package Size/Form Factor	size	SIP
Pins for Peak Off-state Voltage $V_{\text{DRM}}-1$	Pins ($V_{\text{DRM}}1$)	"2-3, 4-3, 7-8, 9-8, 12-13, 14-13, 17-18, 19-18"
Peak Off-state Voltage	$V_{\text{DRM}}1$ (V)	270
Switching Voltage	$V_S @ V_{\text{DRM}}1$ (V)	350
Pins for Peak Off-state Voltage $V_{\text{DRM}}-2$	Pins ($V_{\text{DRM}}2$)	"2-4, 7-9, 12-14, 17-19"
Peak Off-state Voltage	$V_{\text{DRM}}2$ (V)	270
Switching Voltage	$V_S @ V_{\text{DRM}}2$ (V)	350
On-state Voltage	$V_T \text{ MAX}$ (V)	8.0000
On-state forward Voltage	V_F (V)	
Leakage Current	I_{DRM}	5
Switching Current	$I_S \text{ MAX}$ (mA)	800
Holding Current	$I_H \text{ MIN}$ (mA)	150
Minimum Off-state Capacitance	C_O (pF) Min	30.0
Maximum Off-state Capacitance	C_O (pF) Max	30
Peak Pulse Current - 2x10 $\hat{\mu}$ s	$I_{\text{PP}} 2\text{x}10\hat{\mu}\text{s}$ (A)	150
Peak Pulse Current - 8x20 $\hat{\mu}$ s	$I_{\text{PP}} 8\text{x}20\hat{\mu}\text{s}$ (A)	150
Peak Pulse Current - 10x160 $\hat{\mu}$ s	$I_{\text{PP}} 10\text{x}160\hat{\mu}\text{s}$ (A)	90
Peak Pulse Current - 10x560 $\hat{\mu}$ s	$I_{\text{PP}} 10\text{x}560\hat{\mu}\text{s}$ (A)	50
Peak Pulse Current - 10x1000 $\hat{\mu}$ s	$I_{\text{PP}} 10\text{x}1000\hat{\mu}\text{s}$ (A)	45
Peak Pulse Current - 5x310 $\hat{\mu}$ s	$I_{\text{PP}} 5\text{x}310\hat{\mu}\text{s}$ (A)	0
Peak Pulse Current - 5x320 $\hat{\mu}$ s	$I_{\text{PP}} 5\text{x}320\hat{\mu}\text{s}$ (A)	0
Peak Pulse Current - 10x700 $\hat{\mu}$ s	$I_{\text{PP}} 10\text{x}700\hat{\mu}\text{s}$ (A)	0
Peak One-cycle Surge Current	$I_{\text{TSM}} 60\text{Hz}$ (A)	20
Rate of Rise of Current	$di/dt \text{ MAX}$ (A/ $\hat{\mu}$ s)	500
Certifications	Cert	



PROTECTS AGAINST:

Lightning Transients

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