

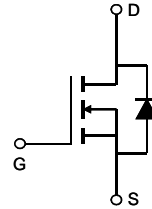
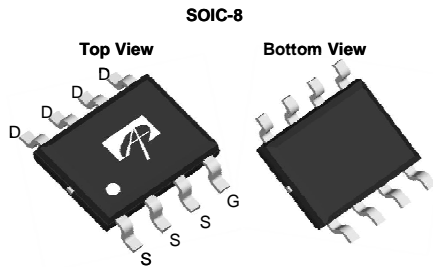
### General Description

The AO4310 uses trench MOSFET technology that is uniquely optimized to provide the most efficient high frequency switching performance. Power losses are minimized due to an extremely low combination of  $R_{DS(ON)}$  and  $C_{rss}$ . In addition, switching behavior is well controlled with a "Schottky style" soft recovery body diode.

### Product Summary

$V_{DS}$	36V
$I_D$ (at $V_{GS}=10V$ )	27A
$R_{DS(ON)}$ (at $V_{GS}=10V$ )	< 3.1m $\Omega$
$R_{DS(ON)}$ (at $V_{GS} = 4.5V$ )	< 4.2m $\Omega$

100% UIS Tested  
 100%  $R_g$  Tested



### Absolute Maximum Ratings $T_A=25^\circ C$ unless otherwise noted

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	$V_{DS}$	36	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_D$	$T_A=25^\circ C$	27
		$T_A=70^\circ C$	22
Pulsed Drain Current <sup>C</sup>	$I_{DM}$	390	A
Avalanche Current <sup>C</sup>	$I_{AS}, I_{AR}$	67	A
Avalanche energy $L=0.1mH$ <sup>C</sup>	$E_{AS}, E_{AR}$	224	mJ
Power Dissipation <sup>B</sup>	$P_D$	$T_A=25^\circ C$	3.6
		$T_A=70^\circ C$	2.3
Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	$^\circ C$

### Thermal Characteristics

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient <sup>A</sup>	$R_{\theta JA}$	27	35	$^\circ C/W$
Maximum Junction-to-Ambient <sup>A D</sup>		52	65	$^\circ C/W$
Maximum Junction-to-Lead	$R_{\theta JL}$	10	15	$^\circ C/W$

**Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise noted)**

Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>STATIC PARAMETERS</b>						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	I <sub>D</sub> =250μA, V <sub>GS</sub> =0V	36			V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =36V, V <sub>GS</sub> =0V T <sub>J</sub> =55°C			1 5	μA
I <sub>GSS</sub>	Gate-Body leakage current	V <sub>DS</sub> =0V, V <sub>GS</sub> = ±20V			100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.2	1.8	2.3	V
I <sub>D(ON)</sub>	On state drain current	V <sub>GS</sub> =10V, V <sub>DS</sub> =5V	390			A
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =20A T <sub>J</sub> =125°C		2.6 3.7	3.1 4.5	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =20A		3.3	4.2	
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>D</sub> =20A		151		S
V <sub>SD</sub>	Diode Forward Voltage	I <sub>S</sub> =1A, V <sub>GS</sub> =0V		0.7	1	V
I <sub>S</sub>	Maximum Body-Diode Continuous Current				5	A
<b>DYNAMIC PARAMETERS</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V, V <sub>DS</sub> =18V, f=1MHz	2595	3248	3900	pF
C <sub>oss</sub>	Output Capacitance		790	1130	1470	pF
C <sub>riss</sub>	Reverse Transfer Capacitance		16	54	95	pF
R <sub>g</sub>	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz	0.8	1.8	2.7	Ω
<b>SWITCHING PARAMETERS</b>						
Q <sub>g(10V)</sub>	Total Gate Charge	V <sub>GS</sub> =10V, V <sub>DS</sub> =18V, I <sub>D</sub> =20A	37	46.4	57	nC
Q <sub>g(4.5V)</sub>	Total Gate Charge		16	20.6	27	nC
Q <sub>gs</sub>	Gate Source Charge		8			nC
Q <sub>gd</sub>	Gate Drain Charge		6.2			nC
t <sub>D(on)</sub>	Turn-On DelayTime	V <sub>GS</sub> =10V, V <sub>DS</sub> =18V, R <sub>L</sub> =0.9Ω, R <sub>GEN</sub> =3Ω		8.5		ns
t <sub>r</sub>	Turn-On Rise Time		4.8			ns
t <sub>D(off)</sub>	Turn-Off DelayTime		40.8			ns
t <sub>f</sub>	Turn-Off Fall Time		9.8			ns
t <sub>rr</sub>	Body Diode Reverse Recovery Time	I <sub>F</sub> =20A, dI/dt=500A/μs	17	21.7	26	ns
Q <sub>rr</sub>	Body Diode Reverse Recovery Charge	I <sub>F</sub> =20A, dI/dt=500A/μs	48	60.5	73	nC

A. The value of R<sub>θJA</sub> is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with T<sub>A</sub>=25° C. The value in any given application depends on the user's specific board design.

B. The power dissipation P<sub>D</sub> is based on T<sub>J(MAX)</sub>=150° C, using ≤ 10s junction-to-ambient thermal resistance.

C. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150° C. Ratings are based on low frequency and duty cycles to keep initial T<sub>J</sub>=25° C.

D. The R<sub>θJA</sub> is the sum of the thermal impedance from junction to lead R<sub>θJL</sub> and lead to ambient.

E. The static characteristics in Figures 1 to 6 are obtained using <300μs pulses, duty cycle 0.5% max.

F. These curves are based on the junction-to-ambient thermal impedance which is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, assuming a maximum junction temperature of T<sub>J(MAX)</sub>=150° C. The SOA curve provides a single pulse rating.

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**TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS**

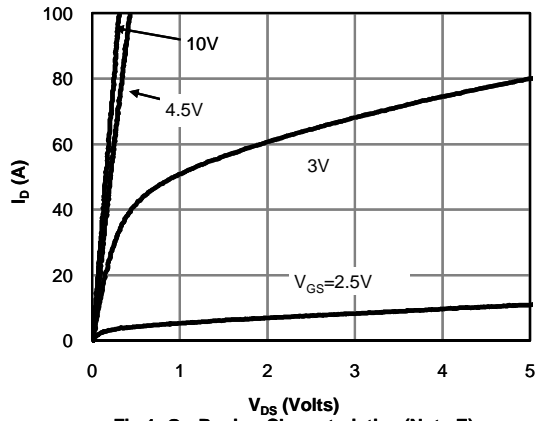


Figure 1: On-Region Characteristics (Note E)

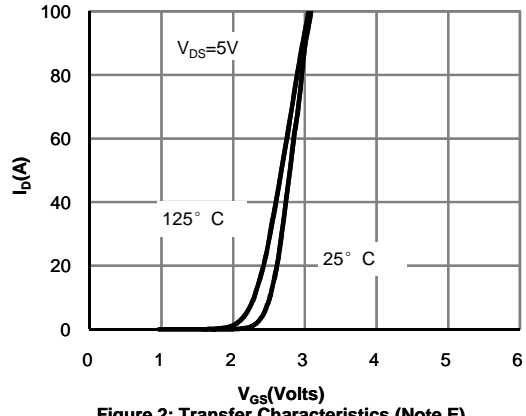


Figure 2: Transfer Characteristics (Note E)

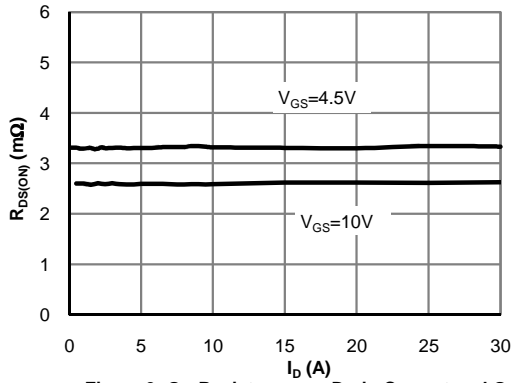


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

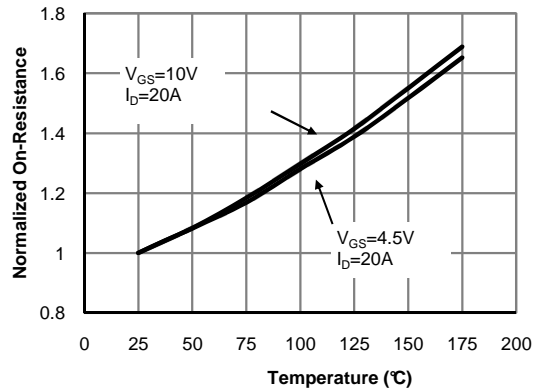


Figure 4: On-Resistance vs. Junction Temperature (Note E)

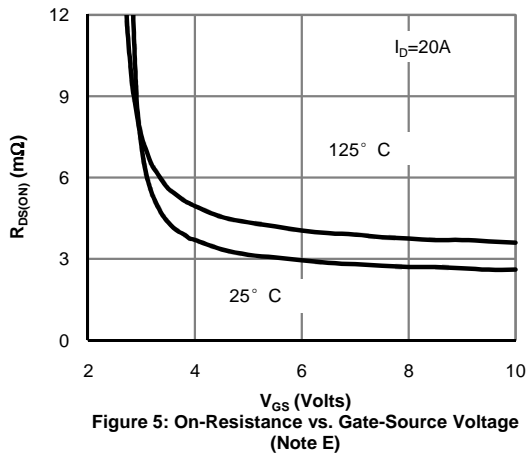


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

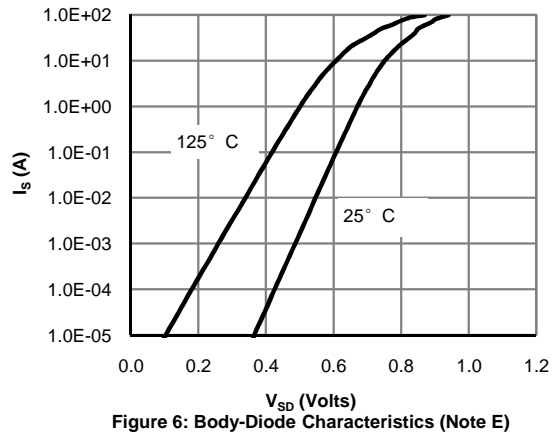
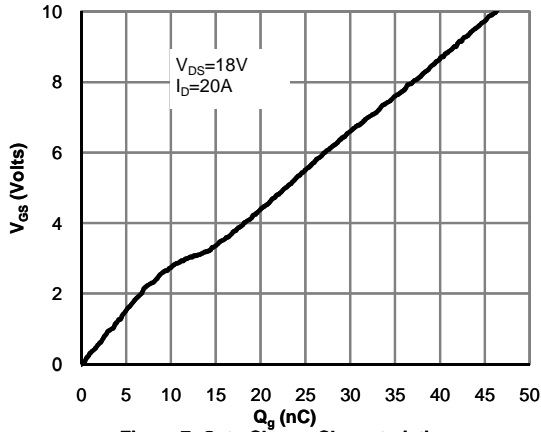
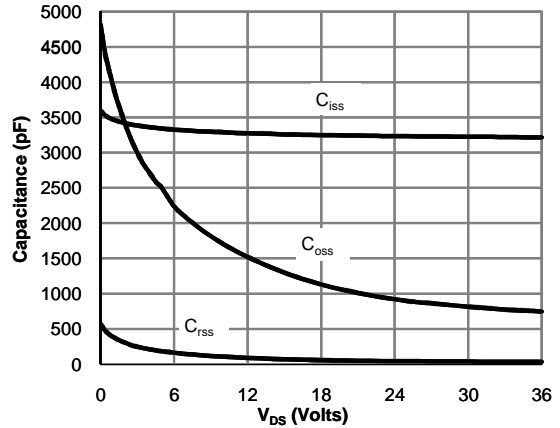


Figure 6: Body-Diode Characteristics (Note E)

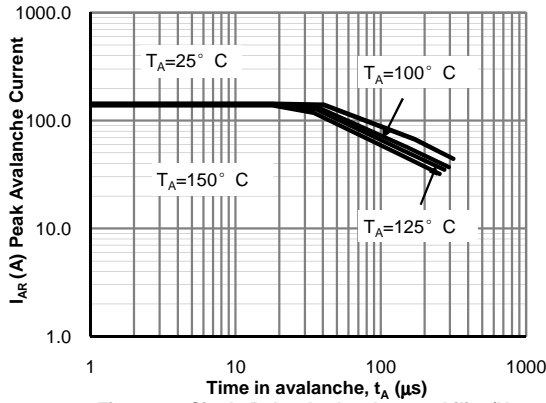
**TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS**



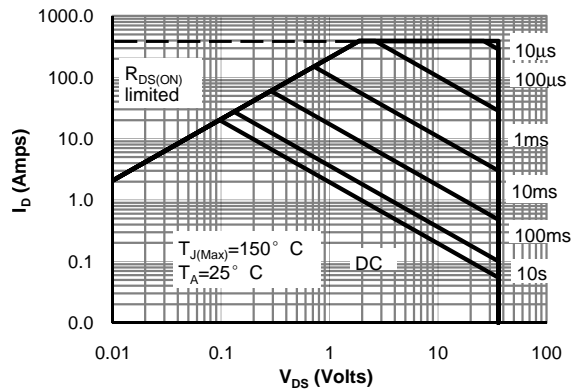
**Figure 7: Gate-Charge Characteristics**



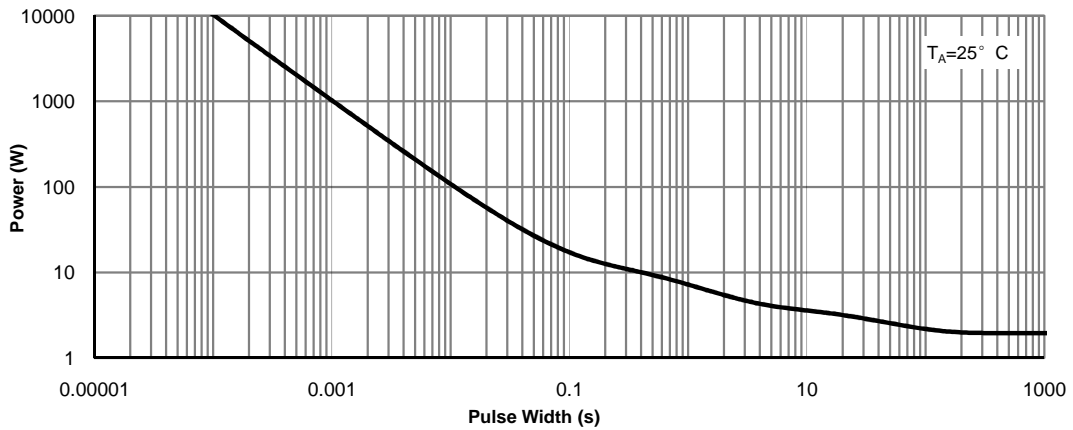
**Figure 8: Capacitance Characteristics**



**Figure 12: Single Pulse Avalanche capability (Note C)**

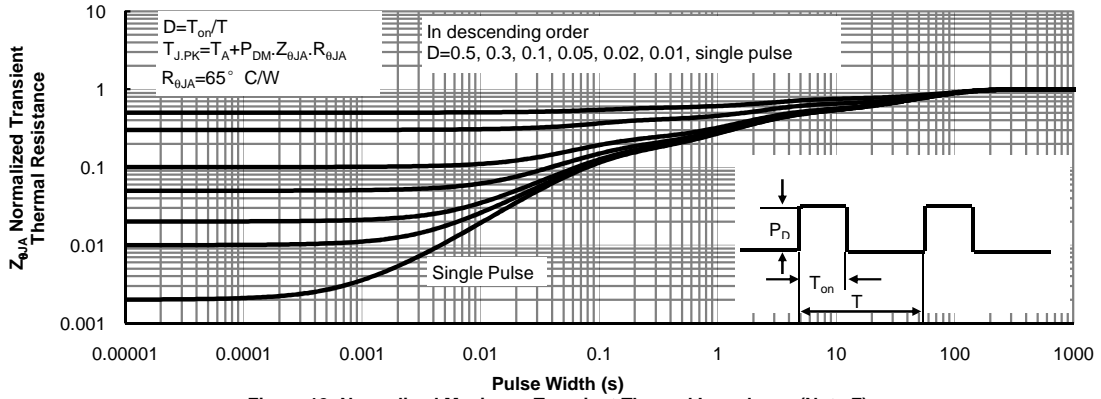


**Figure 10: Maximum Forward Biased Safe Operating Area (Note F)**



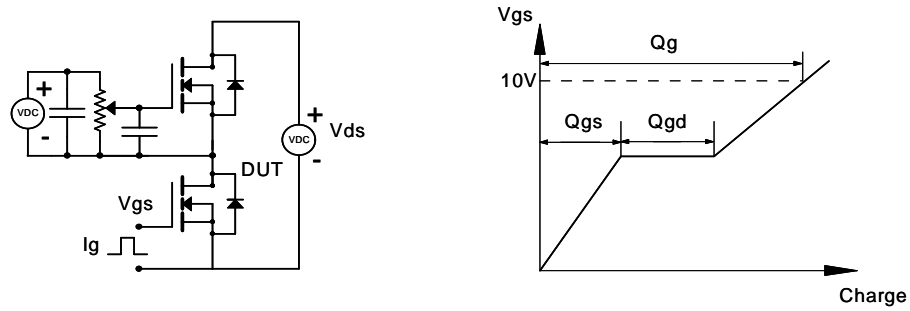
**Figure 11: Single Pulse Power Rating Junction-to-Ambient (Note F)**

**TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS**

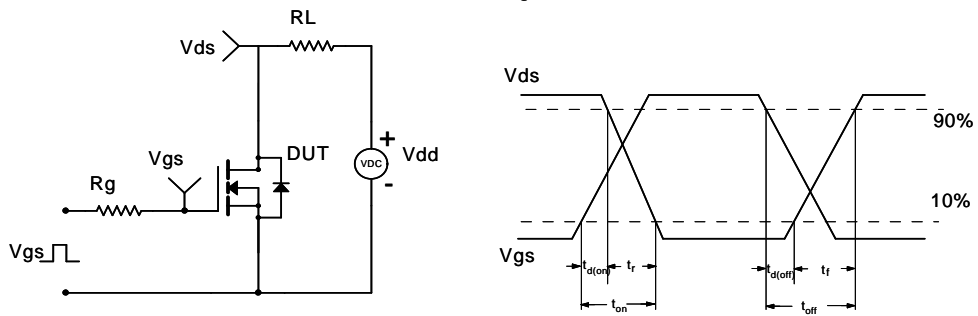


**Figure 12: Normalized Maximum Transient Thermal Impedance (Note F)**

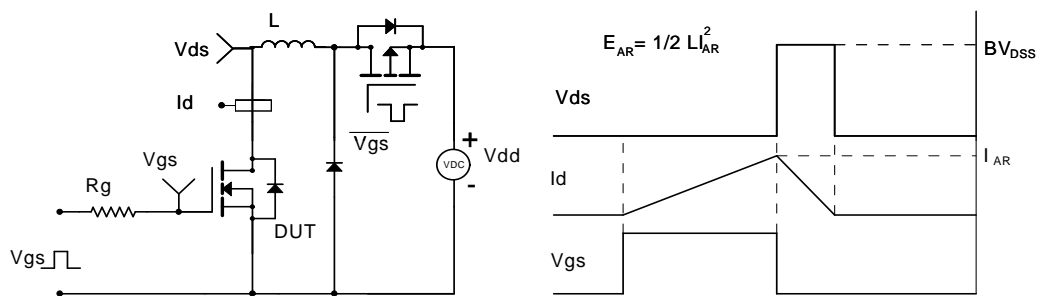
**Gate Charge Test Circuit & Waveform**



**Resistive Switching Test Circuit & Waveforms**



**Unclamped Inductive Switching (UIS) Test Circuit & Waveforms**



**Diode Recovery Test Circuit & Waveforms**

