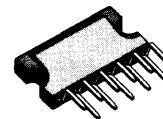


PULSE DRIVEN VERTICAL BOOSTER

ADVANCE DATA

- OUTPUT CURRENT UP TO 2.5App
- 70V MAXIMUM FLYBACK VOLTAGE
- INTERNAL FLYBACK GENERATOR
- INTERNAL REFERENCE VOLTAGE GENERATOR
- INTERNAL RAMP GENERATOR
- SAWTOOTH STARTED BY THE END OF SYNC PULSE OR BY AN INTERNAL MONOSTABLE
- THERMAL PROTECTION

**CLIPWATT 11**
(Plastic Package)**ORDER CODE : STV9303W****DESCRIPTION**

The STV9303 is a pulse driven vertical booster intended for use in color TV. It includes a vertical ramp generator specially designed to fit with deflection processors like STV2102 or STV2110 which provide a 10.5 line length vertical pulse. The discharging of the sawtooth capacitor is triggered by the trailing edge of the vertical sync and the charging by the leading edge.

During the sync pulse duration, the sawtooth will remain at its bottom value. Another possibility is to use the internal monostable (by connecting an external capacitor on Pin 5) to define the point where the sawtooth will restart. This second possibility is very useful to avoid interlacing problems when using a conventional deflection processor delivering a small duration vertical pulse.

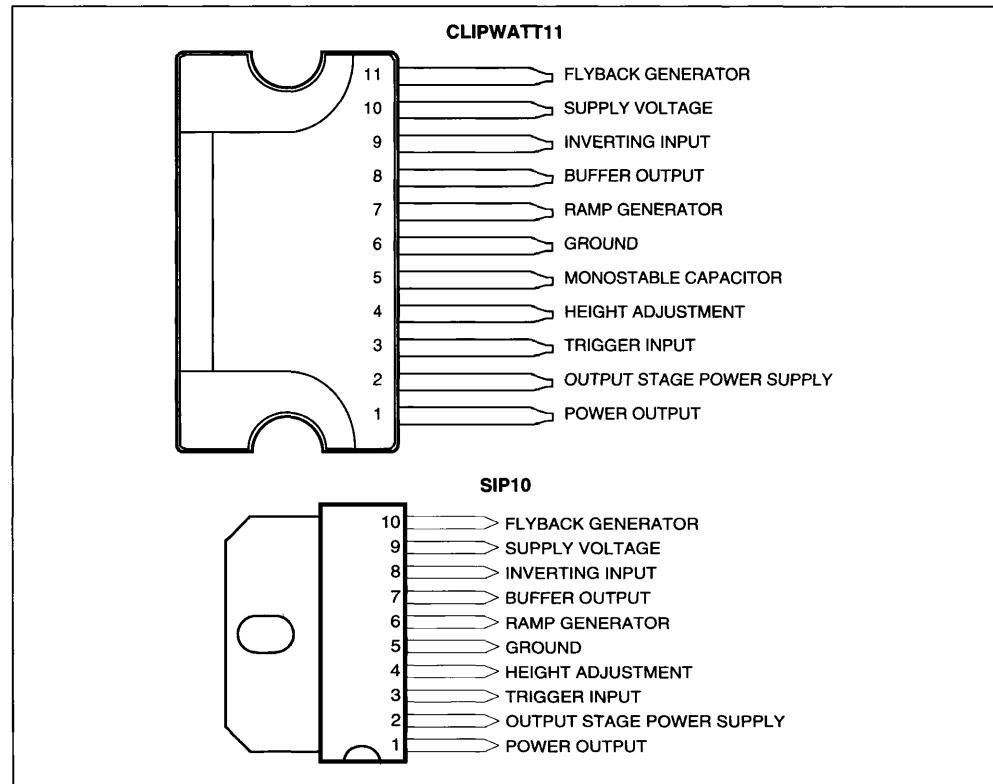
The STV9303 includes a very efficient power amplifier for direct driving of a TV picture tube in B & W or color television.

For power consumption saving, a flyback generator is also included. The current and voltage capabilities (2.5App max output current and 70V flyback peak voltage), make this IC also suitable for large screen TV sets.

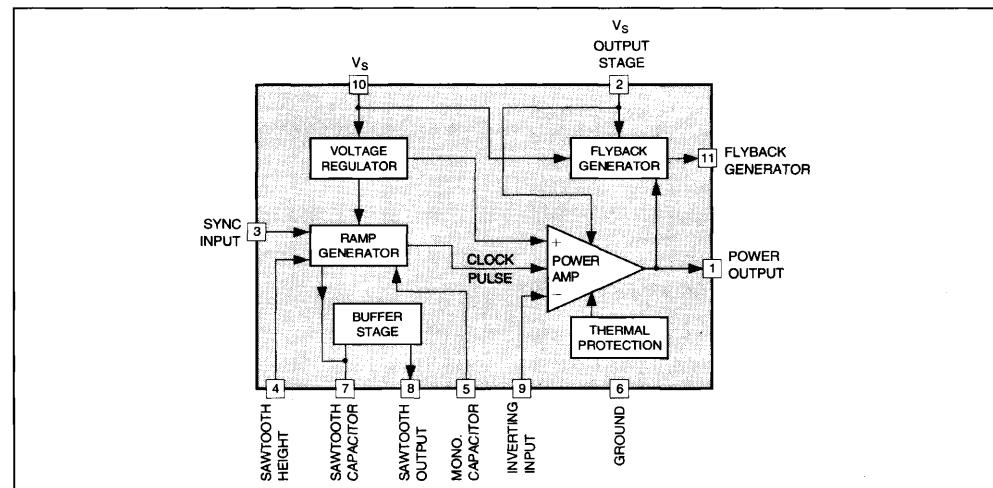
Thermal protection is also provided.

**SIP10**
(Plastic Package)**ORDER CODE : STV9303**

PIN CONNECTIONS



BLOCK DIAGRAM (CLIPWATT)



ABSOLUTE MAXIMUM RATINGS (CLIPWATT Pin Connections)

| Symbol | Parameter | Pins | Value | Unit |
|------------|---|------|--------------------|------|
| V_S | Supply Voltage | 10 | 35 | V |
| V_F, V_O | Flyback Voltage | 1-2 | 70 | V |
| V_I | Amplifier Input Voltage | 9 | V_S | V |
| I_{OP} | Peak Output Current | 1 | 1.5 | A |
| I_{11} | Flyback DC Current at $V_O < V_S$ | 11 | 100 | mA |
| I_{11} | Flyback Peak Current ($f = 50$ or 60Hz , $T_{fly} < 1.5\text{ms}$) | 11 | 1.8 | A |
| V_3 | Trigger Input Voltage | 3 | V_S | V |
| T_{stg} | Storage Temperature | | -40, +150 | °C |
| T_J | Junction Temperature | | Internally limited | |

9303V-01 TBL

9303V-02 TBL

THERMAL DATA

| Symbol | Parameter | | Value | Unit |
|---------------|--|---------------------|--------------|--------------|
| T_{pt} | Junction Temperature at Thermal Shutdown | Typ. | 140 | °C |
| T_{ph} | Thermal Protection Hysteresis | Typ. | 25 | °C |
| $R_{th(j-c)}$ | Junction-case Thermal Resistance | CLIPWATT11 SIP10 | Max. Max. | °C/W °C/W |

9303V-02 TBL

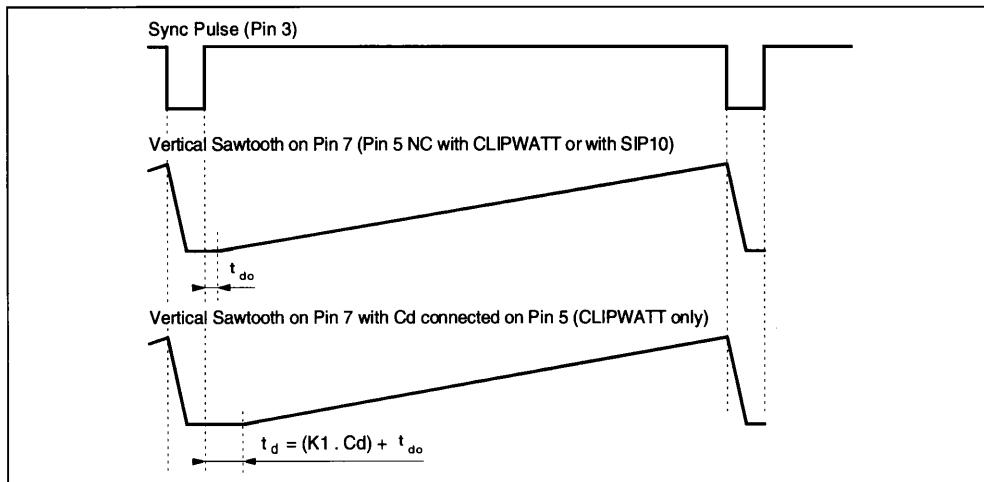
ELECTRICAL CHARACTERISTICS (CLIPWATT Pin Connections)

(VS = 35V, Tamb = 25°C, unless otherwise specified)

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|--------------|--|--|------------|------------|----------|-------|
| I_2 | Pin 2 Quiescent Current | $I_1 = 0, I_{11} = 0$ | 16 | 36 | mA | |
| I_{10} | Pin 10 Quiescent Current | $I_1 = 0, I_{11} = 0$ | 15 | 30 | mA | |
| $-I_7$ | Ramp Generator Bias Current* | $V_7 = 0$ | | 1 | μA | |
| $-I_7$ | Ramp Generator Current | $V_7 = 0, -I_4 = 20\mu\text{A}$ | 18.5 | 20 | 21.5 | μA |
| dI/dI_7 | Ramp Generator Linearity | $V_7 = 0$ to 12V , $-I_4 = 20\mu\text{A}$ | 0.2 | 1 | % | |
| V_{1L} | Out Saturation Voltage to GND | $I_1 = 0.1\text{A}$ $I_1 = 1.25\text{A}$ | 0.1 1.2 | 1 2.0 | 1 V | V |
| V_{1H} | Out Saturation Voltage to VS | $-I_1 = 0.1\text{A}$ $-I_1 = 1.25\text{A}$ | 0.9 1.6 | 1.6 3.0 | 1.6 V | V |
| V_4 | Reference Voltage | $-I_4 = 20\mu\text{A}$ | 6.3 | 6.6 | 6.9 | V |
| dV_4/VS | Reference Voltage Drift versus VS | $VS = 10\text{V}$ to 35V | 1 | 2 | mV/V | |
| dV_4/dI_4 | Reference Voltage Drift versus I_4 | $I_4 = 10\mu\text{A}$ to $30\mu\text{A}$ | 0.1 | 1 | mV/μA | |
| V_R | Internal Reference Voltage | | 4.15 | 4.40 | 4.65 | V |
| V_{D11-10} | Diode Fwd Voltage | $I_D = 1.25\text{A}$ | 1.5 | 3 | V | |
| V_{D1-2} | Diode Fwd Voltage | $I_D = 1.25\text{A}$ | 1.5 | 3 | V | |
| G_V | Output Stage Open Loop Gain | $f = 100\text{Hz}$ | 70 | | dB | |
| V_{1S} | V10-11 Saturation Voltage | $-I_{11} = 1.25\text{A}$ | 1.5 | 3.0 | V | |
| V_{11} | Pin 11 Saturation Voltage | $I_{11} = 20\text{mA}$ | 0.8 | 2 | V | |
| V_3 | Trigger Input Threshold | | 2.6 | 3.0 | 3.4 | V |
| V_8 | Sawtooth Pedestal Voltage | | | 1.85 | | V |
| I_1 | Peak-to-peak Operating Current Range | | 0.4 | 2.5 | A | |
| V_{7M} | Max. Voltage on Pin 7 | | 12 | | V | |
| I_7 | Min. Discharging Current | | 5 | | mA | |
| K1 | Delay between end of Sync Pulse and beginning of Sawtooth versus value of Capacitor on Pin 5 | | | 100 | | μs/nF |
| t_{do} | Max. Delay between end of Sync Pulse and beginning of Sawtooth Capacitor charging without Capacitor on Pin 5 | | | 2 | 10 | μs |

9303V-03 TBL

WAVEFORMS



APPLICATION CIRCUIT (CLIPWATT)

