Preferred Device

SWITCHMODE [™] **Schottky Power Rectifier**

The SWITCHMODE Power Rectifier employs the Schottky Barrier principle in a large area metal-to-silicon power diode. State-of-the-art geometry features epitaxial construction with oxide passivation and metal overlay contact. Ideally suited for use as rectifiers in very low-voltage, high-frequency switching power supplies, free wheeling diodes and polarity protection diodes.

Features

- Highly Stable Oxide Passivated Junction
- Very Low Forward Voltage Drop
- Matched Dual Die Construction
- High Junction Temperature Capability
- High dv/dt Capability
- Excellent Ability to Withstand Reverse Avalanche Energy Transients
- Guardring for Stress Protection
- Epoxy Meets UL 94 V-0 @ 0.125 in
- Electrically Isolated. No Isolation Hardware Required.
- UL Recognized File #E69369 (Note 1)
- Pb-Free Package is Available*

Mechanical Characteristics:

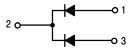
- Case: Epoxy, Molded
- Weight: 1.9 Grams (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Max. for 10 Seconds



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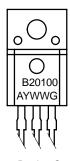
SCHOTTKY BARRIER RECTIFIER 20 AMPERES, 100 VOLTS





ISOLATED TO-220 CASE 221D STYLE 3

MARKING DIAGRAM



B20100 = Device Code A = Assembly Location

Y = Year WW = Work Week G = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping
MBRF20100CT	TO-220	50 Units/Rail
MBRF20100CTG	TO-220 (Pb-Free)	50 Units/Rail

Preferred devices are recommended choices for future use and best overall value.

1. UL Recognized mounting method is per Figure 4

^{*}For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

MAXIMUM RATINGS (Per Leg)

Rating		Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		V _{RRM} V _{RWM} V _R	100	V
Average Rectified Forward Current (Rated V_R), $T_C = 133^{\circ}C$	otal Device	I _{F(AV)}	10 20	Α
Peak Repetitive Forward Current (Rated V_R , Square Wave, 20 kHz), $T_C = 133^{\circ}C$		I _{FRM}	20	Α
Non-repetitive Peak Surge Current (Surge applied at rated load conditions halfwave, single phase, 60 Hz)		I _{FSM}	150	Α
Peak Repetitive Reverse Surge Current (2.0 μs, 1.0 kHz)		I _{RRM}	0.5	Α
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 65 to +150	°C
Voltage Rate of Change (Rated V _R)		dv/dt	10000	V/μs
RMS Isolation Voltage (t = 1.0 second, R.H. \leq 30%, T _A = 25°C) (Note 2) Per Figu	Per Figure 3 ure 4 (Note 1) Per Figure 5	V _{iso1} V _{iso2} V _{iso3}	4500 3500 1500	V

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS (Per Leg)

Rating	Symbol	Value	Unit
Maximum Thermal Resistance, Junction to Case		3.5	°C/W
Lead Temperature for Soldering Purposes: 1/8" from Case for 5 Seconds		260	°C

ELECTRICAL CHARACTERISTICS (Per Leg)

Characteristic	Symbol	Max	Unit
Maximum Instantaneous Forward Voltage (Note 3) $ \begin{aligned} &(i_F=10 \text{ Amp, } T_C=25^\circ\text{C})\\ &(i_F=10 \text{ Amp, } T_C=125^\circ\text{C})\\ &(i_F=20 \text{ Amp, } T_C=25^\circ\text{C})\\ &(i_F=20 \text{ Amp, } T_C=125^\circ\text{C}) \end{aligned} $	VF	0.85 0.75 0.95 0.85	٧
Maximum Instantaneous Reverse Current (Note 3) (Rated DC Voltage, $T_C = 25^{\circ}C$) (Rated DC Voltage, $T_C = 125^{\circ}C$)	i _R	0.15 150	mA

- 1. UL Recognized mounting method is per Figure 4
- 2. Proper strike and creepage distance must be provided.
- 3. Pulse Test: Pulse Width = 300 µs, Duty Cycle ≤ 2.0%

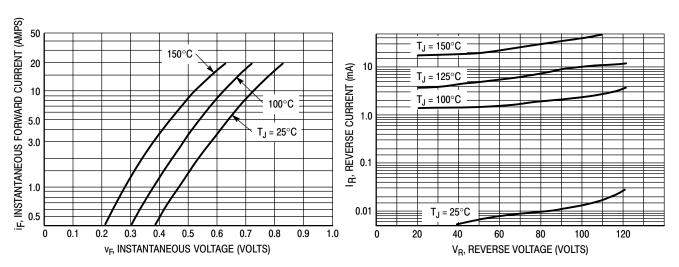


Figure 1. Typical Forward Voltage Per Diode

Figure 2. Typical Reverse Current Per Diode

TEST CONDITIONS FOR ISOLATION TESTS*

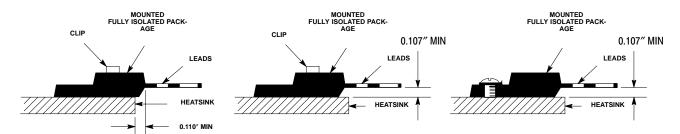


Figure 3. Clip Mounting Position for Isolation Test Number 1

Figure 4. Clip Mounting Position for Isolation Test Number 2

Figure 5. Screw Mounting Position for Isolation Test Number 3

MOUNTING INFORMATION**

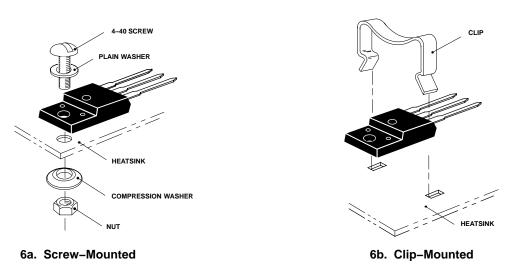


Figure 6. Typical Mounting Techniques

Laboratory tests on a limited number of samples indicate, when using the screw and compression washer mounting technique, a screw torque of 6 to 8 in · lbs is sufficient to provide maximum power dissipation capability. The compression washer helps to maintain a constant pressure on the package over time and during large temperature excursions.

Destructive laboratory tests show that using a hex head 4–40 screw, without washers, and applying a torque in excess of 20 in · lbs will cause the plastic to crack around the mounting hole, resulting in a loss of isolation capability.

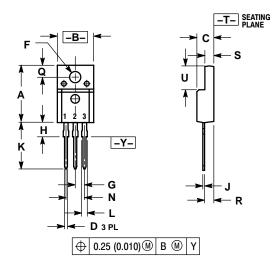
Additional tests on slotted 4–40 screws indicate that the screw slot fails between 15 to 20 in \cdot lbs without adversely affecting the package. However, in order to positively ensure the package integrity of the fully isolated device, ON Semiconductor does not recommend exceeding 10 in \cdot lbs of mounting torque under any mounting conditions.

^{*}Measurement made between leads and heatsink with all leads shorted together.

^{**}For more information about mounting power semiconductors see Application Note AN1040.

PACKAGE DIMENSIONS

TO-220 FULLPAK CASE 221D-03 ISSUE G



NOTES

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH
- 221D-01 THRU 221D-02 OBSOLETE, NEW STANDARD 221D-03.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.625	0.635	15.88	16.12	
В	0.408	0.418	10.37	10.63	
С	0.180	0.190	4.57	4.83	
D	0.026	0.031	0.65	0.78	
F	0.116	0.119	2.95	3.02	
G	0.100 BSC		2.54 BSC		
Н	0.125	0.135	3.18	3.43	
J	0.018	0.025	0.45	0.63	
K	0.530	0.540	13.47	13.73	
L	0.048	0.053	1.23	1.36	
N	0.200	0.200 BSC		5.08 BSC	
Q	0.124	0.128	3.15	3.25	
R	0.099	0.103	2.51	2.62	
S	0.101	0.113	2.57	2.87	
U	0.238	0.258	6.06	6.56	

STYLE 3: PIN 1. ANODE

2. CATHODE ANODE

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