TrueTriangle™

Preliminary Product Specifications

July 2002 *(1 of 6)*

1.75 to 1.78 GHz 3.5V, 28.5 dBm, Korea PCS InGaP HBT Amplifier Module

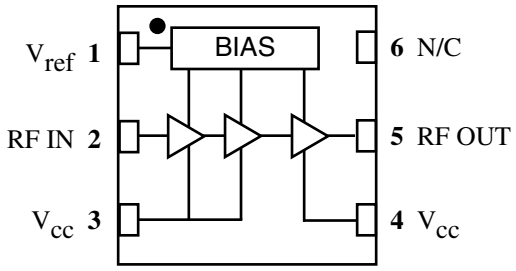
Features

- ❑ InGaP HBT Technology
- ❑ 6mm Square, 50 Ohm Power Module
- ❑ Single Positive Supply
- ❑ 35% Linear Power Added Efficiency
- ❑ +28.5 dBm Output Power (CDMA)
- ❑ +28.0 dBm Output Power (CDMA2K 1X)
- ❑ 30 dB Gain at Operating Output Power
- ❑ On-Board Power Down Mode

Applications

- ❑ Korea PCS Handsets
- ❑ CDMA Handsets
- ❑ CDMA2K 1X Handsets

Functional Block Diagram



Ground connection is on backside

Description

The CHP1230-PM is a 50 ohm matched, single supply, linear power amplifier module intended for use in Korean handsets. The highly integrated amplifier meets the requirements of Korea PCS CDMA systems. It is a member of Celeritek's new ***TrueTriangle™*** family of 3V power amplifier modules.

The CHP1230-PM is packaged in a low-cost, space efficient, 6mm square, matched module that provides excellent electrical stability and low thermal resistance. The module operates from a fixed positive voltage and requires no external matching which significantly reduces space, cost and enhances ease of use.

The 6x6 mm package is self contained, incorporating 50 ohm input and output matching networks optimized for output power, linearity and efficiency.

Celeritek's InGaP HBT technology offers a thermally robust and reliable PAM (power amplifier module) solution.

Absolute Maximum Ratings

Parameter	Rating	Parameter	Rating	Parameter	Rating
Collector Voltage (+V_{cc})	+6.0 V*	Reference Voltage (V_{ref})	+3.1 V	Operating Temperature	-40°C to +85°C
Collector Current (I_{cc})	1.2 A	Power Dissipation	5 W	Storage Temperature	-65°C to +150°C
RF Input Power	7 dBm			Soldering Temperature	260°C for 5 Sec.

* RF Off.

Recommended Operating Conditions

Parameter	Typ	Units	Parameter	Typ	Units
Collector Voltage (+V_{cc})	3.2 to 4.1	Volts	Operating Temperature (PC Board)	-20 to +70	°C
Reference Voltage (V_{ref}) (Fixed and regulated)	+2.95 (±1.2%)	Volts			

Application Information

The CHP1230-PM is a three-stage amplifier that requires a single regulated positive supply along with the unregulated battery voltage for proper operation. Vref is a regulated 2.95 reference voltage for the bias control circuitry. It can also be used as a power down mode select. Vcc is an unregulated supply voltage directly from the battery. Vcc should be applied prior to Vref and before RF input power. The CHP1230-PM can be operated over a range of supply voltages and bias points by adjustment of Vref. It is important that the maximum power dissipation of the package be observed at all times and that the maximum voltage across the device is not exceeded.

Circuit Design Considerations

Biasing The positive Vcc supply voltages are applied to pins 3 and 4. Most bypass decoupling is provided on-board. Vref is applied to pin 1.

The recommended DC bypass capacitance is shown in the schematic diagram on Page 5.

Inadequate bypass capacitance and inductance around the DC supply lines can compromise the adjacent channel power ratio (ACPR), reduce power gain and/or create oscillations.

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Electrical Characteristics

Unless otherwise specified, the following specifications are guaranteed at room temperature with collector voltage ($+V_{cc}$) = 3.5 V.

Parameter	Condition	Min	Typ	Max	Units
Frequency Range		1.75		1.78	GHz
Gain	Pout = +28.5 dBm	29	31	33	dB
Gain Delta	Pout = +28.5 to +12.0 dBm		3.0		dB
Gain Ripple*	1750-1780 MHz		1.0	1.5	dB
Gain Variation	Over supply voltage		2		dB/V
	Over temperature		0.03		dB/°C
Power Output	CDMA mode		+28.5		dBm
Harmonics	2nd @ Digital power output, no output trapping, Po = +28.5 dBm		-30		dBc
	3rd @ Digital power output, no output trapping, Po = +28.5 dBm		-30		dBc
Noise Power in Receive Band				-90	dBm
Linearity (ACPR)	CDMA mode @ +28.5 dBm Pout, 1.25 MHz offset		-53	-48	dBc/30KHz
	CDMA mode @ +12.0 dBm Pout, 1.25 MHz offset		-56	-50	dBc/30KHz
	CDMA mode @ +28.5 dBm Pout, 1.98 MHz offset		-58	-56	dBc/30KHz
	CDMA2K 1X mode** @ +28.5 dBm Pout, 1.25 MHz offset		-48	-47	dBc/30KHz
	CDMA2K 1X mode** @ +12.0 dBm Pout, 1.25 MHz offset		-55	-48	dBc/30KHz
	CDMA2K 1X mode** @ +28.5 dBm Pout, 1.98 MHz offset		-63	-58	dBc/30KHz
Ruggedness	6:1 VSWR @ 28.5 dBm Pout	–	no damage	–	–
Noise Figure			5.0	6.0	dB
Input Return Loss			-10		dB
I_{cc} (V_{cc} = 3.5V)	Pout = +12.0 dBm - CDMA mode		100	110	mA
	Pout = +28.5 dBm - CDMA mode		565	590	mA
Quiescent Current (I_q)	No RF		55		mA
V_{ref} Supply Current (I_{ref})			3.0	5.0	mA
V_{ref} Supply Voltage (V_{ref})	Fixed and regulated (1.2% tolerance)		2.95		V
Leakage Current	V_{ref} = 0 V, V_{cc} = 3.5 V			10	µA

* Specifications guaranteed over the temperature range of -20°C to +70°C. ** Modulation HPSK in 1.2288 MHz, RC3 PAR = 4.7 @ 1% CCDF.

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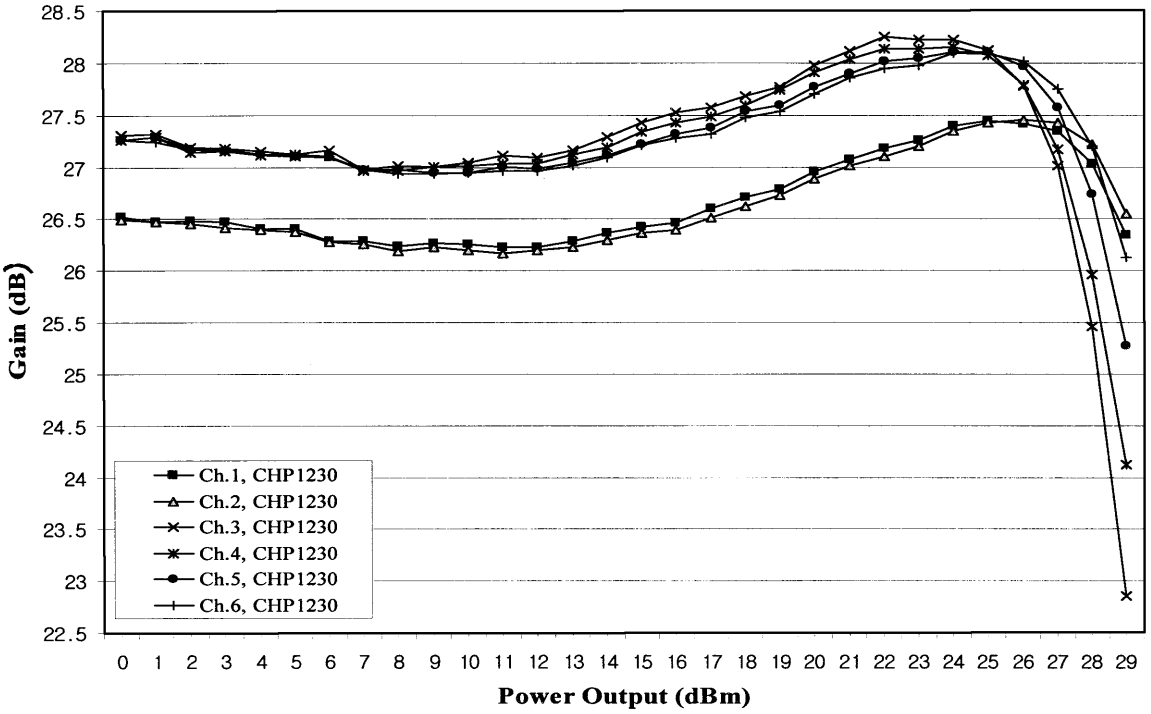
Modulation When biased as specified, the CHP1230-PM will achieve the required adjacent channel response for the digital system specified. Celeritek tests 100% of each product under digital modulation to ensure correlation to customer applications.

Thermal

1. The ground pad on the backside of the CHP1230-PM must be soldered to the ground plane.
2. All leads of the package must be soldered to the appropriate electrical connection.

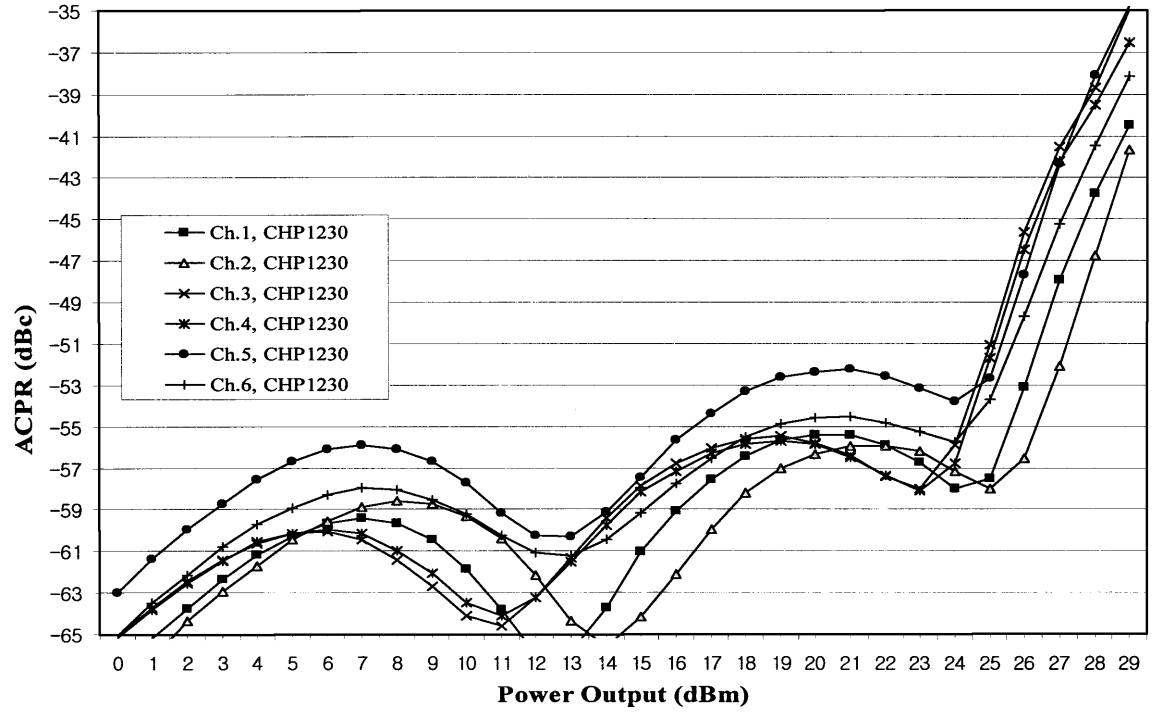
Typical Performance

Gain vs Power Output



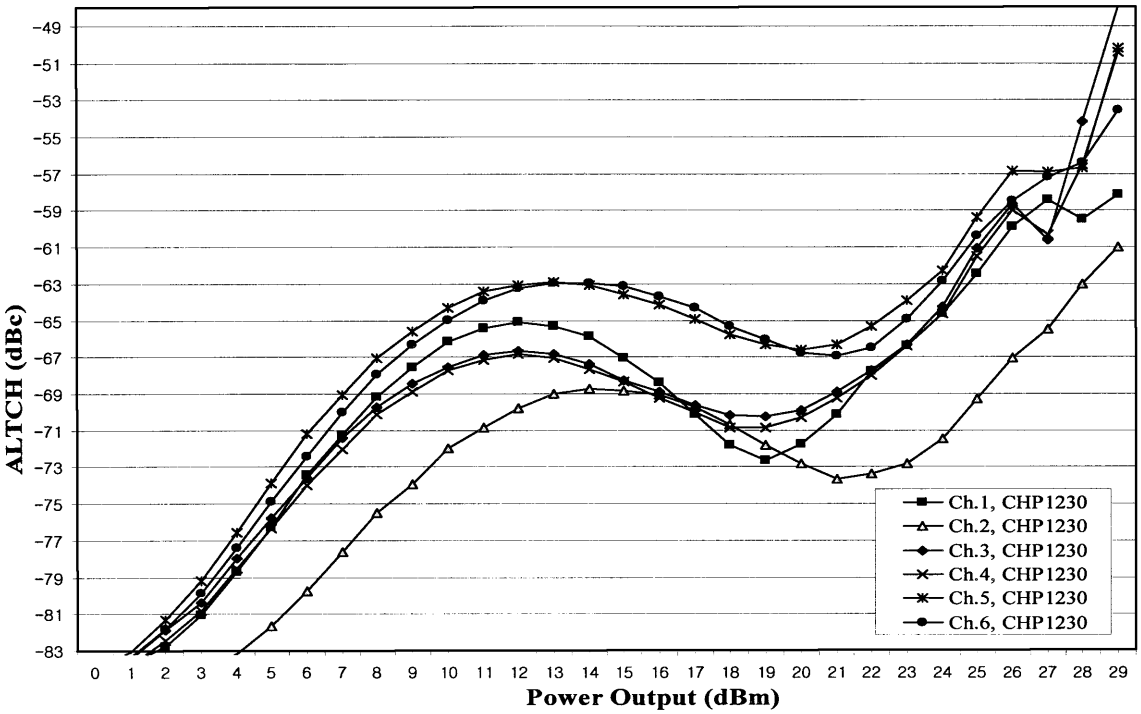
ACPR vs Power Output

(CDMA2K 1X Mode @ 1.25 MHz Offset)

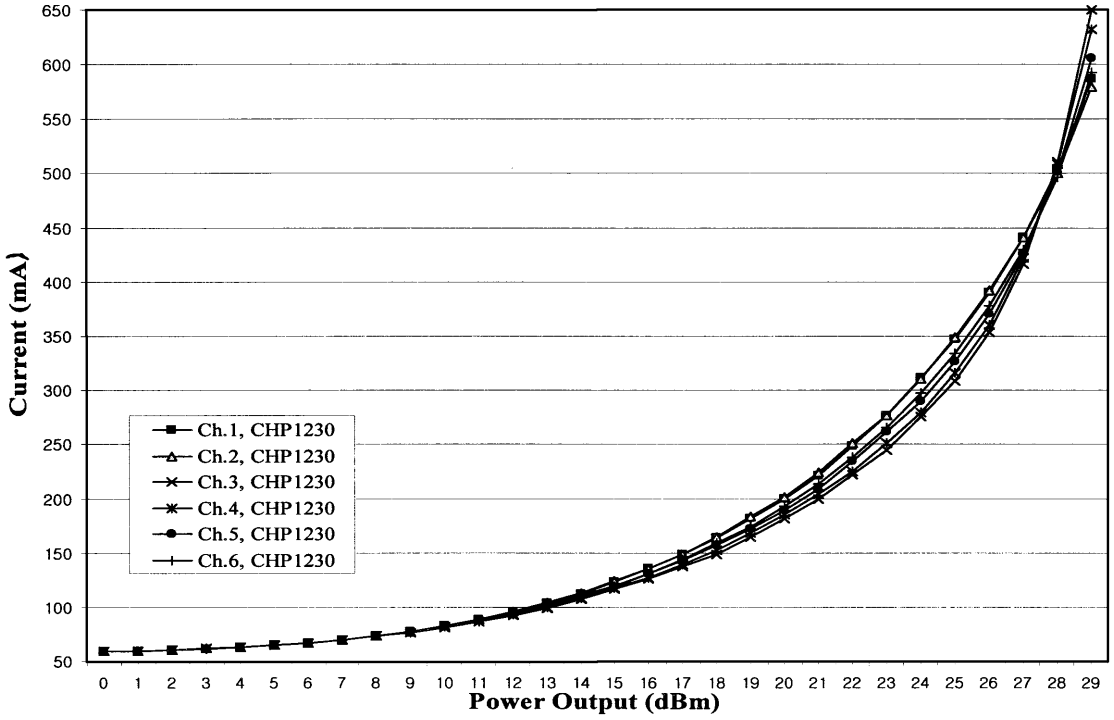


Typical Performance

ALTCH Power vs Power Output
(CDMA2K 1X Mode @ 1.98 MHz Offset)



Current vs Power Output



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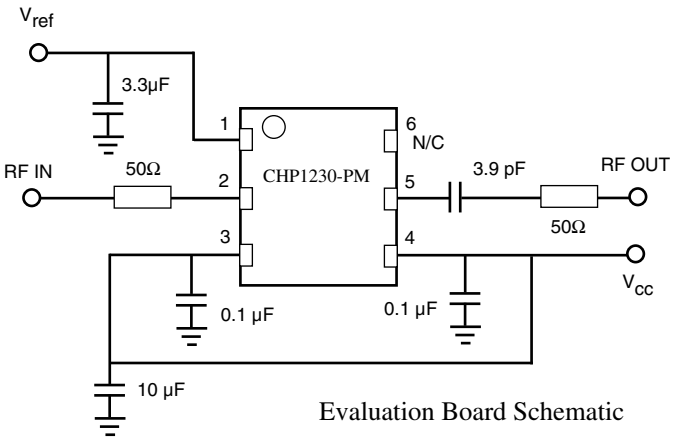
Recommended Application Circuit

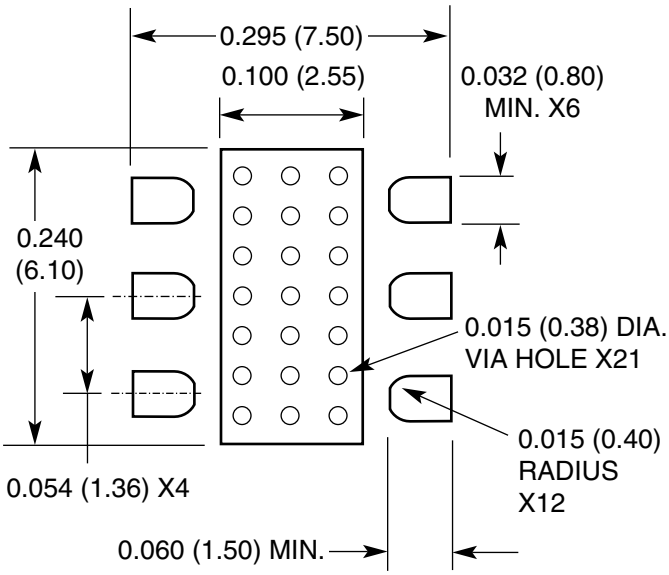
Note: This schematic represents the topology of the application circuit recommended by Celeritek.



Evaluation Board Schematic

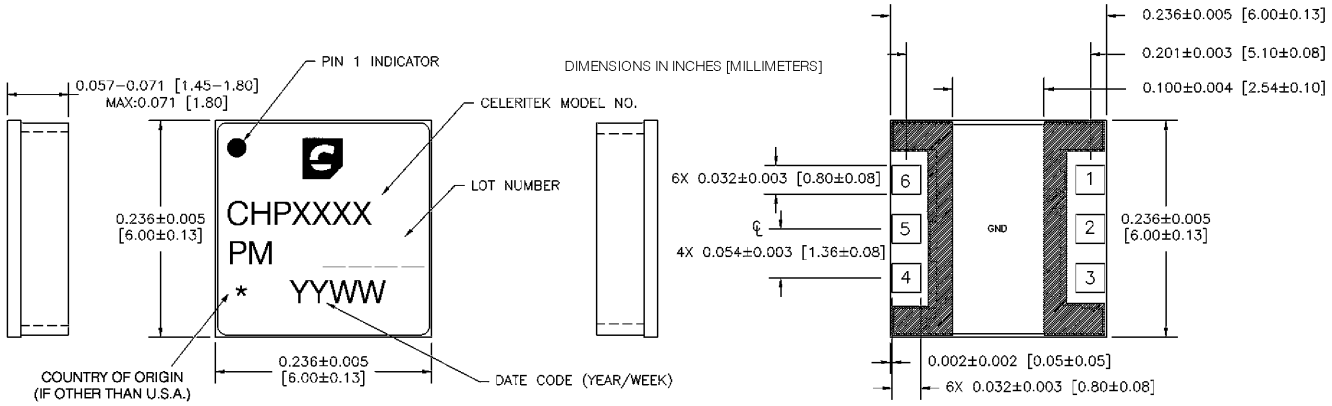
Board substrate:
ER = 4.60
Thickness = 0.031 in.

PCB Footprint (Minimum Pad Dimensions)



DIMENSIONS IN INCHES (mm)
DRAWING NOT TO SCALE

Physical Dimensions



Ordering Information

The CHP1230-PM is available in a surface mount 50 ohm matched module and devices are available in tube or tape and reel.

Part Number for Ordering	Package
CHP1230-PM-0000	**PM6 surface mount power package in tube**
CHP1230-PM-000T	**PM6 surface mount power package in tape and reel**
PB-CHP1230-PM	**Evaluation Board with SMA connectors for CHP1230-PM**

Notes

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