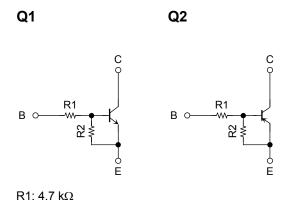
TOSHIBA Transistor Silicon NPN/PNP Epitaxial Type (PCT Process) (Transistor with Built-in Bias Resistor)

RN4981AFS

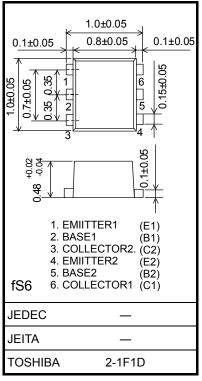
Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

- Two devices are incorporated into a fine-pitch, small-mold (6-pin) package.
- Incorporating a bias resistor into the transistor reduces the number of parts, so enabling the manufacture of ever more compact equipment and lowering assembly cost.

Equivalent Circuit and Bias Resistor Values



Unit: mm

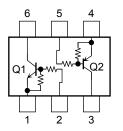


Weight: 0.001 g (typ.)

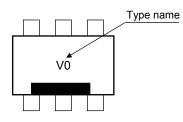
R2: 4.7 kΩ

(Q1, Q2 common)

Equivalent Circuit (top view)



Marking



Absolute Maximum Ratings (Ta = 25°C) (Q1)

| Characteristic | Symbol | Rating | Unit |
|---------------------------|------------------|--------|------|
| Collector-base voltage | V _{CBO} | 50 | V |
| Collector-emitter voltage | V _{CEO} | 50 | V |
| Emitter-base voltage | V _{EBO} | 10 | V |
| Collector current | Ι _C | 80 | mA |

Absolute Maximum Ratings (Ta = 25°C) (Q2)

| Characteristic | Symbol | Rating | Unit |
|---------------------------|------------------|--------|------|
| Collector-base voltage | V _{CBO} | -50 | V |
| Collector-emitter voltage | V _{CEO} | -50 | V |
| Emitter-base voltage | V _{EBO} | -10 | V |
| Collector current | ΙC | -80 | mA |

Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

| Characteristic | Symbol | Rating | Unit |
|-----------------------------|-------------------------|---------|------|
| Collector power dissipation | P _C (Note 1) | 50 | mW |
| Junction temperature | Tj | 150 | °C |
| Storage temperature range | T _{stg} | -55~150 | °C |

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Total rating

Electrical Characteristics (Ta = 25°C) (Q1)

| Characteristic | Symbol | Test Condition | Min | Тур. | Мах | Unit |
|--------------------------------------|-----------------------|--|------|------|------|------|
| Collector cutoff current | I _{CBO} | $V_{CB} = 50 \text{ V}, I_E = 0$ | | | 100 | nA |
| | ICEO | $V_{CE} = 50 \text{ V}, \text{ I}_{B} = 0$ | _ | _ | 500 | ПА |
| Emitter cutoff current | I _{EBO} | $V_{EB} = 10 \text{ V}, \text{ I}_{C} = 0$ | 0.89 | _ | 1.33 | mA |
| DC current gain | h _{FE} | $V_{CE} = 5 \text{ V}, \text{ I}_{C} = 10 \text{ mA}$ | 30 | _ | _ | |
| Collector-emitter saturation voltage | V _{CE (sat)} | $I_{C} = 5 \text{ mA}, I_{B} = 0.5 \text{ mA}$ | _ | _ | 0.15 | V |
| Input voltage (ON) | V _{I (ON)} | $V_{CE} = 0.2 \text{ V}, \text{ I}_{C} = 5 \text{ mA}$ | 1.2 | _ | 2.2 | V |
| Input voltage (OFF) | VI (OFF) | $V_{CE} = 5 \text{ V}, \text{ I}_{C} = 0.1 \text{ mA}$ | 0.8 | _ | 1.5 | V |
| Collector output capacitance | C _{ob} | $V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$ | | 0.7 | | pF |

Electrical Characteristics (Ta =25°C) (Q2)

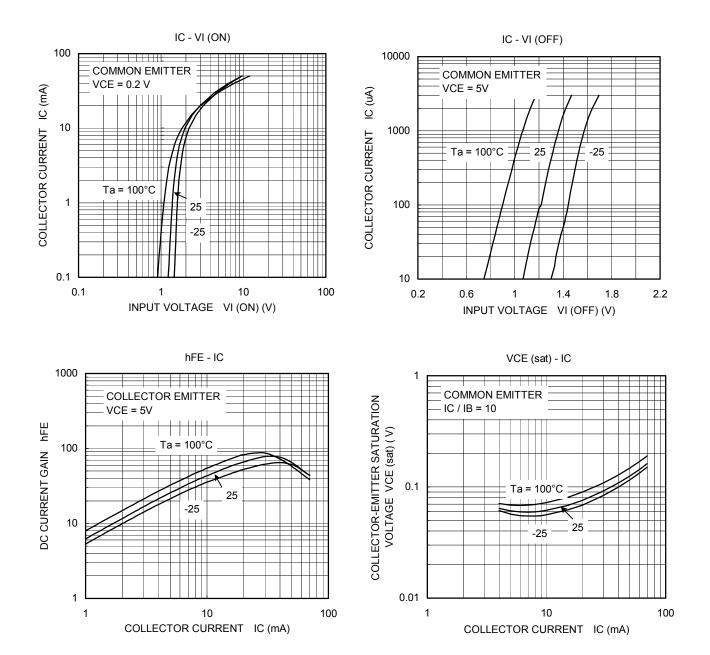
| Characteristic | Symbol | Test Condition | Min | Тур. | Мах | Unit |
|--------------------------------------|-----------------------|--|-------|------|-------|------|
| Collector cutoff current | I _{CBO} | $V_{CB}=-50~V,~I_{E}=0$ | | _ | -100 | nA |
| | ICEO | $V_{CE} = -50 \text{ V}, \text{ I}_B = 0$ | _ | _ | -500 | 114 |
| Emitter cutoff current | I _{EBO} | $V_{EB} = -10 \text{ V}, I_C = 0$ | -0.89 | _ | -1.33 | mA |
| DC current gain | h _{FE} | $V_{CE} = -5 \text{ V}, \text{ I}_{C} = -10 \text{ mA}$ | 30 | _ | _ | |
| Collector-emitter saturation voltage | V _{CE (sat)} | $I_{C} = -5 \text{ mA}, I_{B} = -0.5 \text{ mA}$ | _ | _ | -0.15 | V |
| Input voltage (ON) | V _{I (ON)} | $V_{CE} = -0.2 \text{ V}, I_C = -5 \text{ mA}$ | -1.2 | _ | -2.2 | V |
| Input voltage (OFF) | VI (OFF) | $V_{CE} = -5 \text{ V}, \text{ I}_{C} = -0.1 \text{ mA}$ | -0.8 | _ | -1.5 | V |
| Collector output capacitance | C _{ob} | $V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$ | _ | 0.9 | _ | pF |

Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)

| Characteristic | Symbol | Test Condition | Min | Тур. | Max | Unit |
|----------------|--------|----------------|------|------|------|------|
| Input resistor | R1 | — | 3.76 | 4.7 | 5.64 | kΩ |
| Resistor ratio | R1/R2 | | 0.8 | 1.0 | 1.2 | |

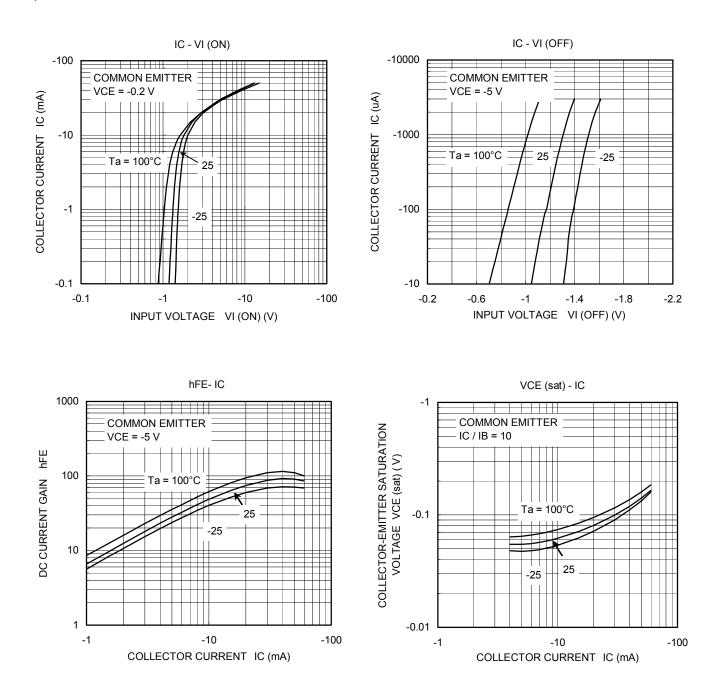
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Q1



TOSHIBA

Q2



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