

**General Description**

This Trench MOSFET has better characteristics, such as fast switching time, low on resistance, low gate charge and excellent avalanche characteristics. It is mainly suitable for DC/DC Converter, Synchronous Rectification and a load switch in battery powered applications

**FEATURES**

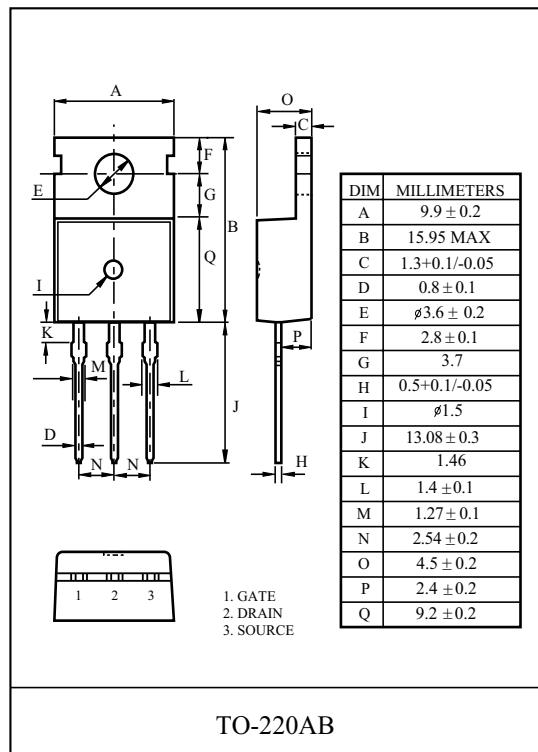
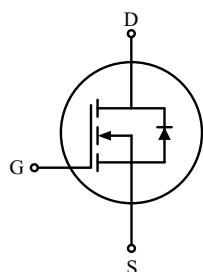
- $V_{DSS} = 60V$ ,  $I_D = 160A$
- Drain-Source ON Resistance :  
 $R_{DS(ON)} = 3.5m\Omega$  (Max.) @  $V_{GS} = 10V$

**MAXIMUM RATING (Tc=25 °C)**

CHARACTERISTIC	SYMBOL	RATING	UNIT
Drain-Source Voltage	$V_{DSS}$	60	V
Gate-Source Voltage	$V_{GSS}$	$\pm 20$	V
Drain Current	$I_D$	160*	A
		101	
	$I_{DP}$	480*	
Single Pulsed Avalanche Energy (Note 2)	$E_{AS}$	960	mJ
Repetitive Avalanche Energy (Note 1)	$E_{AR}$	12	mJ
Peak Diode Recovery dv/dt (Note 3)	dv/dt	4.5	V/ns
Drain Power Dissipation	$P_D$	167	W
		1.33	W/°C
Maximum Junction Temperature	$T_j$	150	
Storage Temperature Range	$T_{stg}$	-55 ~ 150	
<b>Thermal Characteristics</b>			
Thermal Resistance, Junction-to-Case	$R_{thJC}$	0.75	/W
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	62.5	/W

\* : Drain current limited by maximum junction temperature.

Calculated continuous Current based on maximum allowable junction temperature

**PIN CONNECTION**

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## ELECTRICAL CHARACTERISTICS (Tc=25 °C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
<b>Static</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> =250 μA, V <sub>GS</sub> =0V	60	-	-	V
Breakdown Voltage Temperature Coefficient	BV <sub>DSS</sub> / T <sub>j</sub>	I <sub>D</sub> =5mA, Referenced to 25	-	0.06	-	V/°C
Drain Cut-off Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V,	-	-	10	μA
Gate Threshold Voltage	V <sub>th</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250 μA	2.0	-	4.0	V
Gate Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	± 100	nA
Drain-Source ON Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =80A	-	2.9	3.5	mΩ
<b>Dynamic</b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =48V, I <sub>D</sub> =80A V <sub>GS</sub> =10V (Note 4,5)	-	200	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	35	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	70	-	
Turn-on Delay time	t <sub>d(on)</sub>	V <sub>DD</sub> =30V I <sub>D</sub> =80A R <sub>G</sub> =25 (Note 4,5)	-	110	-	ns
Turn-on Rise time	t <sub>r</sub>		-	150	-	
Turn-off Delay time	t <sub>d(off)</sub>		-	460	-	
Turn-off Fall time	t <sub>f</sub>		-	280	-	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1.0MHz	-	8400	-	pF
Output Capacitance	C <sub>oss</sub>		-	960	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	520	-	
<b>Source-Drain Diode Ratings</b>						
Continuous Source Current	I <sub>S</sub>	V <sub>GS</sub> <V <sub>th</sub>	-	-	150	A
Pulsed Source Current	I <sub>SP</sub>		-	-	600	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =150A, V <sub>GS</sub> =0V	-	-	1.4	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>S</sub> =80A, V <sub>GS</sub> =0V, dI <sub>S</sub> /dt=300A/μs	-	65	-	ns
Reverse Recovery Charge	Q <sub>rr</sub>		-	0.18	-	μC

Note 1) Repetitve rating : Pulse width limited by junction temperature.

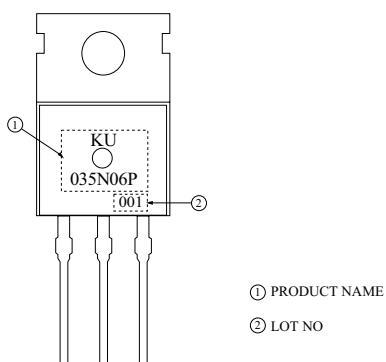
Note 2) L=100 μH, I<sub>S</sub>=80A, V<sub>DD</sub>=48V, R<sub>G</sub>=25 Ω, Starting T<sub>j</sub>=25 °C.

Note 3) I<sub>S</sub>=80A, dI<sub>S</sub>/dt=200A/μs, V<sub>DD</sub>=BV<sub>DSS</sub>, Starting T<sub>j</sub>=25 °C.

Note 4) Pulse Test : Pulse width = 300μs, Duty Cycle = 2%.

Note 5) Essentially independent of operating temperature.

## Marking



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Fig1.  $I_D$  -  $V_{DS}$

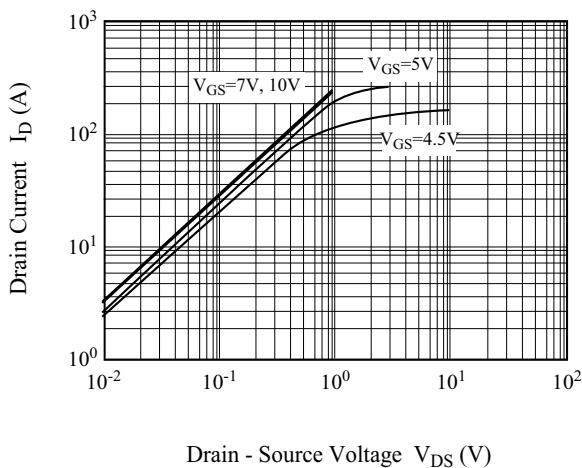


Fig2.  $I_D$  -  $V_{GS}$

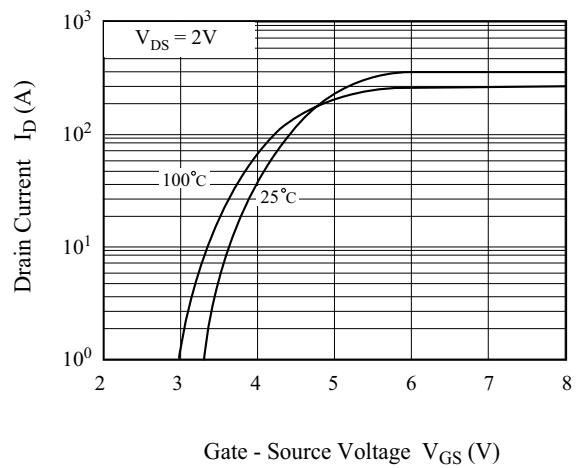


Fig3.  $BV_{DSS}$  -  $T_j$

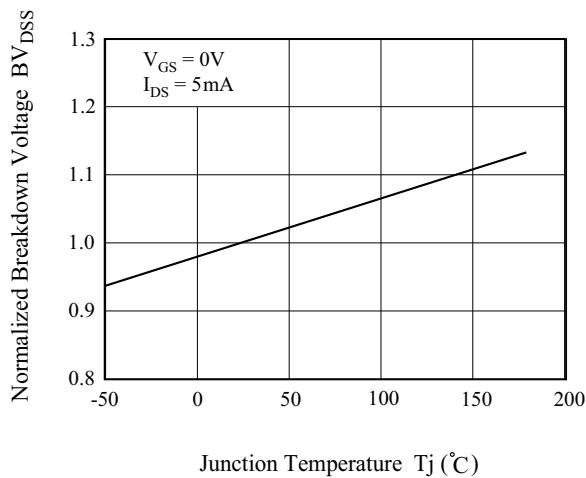


Fig4.  $R_{DS(ON)}$  -  $I_D$

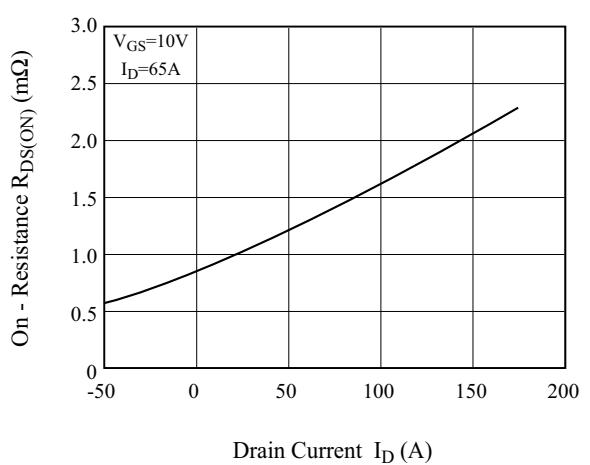


Fig5.  $I_S$  -  $V_{SD}$  - I

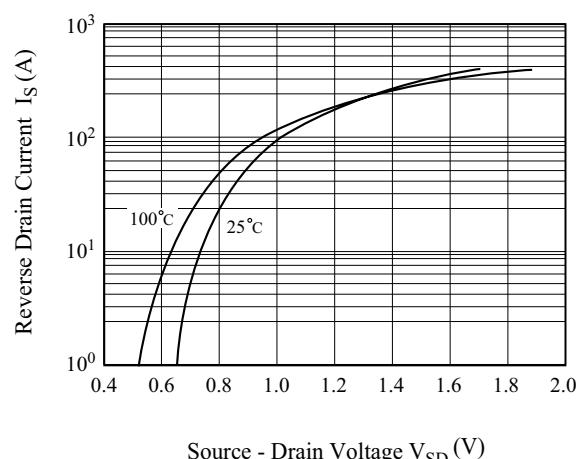
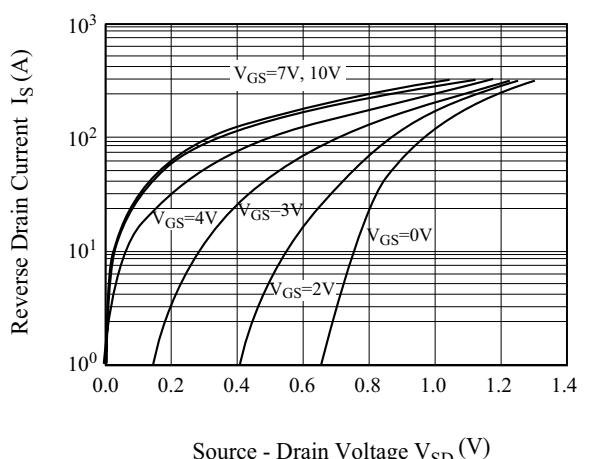


Fig6.  $I_S$  -  $V_{SD}$  - II



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Fig7.  $R_{DS(ON)}$  -  $I_D$

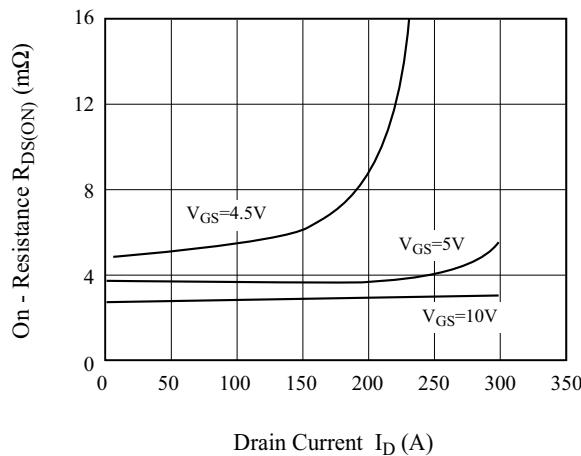


Fig8.  $I_D$ -  $T_j$

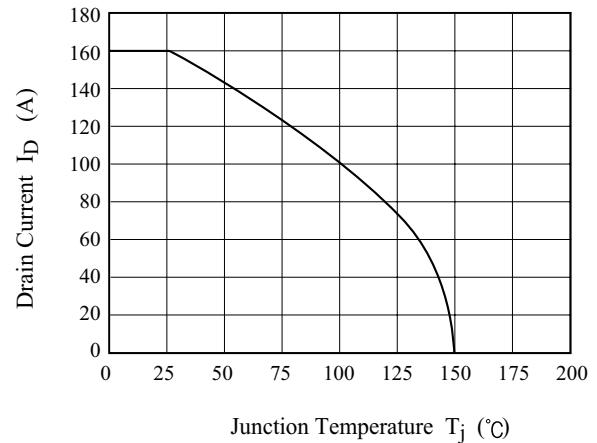


Fig 9.  $C$  -  $V_{DS}$

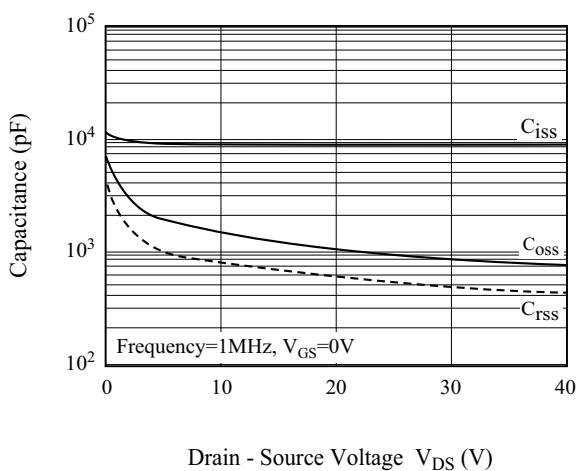


Fig10.  $Q_g$ -  $V_{GS}$

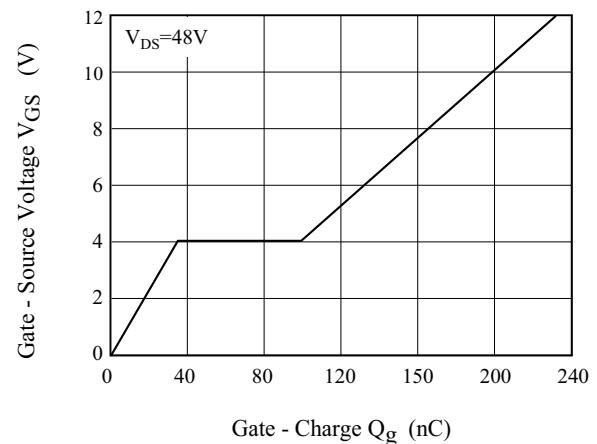


Fig11. Safe Operation Area

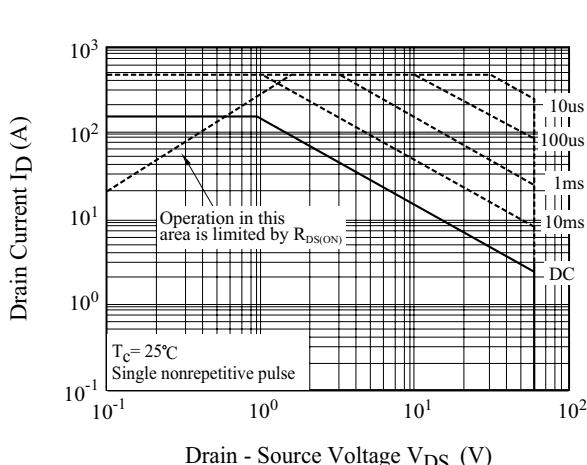
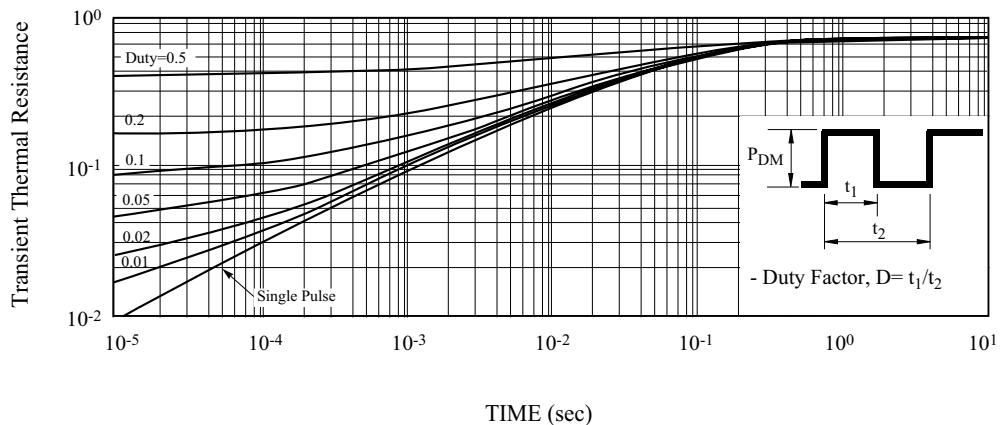


Fig12. Transient Thermal Response Curve



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Fig13. Gate Charge

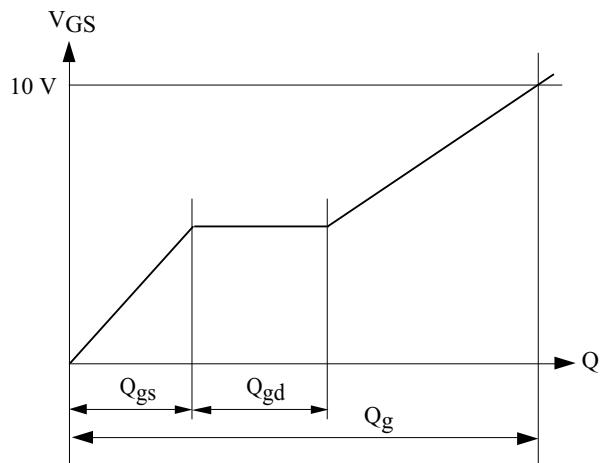
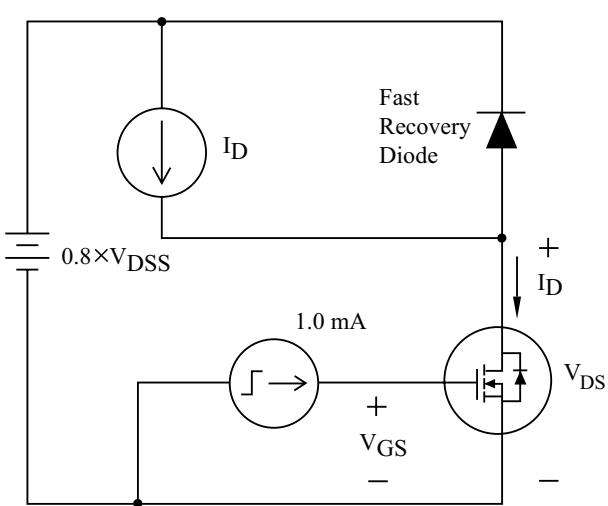
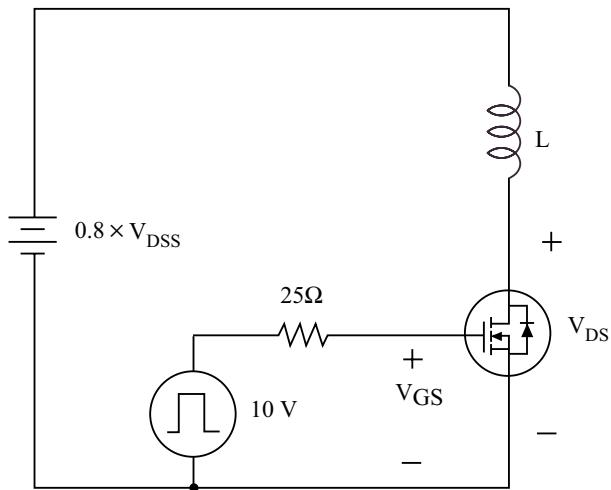


Fig14. Single Pulsed Avalanche Energy



$$E_{AS} = \frac{1}{2} L I_{AS}^2 \frac{BV_{DSS}}{BV_{DSS} - V_{DD}}$$

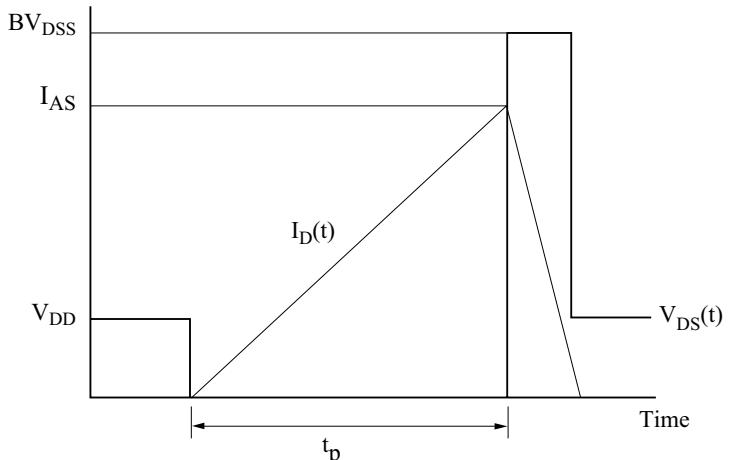
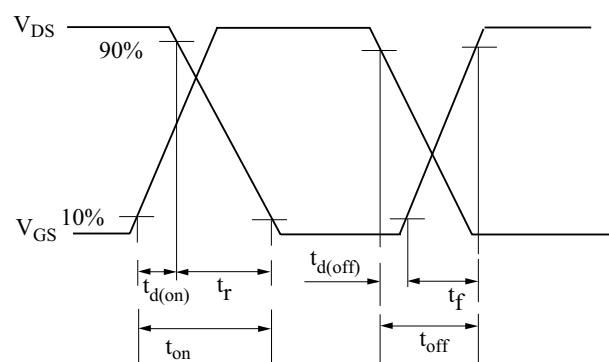
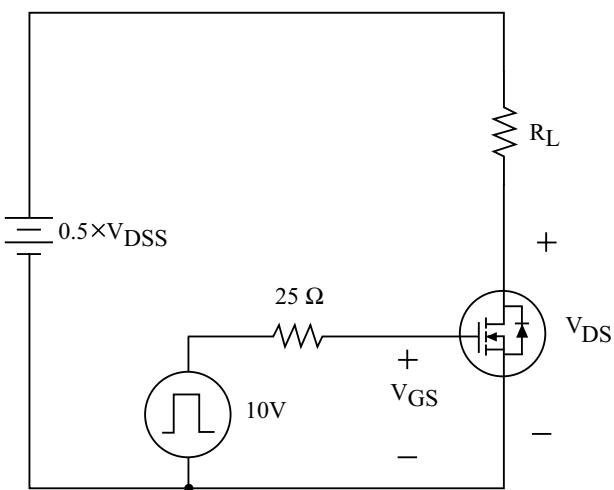


Fig15. Resistive Load Switching



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Fig16. Source - Drain Diode Reverse Recovery and dv /dt

